

# Decidability and Symbolic Verification

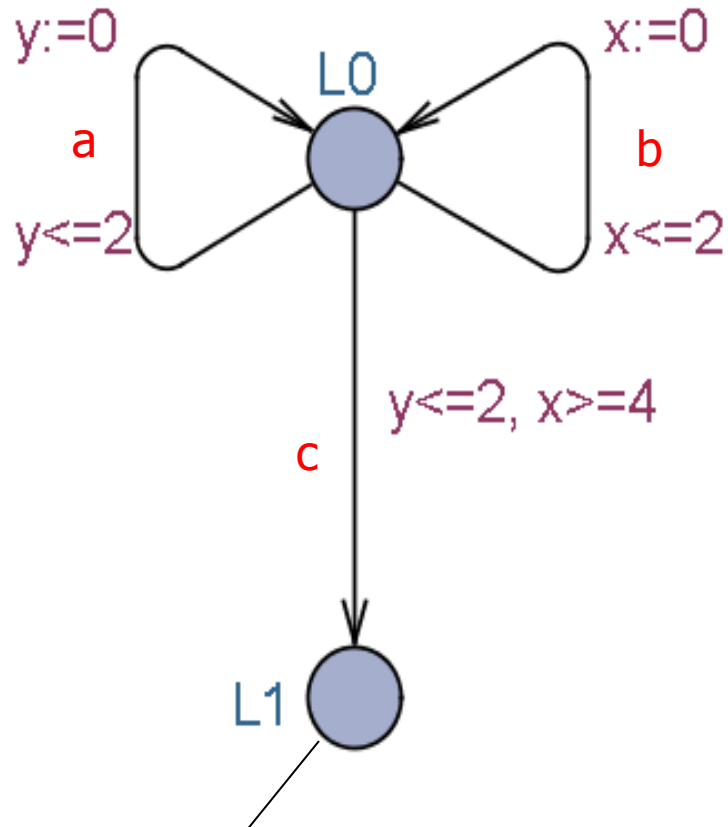
Kim G. Larsen  
Aalborg University, DENMARK



# Decidability



# Reachability ?



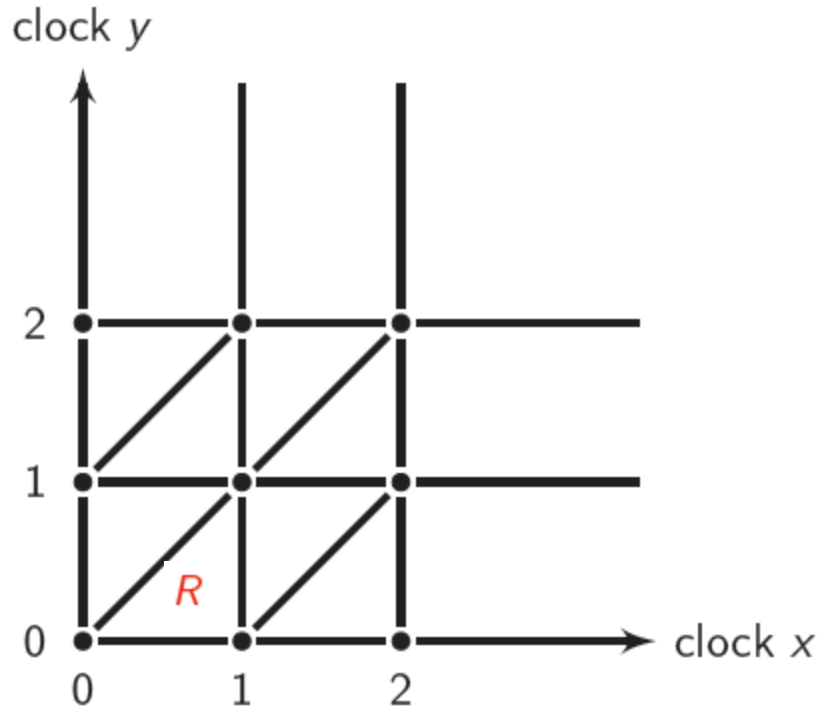
OBSTACLE:  
Uncountably infinite  
state space

$L \times \mathbb{R}^C$   
locations      clock-valuations

Reachable from initial state  $(L0, x=0, y=0)$  ?



# The Region Abstraction



region  $R$  defined by:

$$\begin{cases} 0 < x < 1 \\ 0 < y < 1 \\ y < x \end{cases}$$

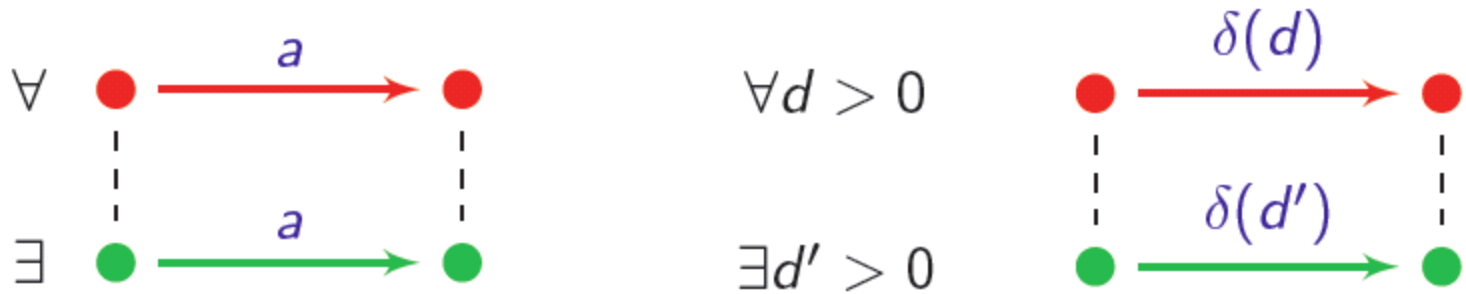
- “compatibility” between regions and constraints
- “compatibility” between regions and time elapsing

↪ an equivalence of finite index  
a time-abstract bisimulation



# Time Abstracted Bisimulation

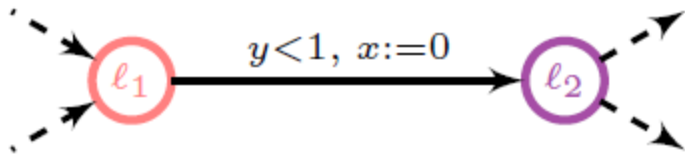
This is a relation between  $\bullet$  and  $\bullet$  such that:



... and vice-versa (swap  $\bullet$  and  $\bullet$ ).



# Regions – From Infinite to Finite



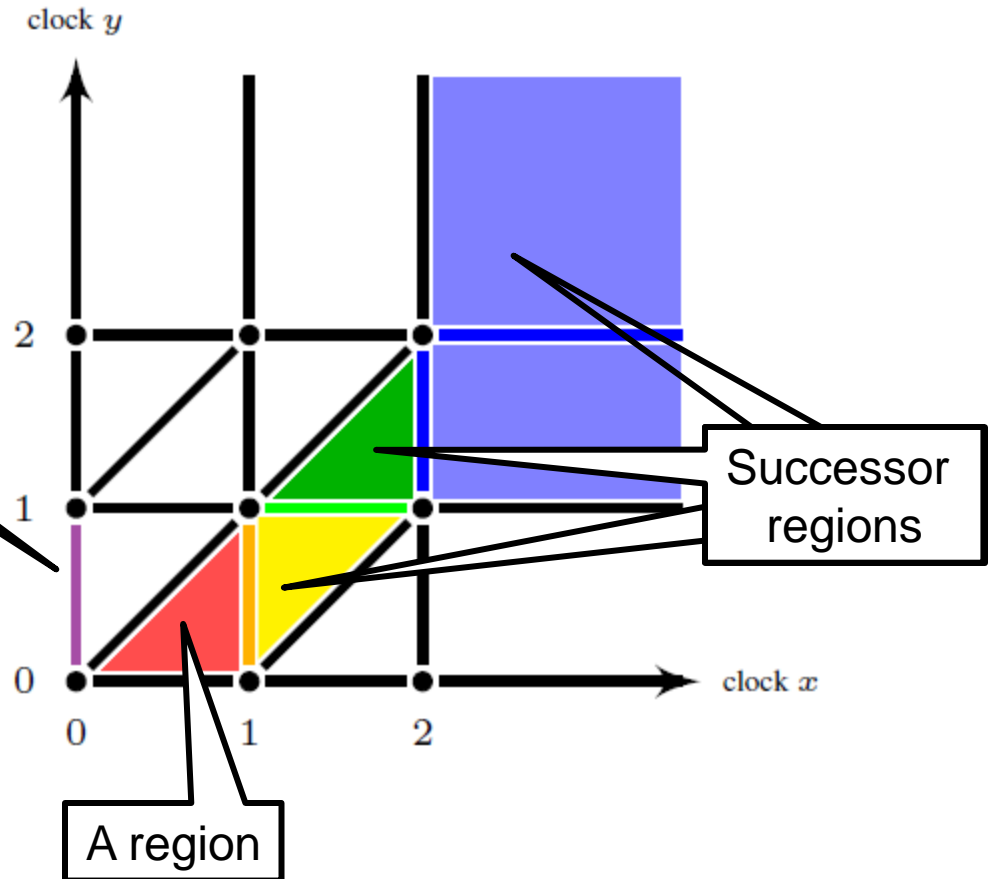
Reset  
region

**THM [AD90]**

Reachability is decidable  
(and PSPACE-complete) for  
timed automata

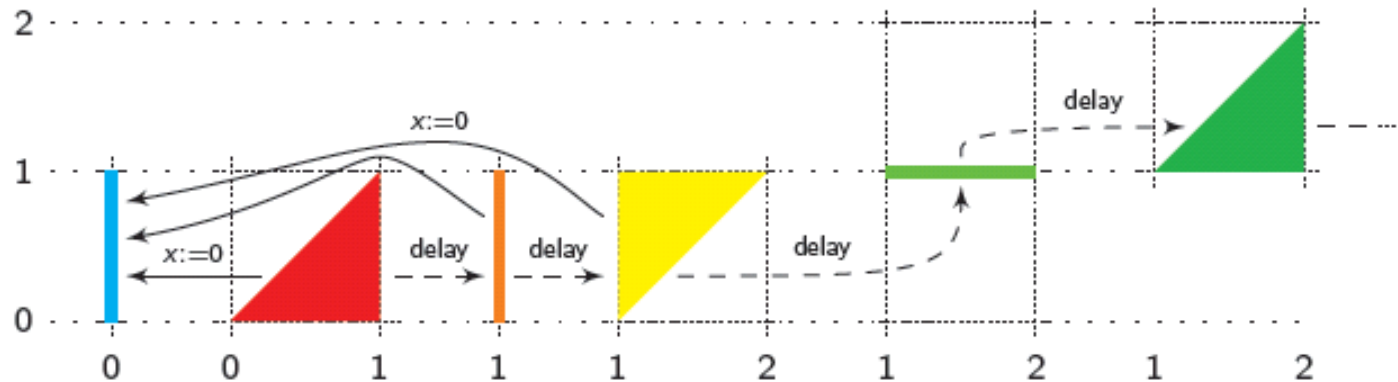
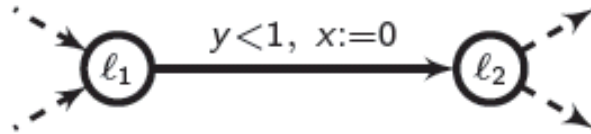
**THM [CY90]**

Time-optimal reachability is decidable  
(and PSPACE-complete) for  
timed automata

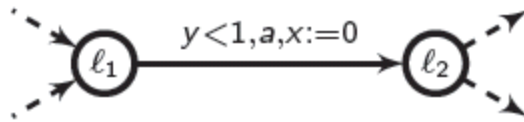


# Region Graph

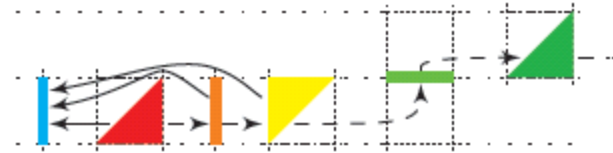
It “mimicks” the behaviours of the clocks.



# Region Automaton = Finite Bisimulation Quotient

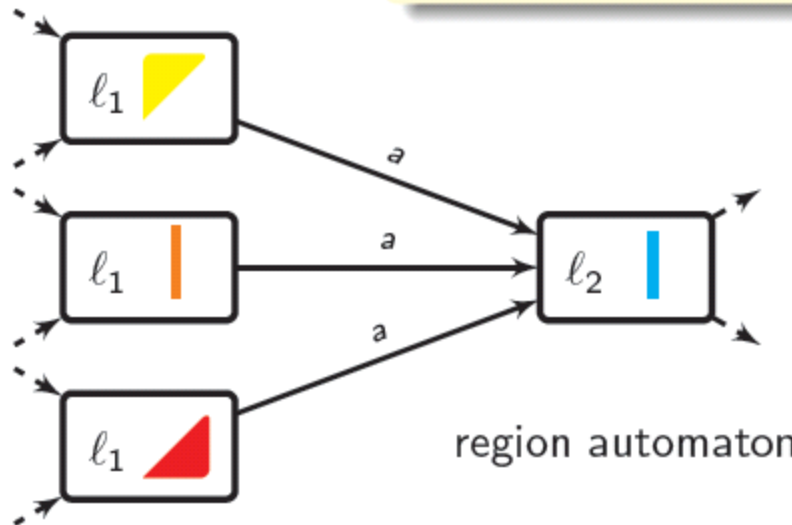


timed automaton



region graph

$$\mathcal{L}(\text{reg. aut.}) = \text{UNTIME}(\mathcal{L}(\text{timed aut.}))$$



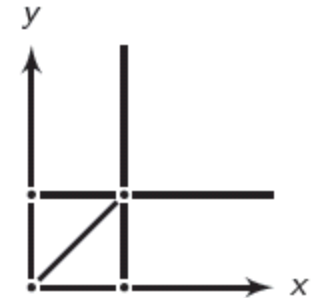
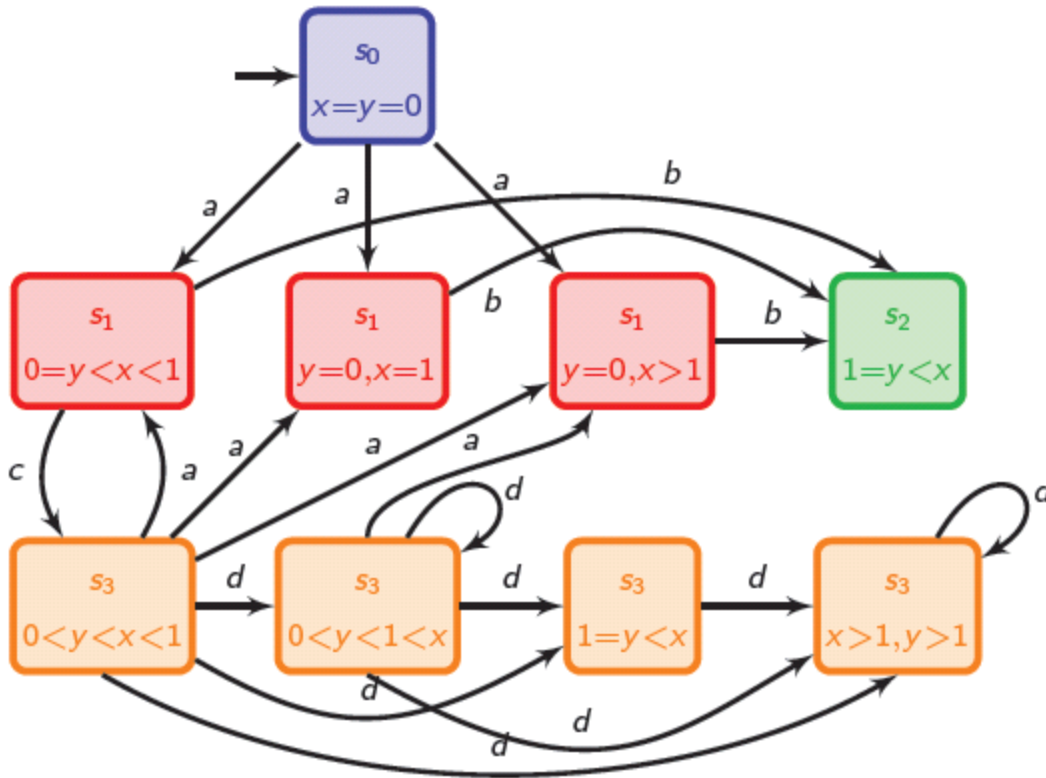
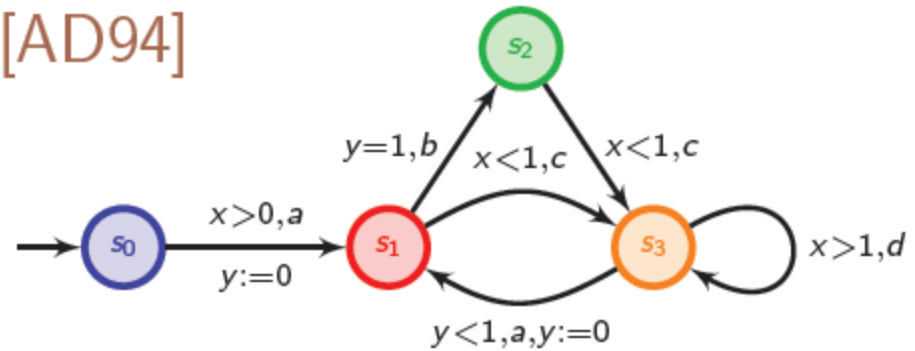
region automaton



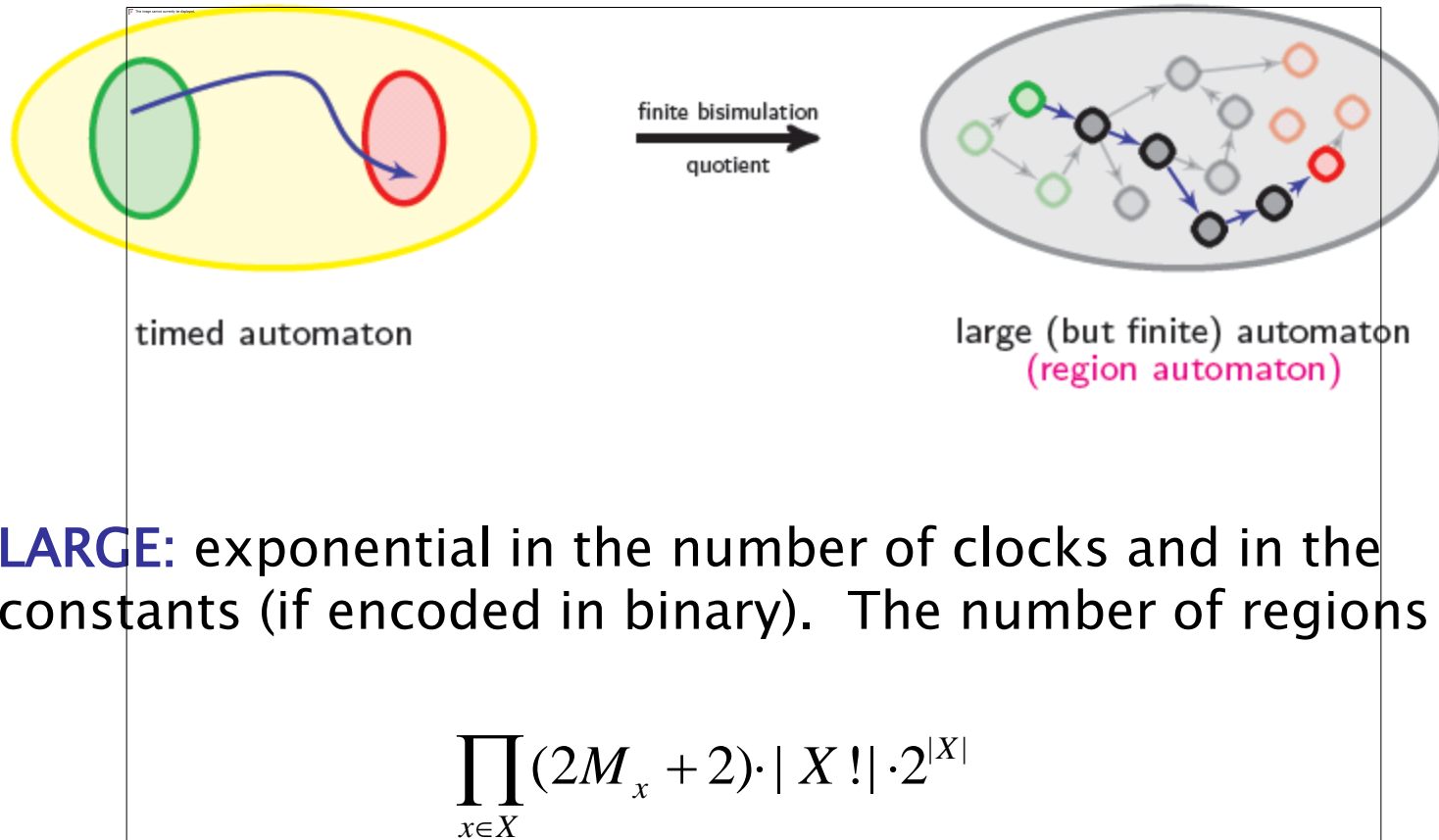


# An Example

[AD94]



# Region Automaton



# Fundamental Results

- Reachability ☺ PSPACE-c
- Model-checking
  - TCTL ☺ PSPACE-c ; MTL ☹ UNDECIDABLE ; MITL ☺ EXPSPACE-c
- Bisimulation, Simulation
  - Timed ☺ EXPTIME-c ; Untimed ☺ EXPSPACE-c
- Trace-inclusion
  - Timed ☹ UNDECIDABLE ; Untimed ☺ EXPSPACE-c



# Symbolic Verification

## The UPPAAL Verification Engine



# THE "secret" of UPPAAL

The screenshot displays the UPPAAL simulator interface. The main window shows a simulation trace and a list of constraints. A black box highlights the following constraints:

- Train(4).x  $\in$  [23,60]
- Train(5).x  $\in$  [30,65]
- Train(0).x - time  $\leq$  -50
- Train(0).x - Train(1).x  $\in$  [10,20]
- Train(0).x - Train(2).x  $\in$  [0,5]
- Train(3).x - Train(0).x  $\in$  [17,40]
- Train(4).x - Train(0).x  $\in$  [10,35]
- Train(2).x - Train(1).x  $\in$  [7,20]

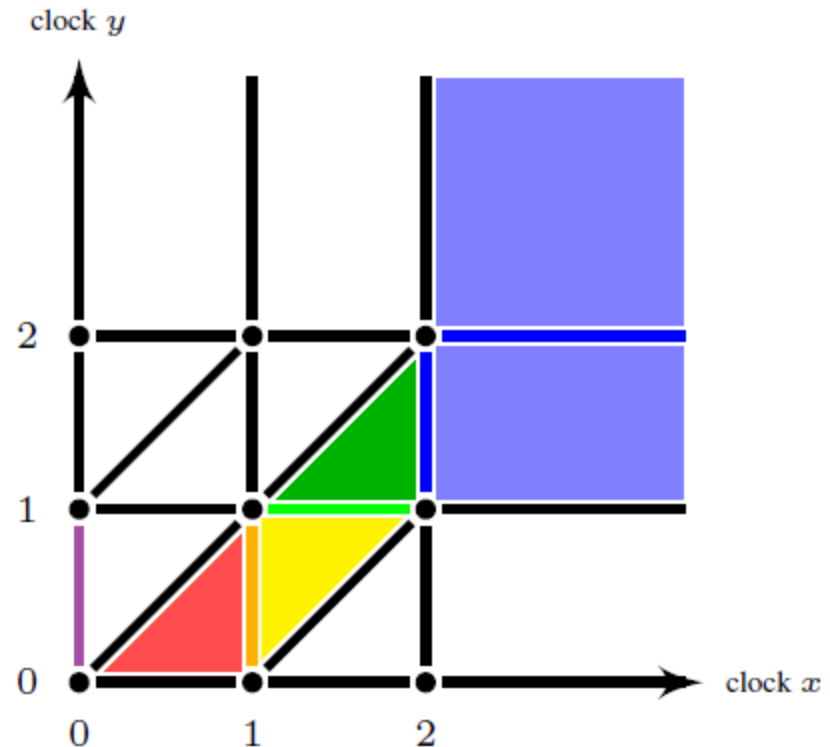
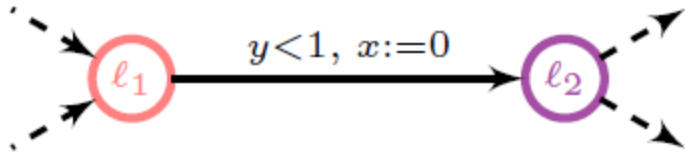
The simulation trace shows the following sequence of events:

```
Train(1)
(Safe, Cross, Stop, Stop, Stop, Stop, Occ)
leave[1]: Train(1) → Gate[1]
(Safe, Safe, Stop, Stop, Stop, Stop, Free)
go[front()]: Gate → Train(5)
(Safe, Safe, Stop, Stop, Stop, Start, Occ)
appr[0]: Train(0) → Gate[0]
```

The interface also includes a menu bar (File, Edit, View, Tools, Options, Help), a toolbar, and a status bar. The main window is divided into several panes: a list of enabled transitions, a simulation trace, a trace file, and a diagram of the system components (Train(0), Train(1), Train(2), Train(3), Train(4), Train(5), Gate, Cross, Stop, Safe, Occ, Free).



# Regions - From Infinite to Finite



## Theorem

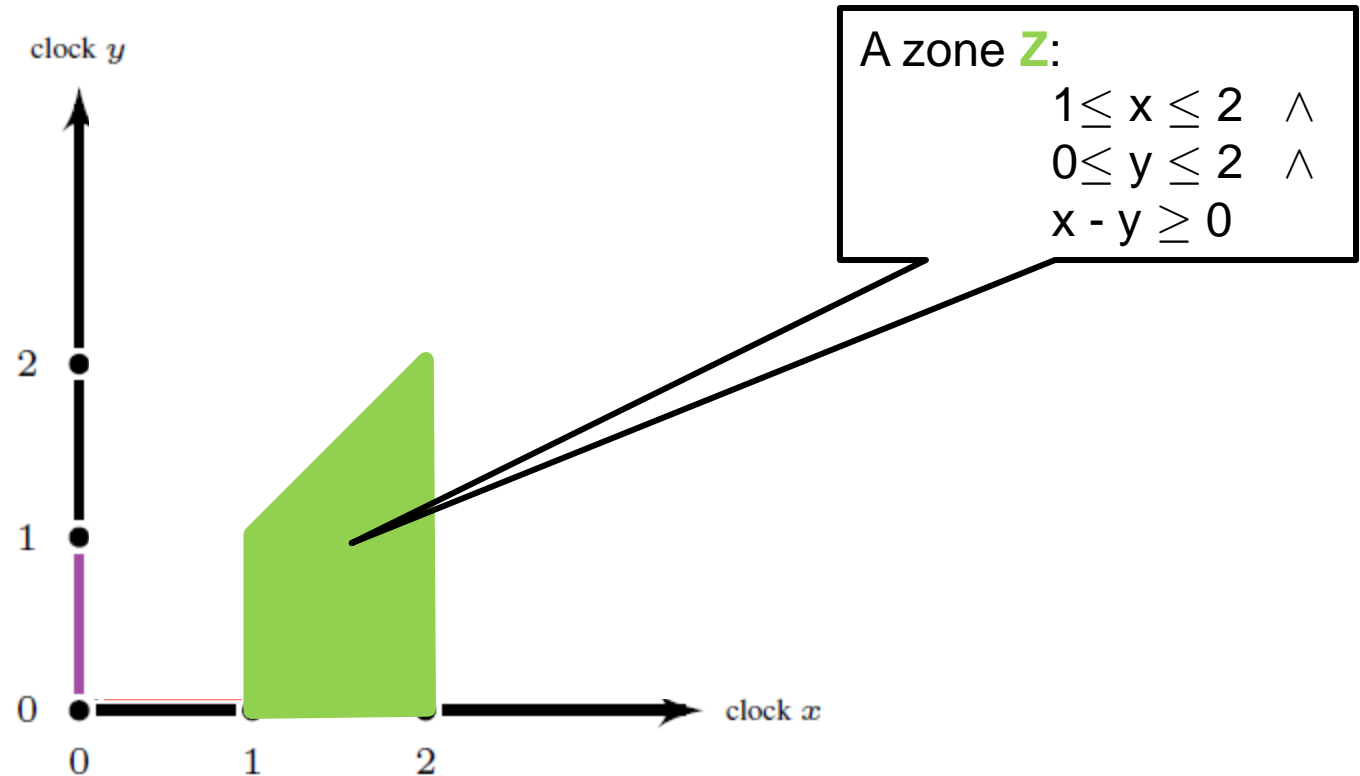
The number of regions is  $n! \cdot 2^n \cdot \prod_{x \in C} (2c_x + 2)$ .

Region construction: [AD94]

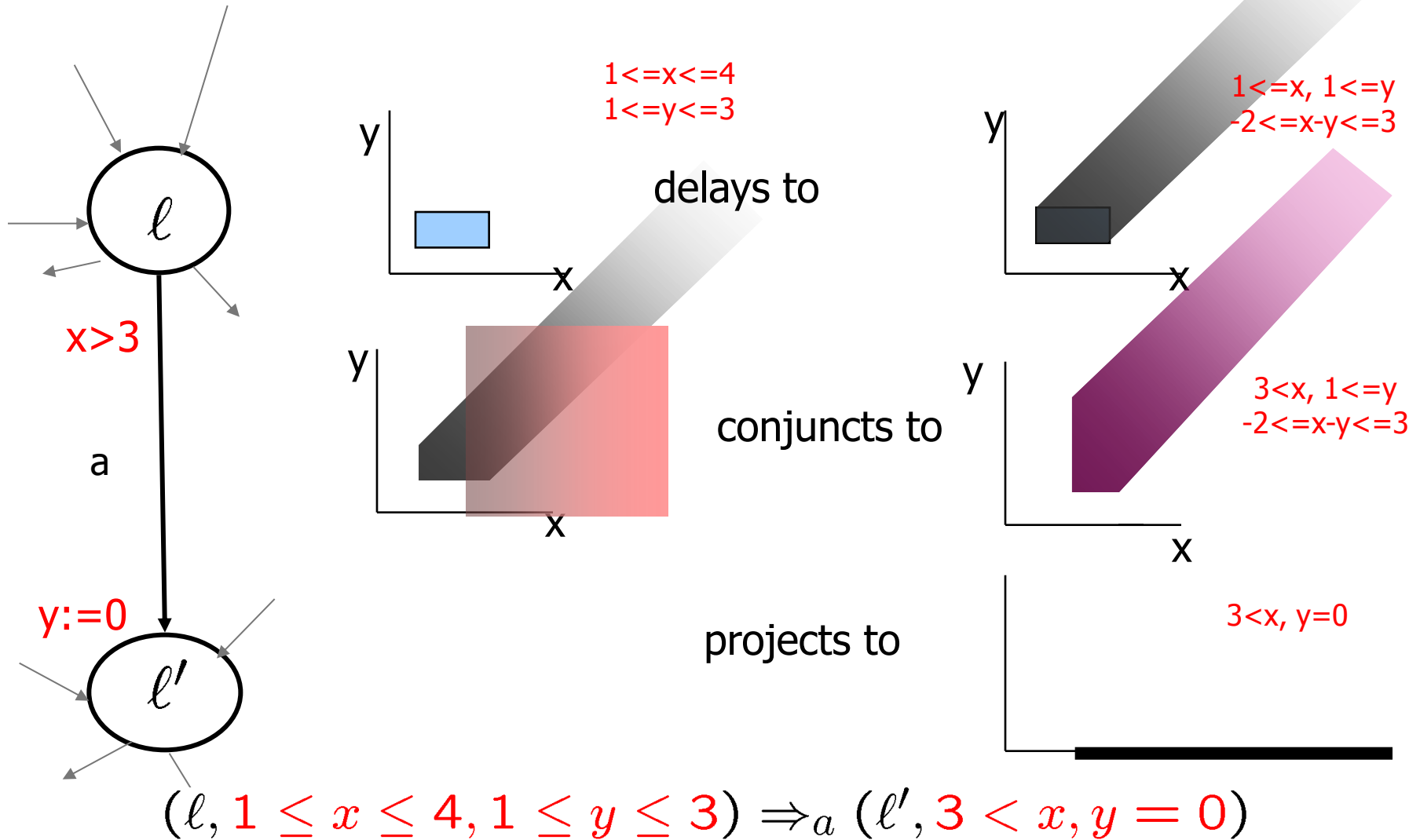
In practice: Zones



# Zones – From Finite to Efficiency

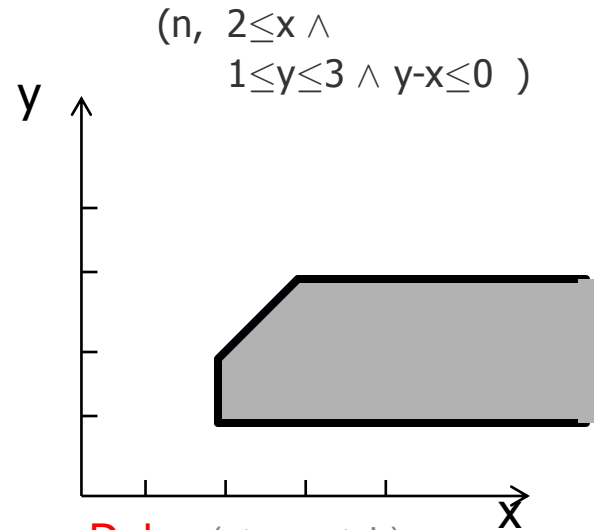
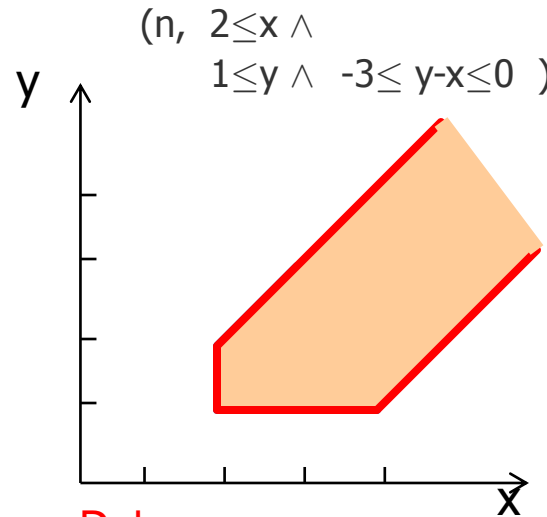
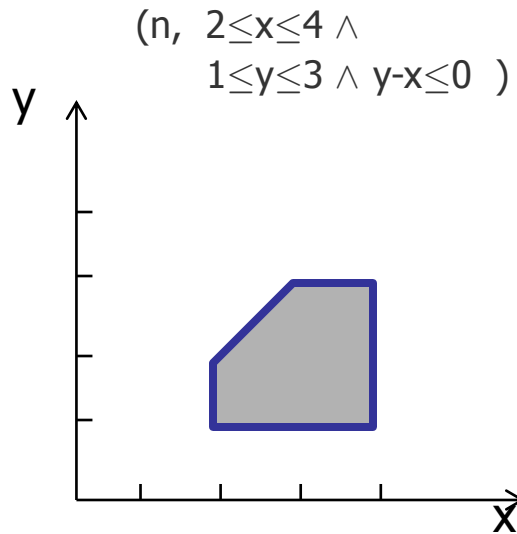


# Symbolic Transitions



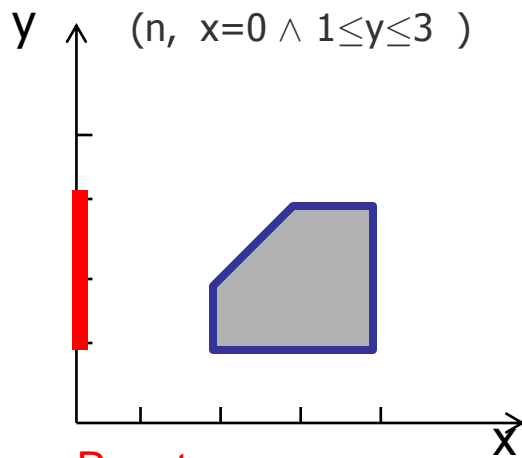


# Zones – Operations

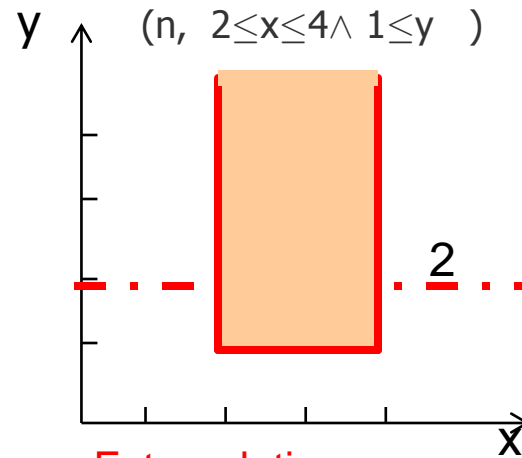


Delay

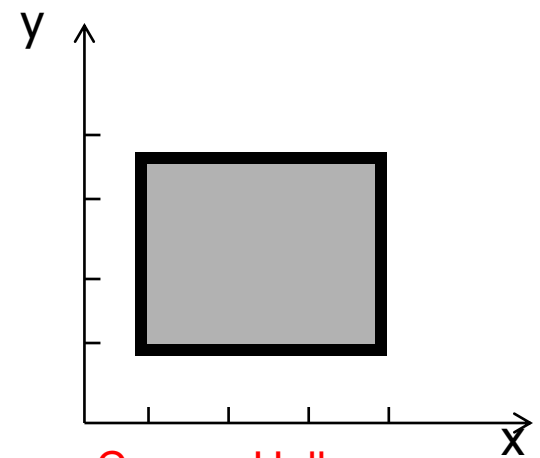
Delay (stopwatch)



Reset



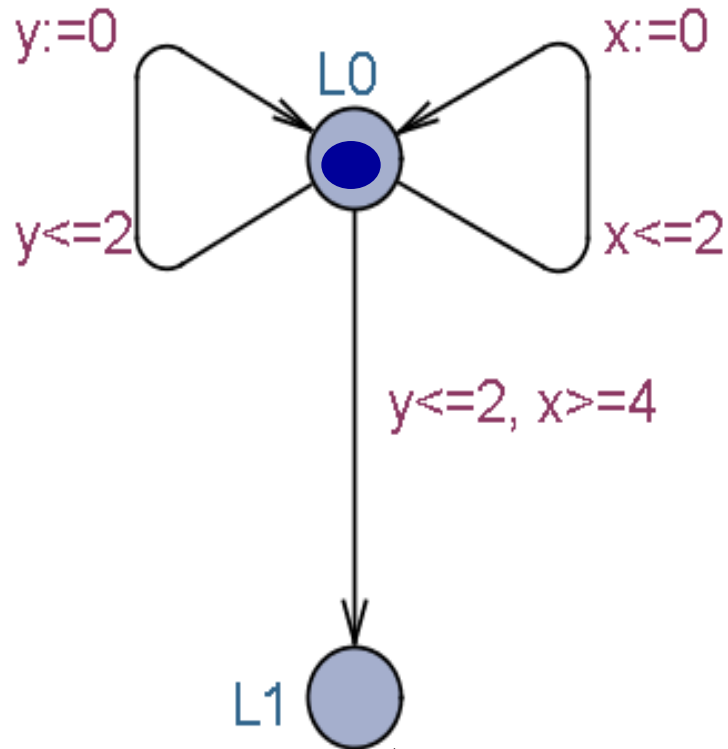
Extrapolation



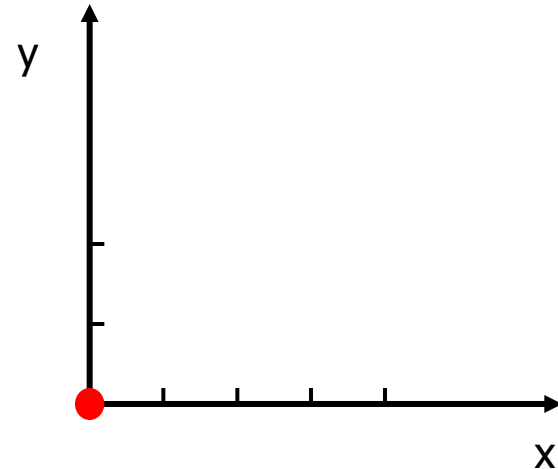
Convex Hull



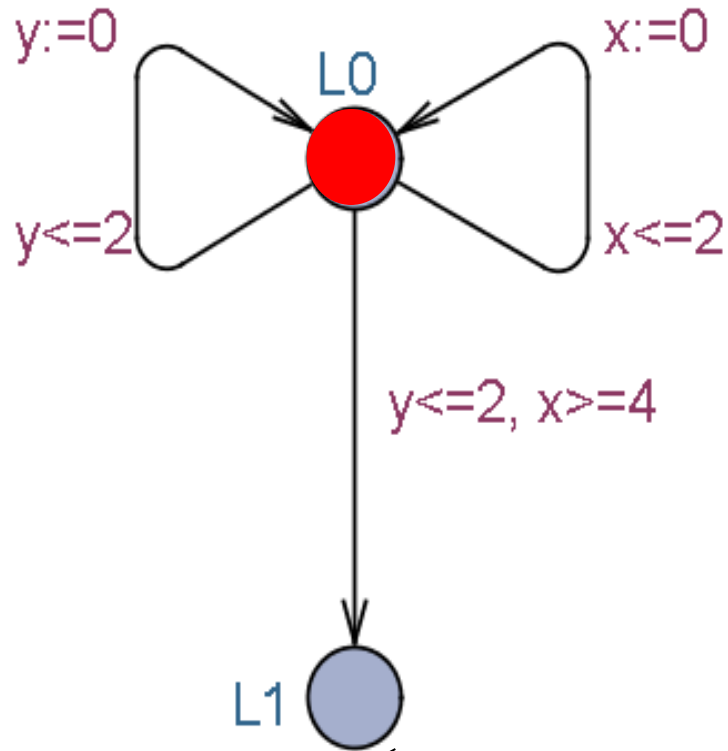
# Symbolic Exploration



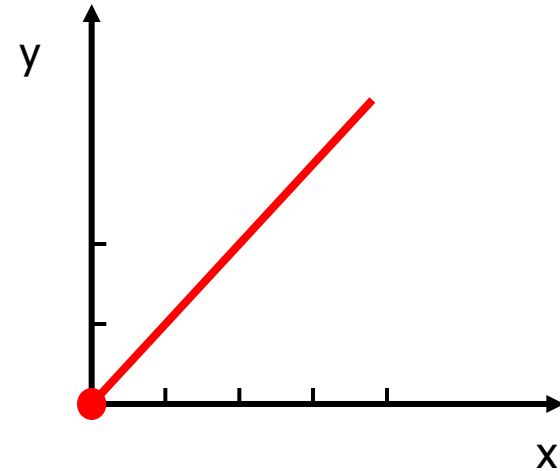
Reachable?



# Symbolic Exploration



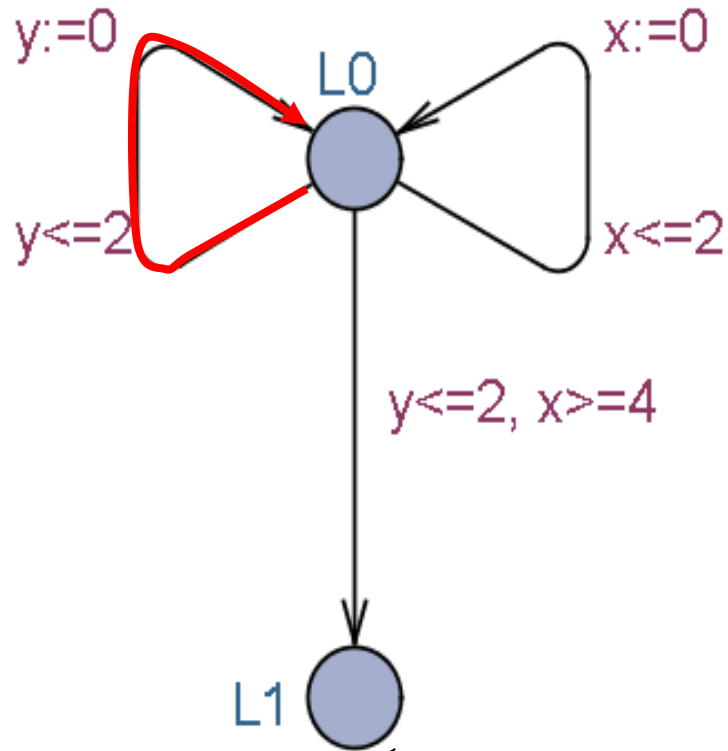
Reachable?



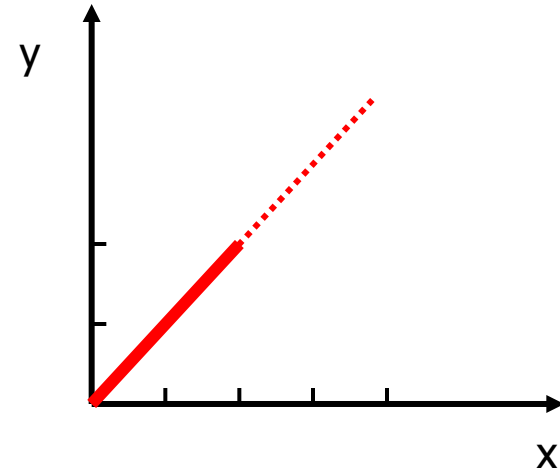
Delay



# Symbolic Exploration



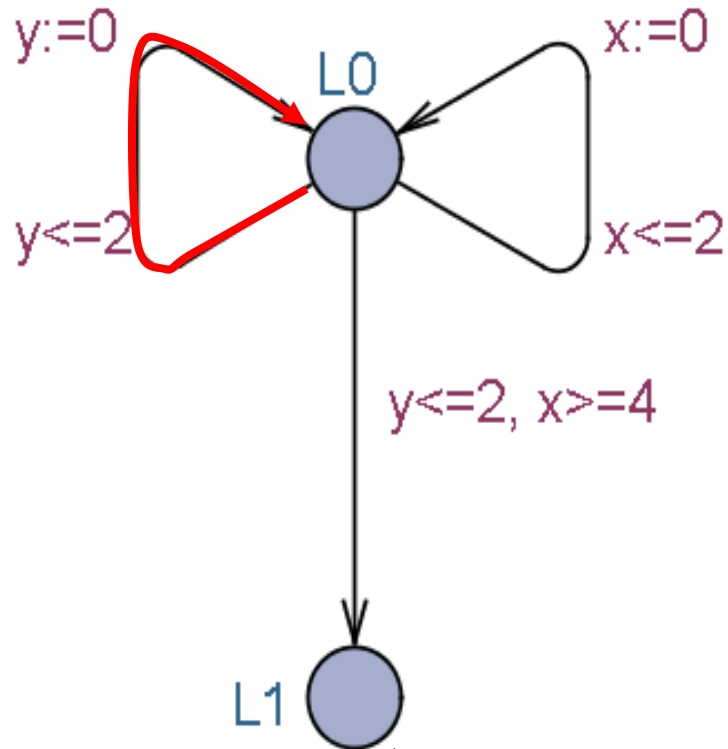
Reachable?



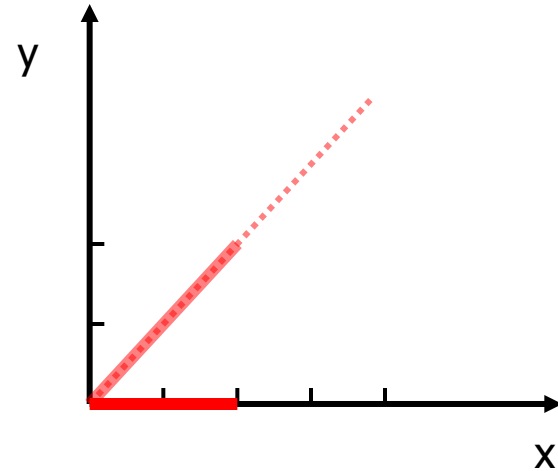
Left



# Symbolic Exploration



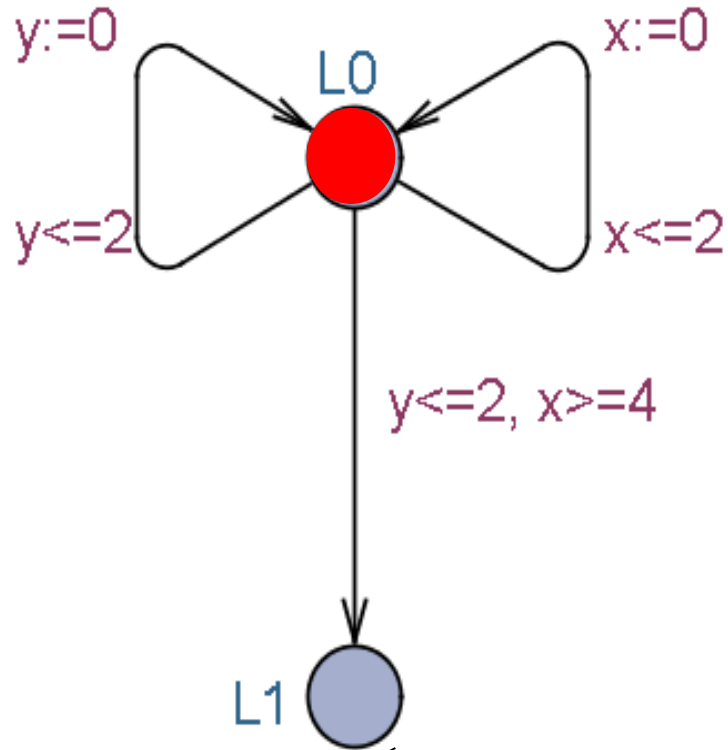
Reachable?



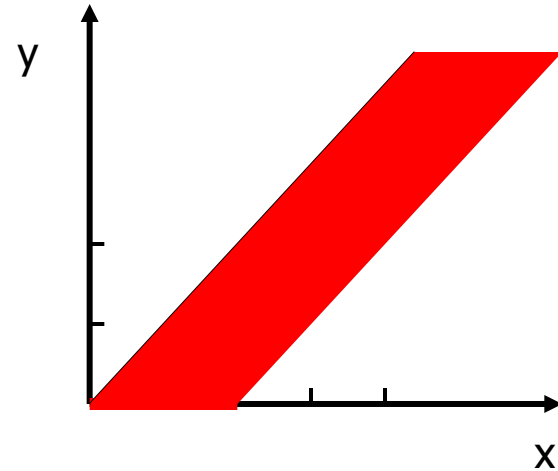
Left



# Symbolic Exploration



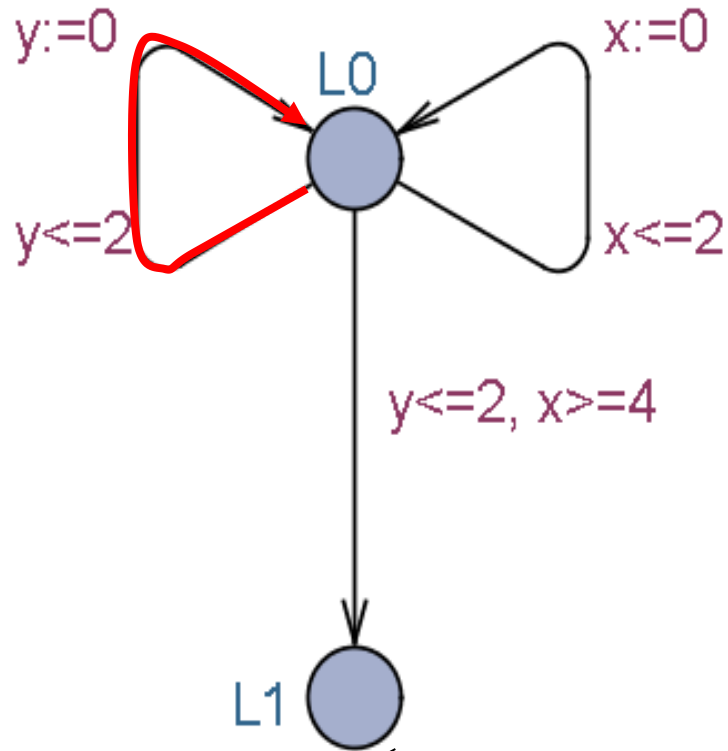
Reachable?



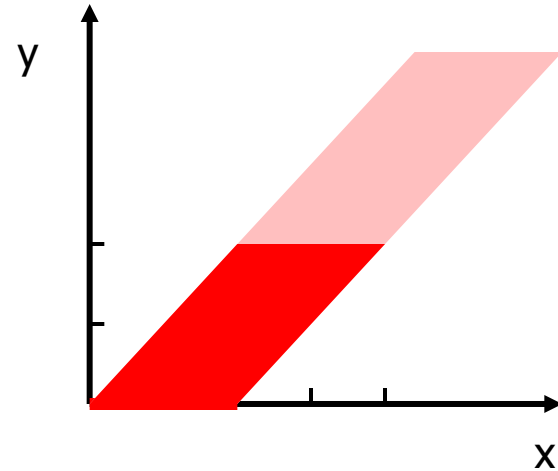
Delay



# Symbolic Exploration



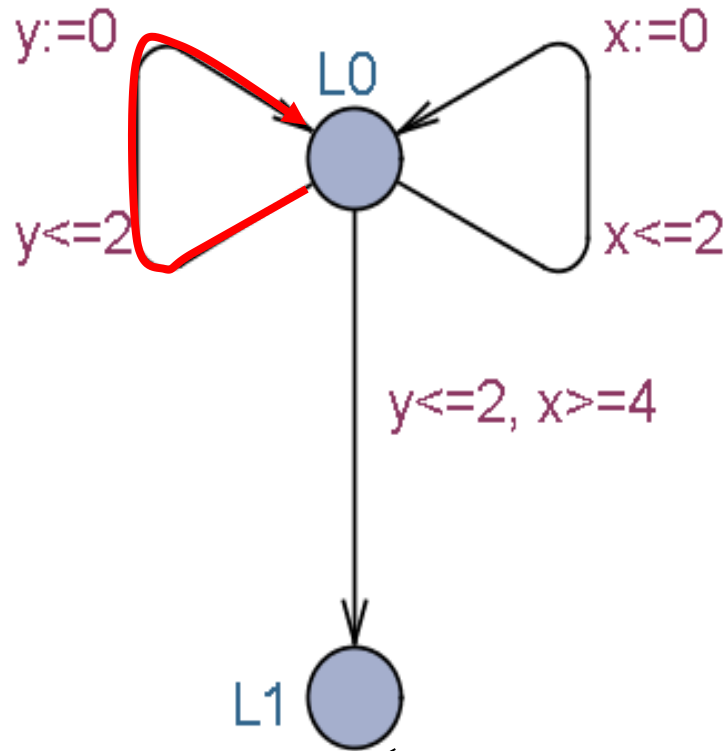
Reachable?



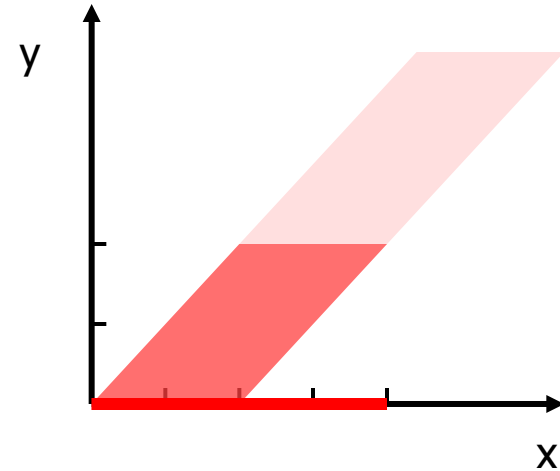
Left



# Symbolic Exploration



Reachable?

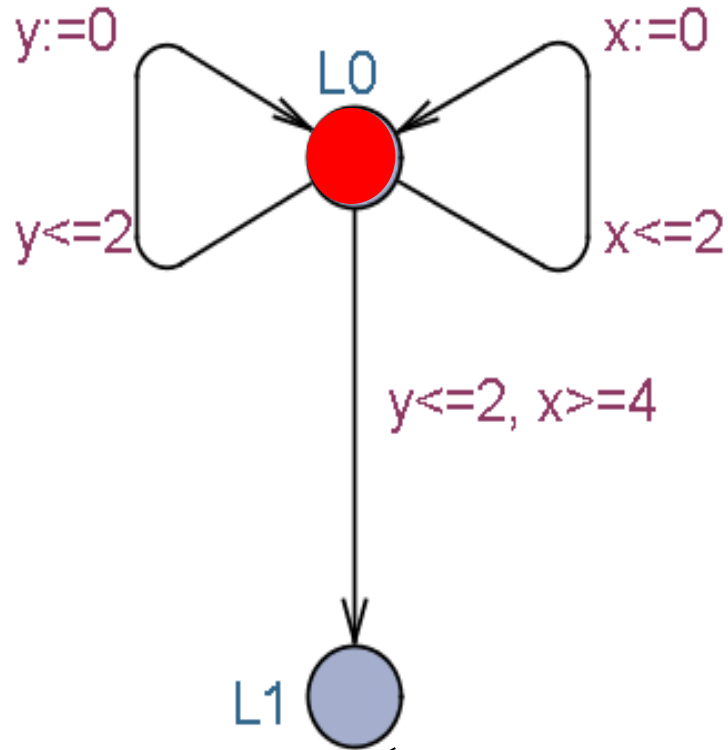


Left

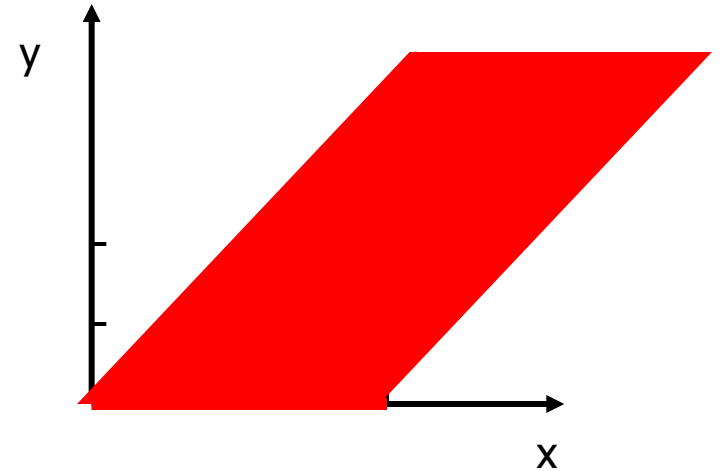




# Symbolic Exploration



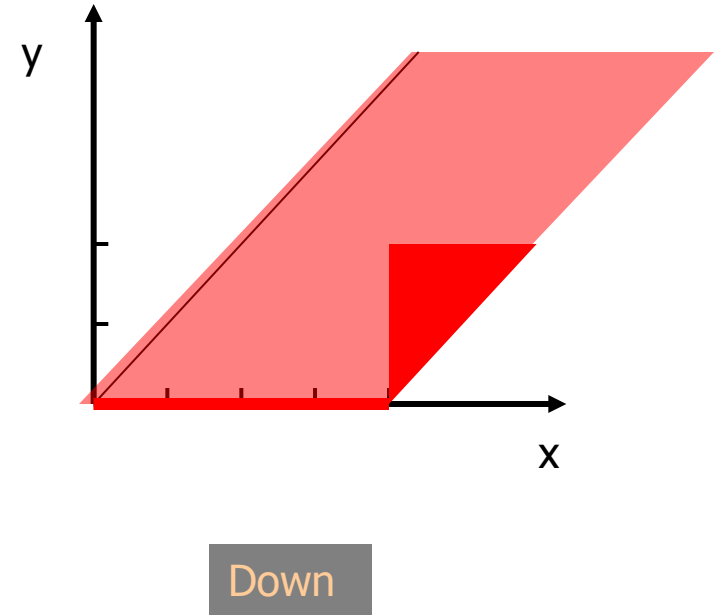
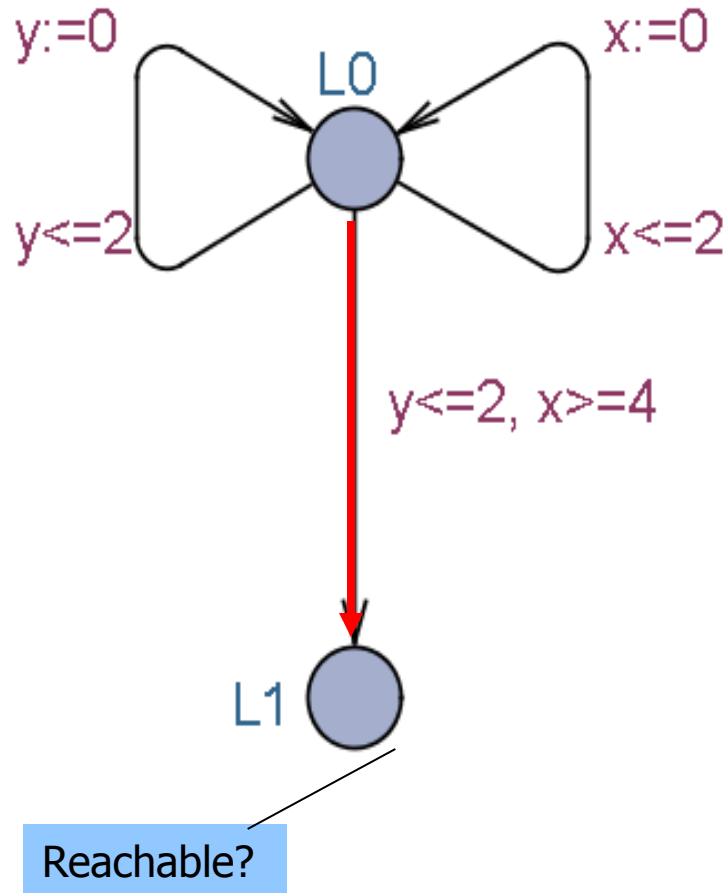
Reachable?



Delay



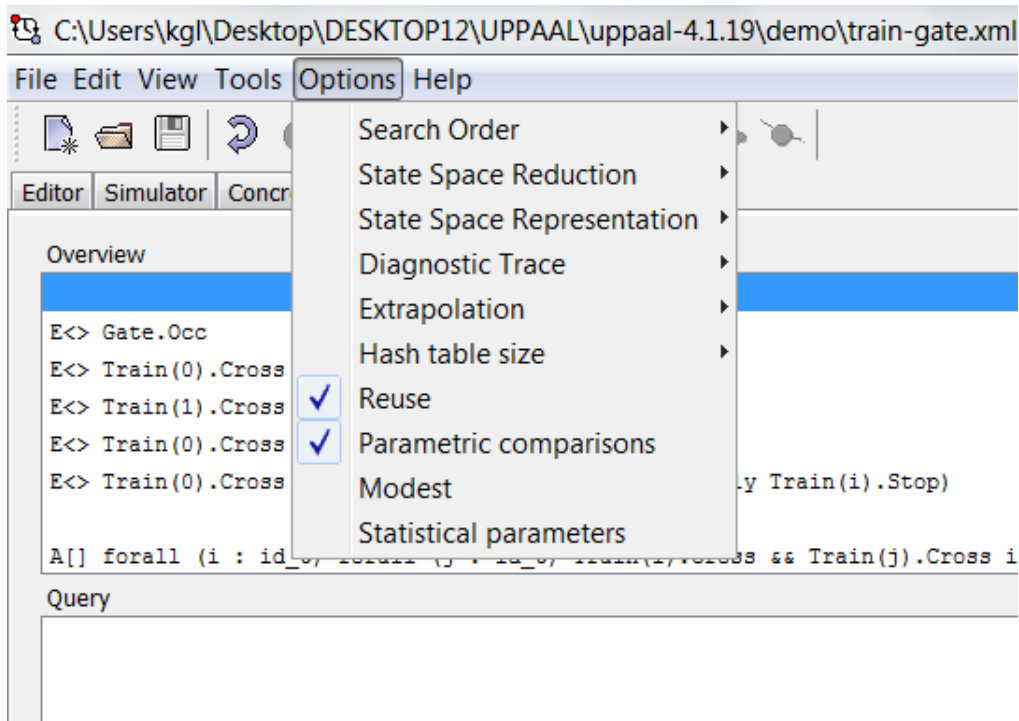
# Symbolic Exploration



# Verification Options



# Verification Options



## Search Order

- Depth First
- Breadth First
- Random Depth First

## State Space Reduction

- None
- Conservative
- Aggressive
- Extreme

## State Space Representation

- DBM
- Compact Form
- Under Approximation
- Over Approximation

## Diagnostic Trace

- Some
- Shortest
- Fastest

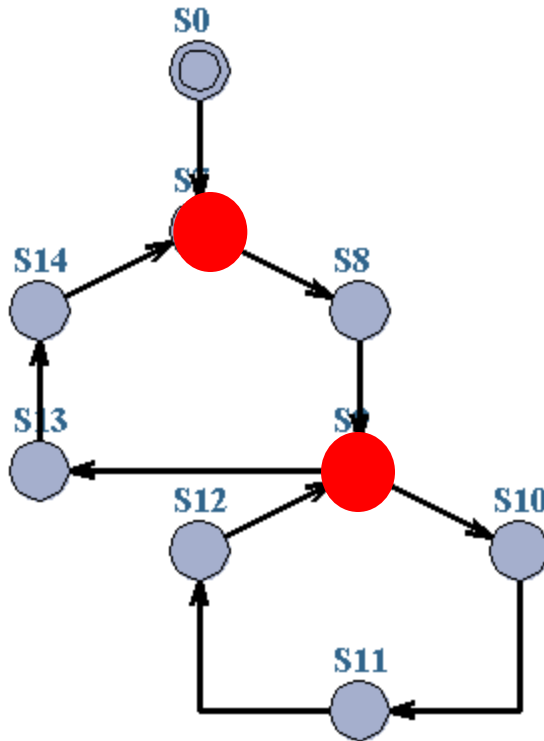
## Extrapolation

## Hash Table size

## Reuse



# State Space Reduction



## Cycles:

Only symbolic states involving loop-entry points need to be saved on **Passed** list

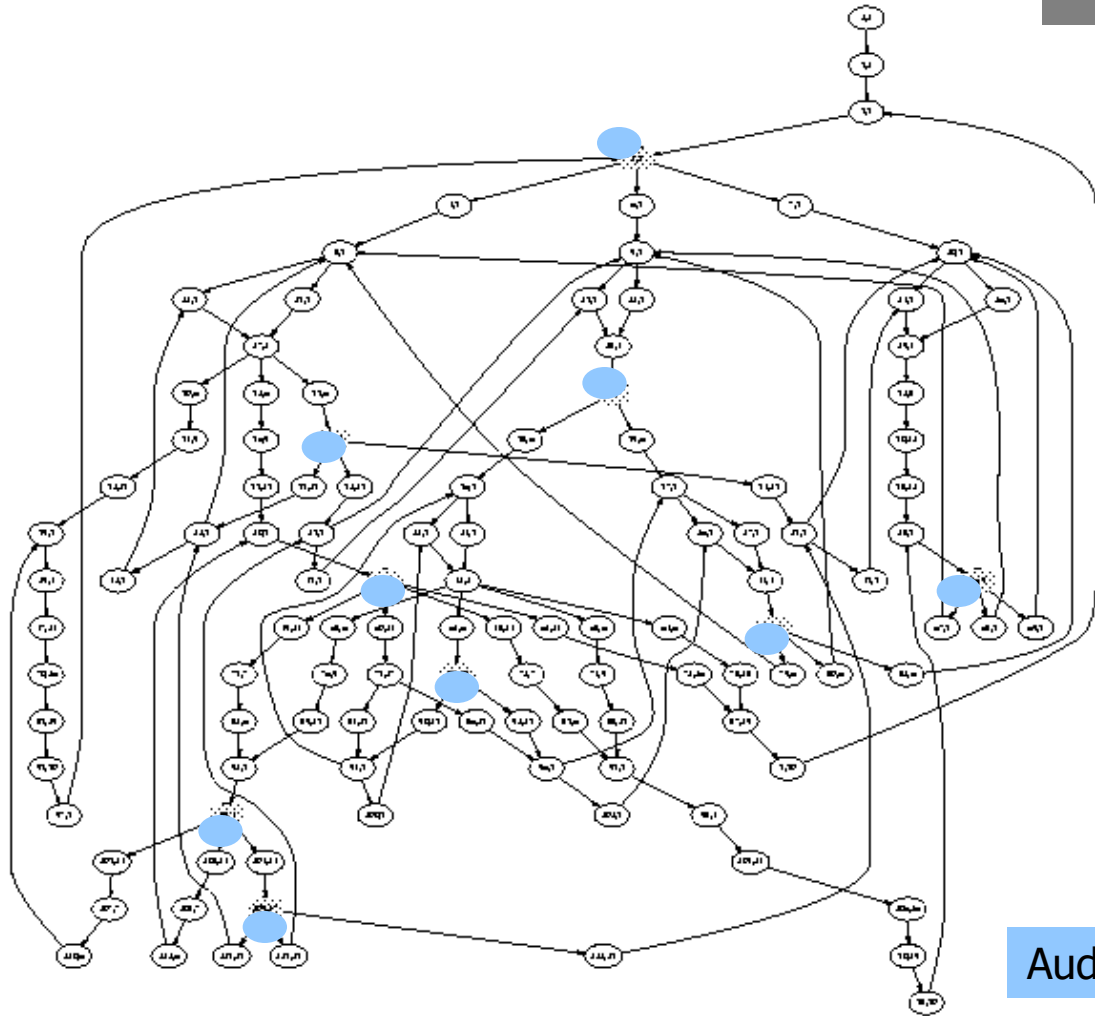


# To Store or Not To Store

Behrmann, Larsen, Pelanek 2003

117 states<sub>total</sub>  
→  
81 states<sub>entrypoint</sub>  
→  
9 states

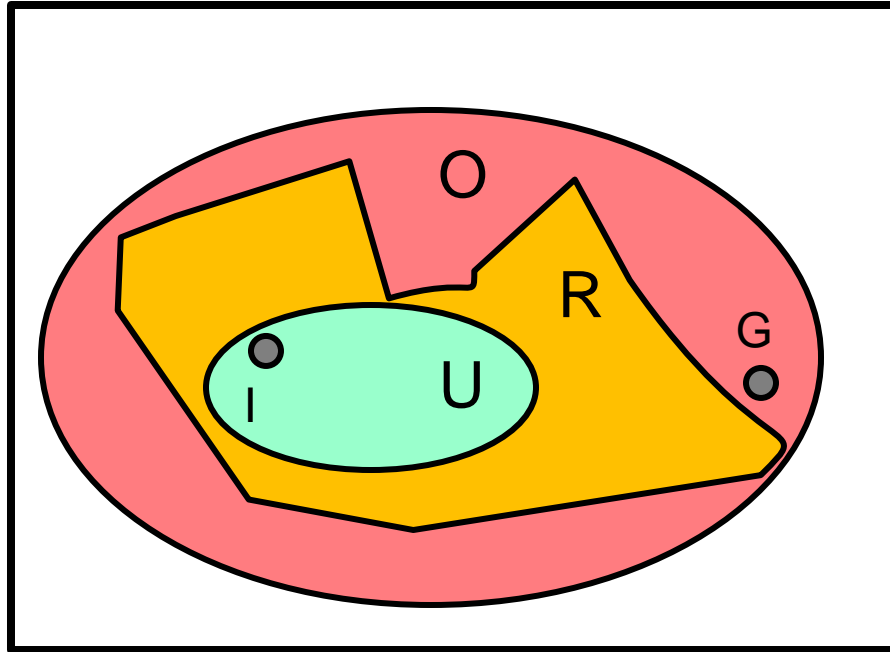
Time OH  
less than 10%



Audio Protocol



# Over/Under Approximation



Declared State Space

Question:

$G \in R ?$

How to use:

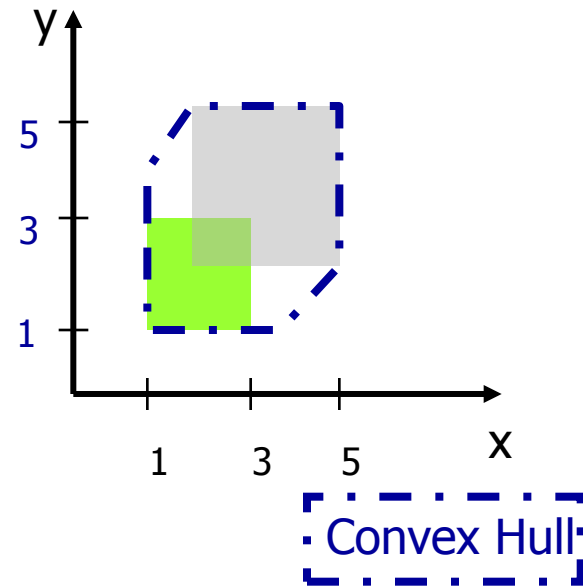
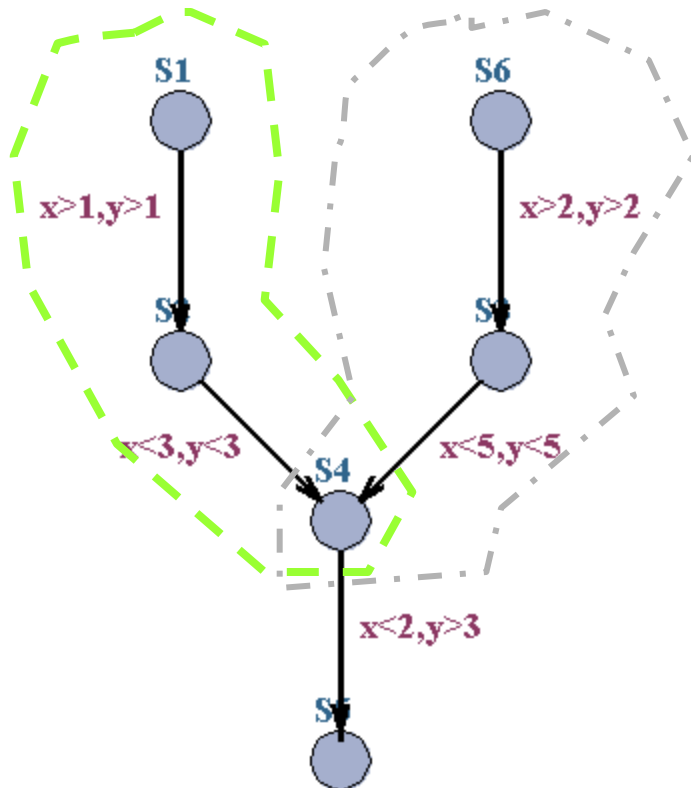
$G \in O ?$

$G \in U ?$

$$G \in U \Rightarrow G \in R$$

$$\neg(G \in O) \Rightarrow \neg(G \in R)$$

# Over-approximation Convex Hull

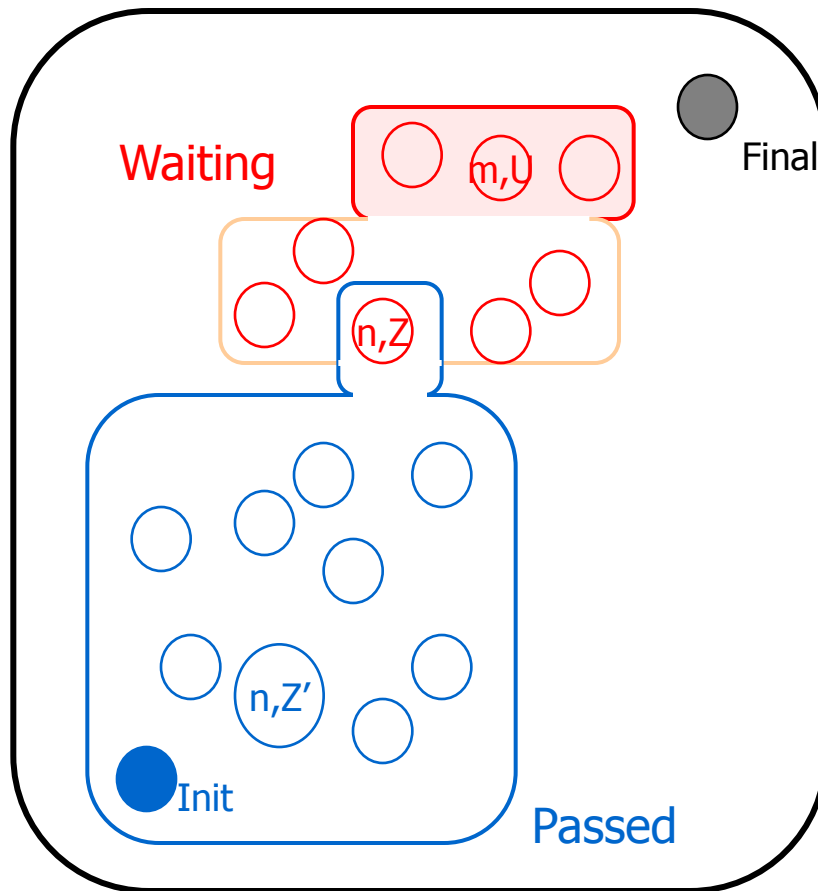


TACAS04: An **EXACT** method performing as well as Convex Hull has been developed based on abstractions taking max constants into account distinguishing between clocks, locations and  $\leq$  &  $\geq$

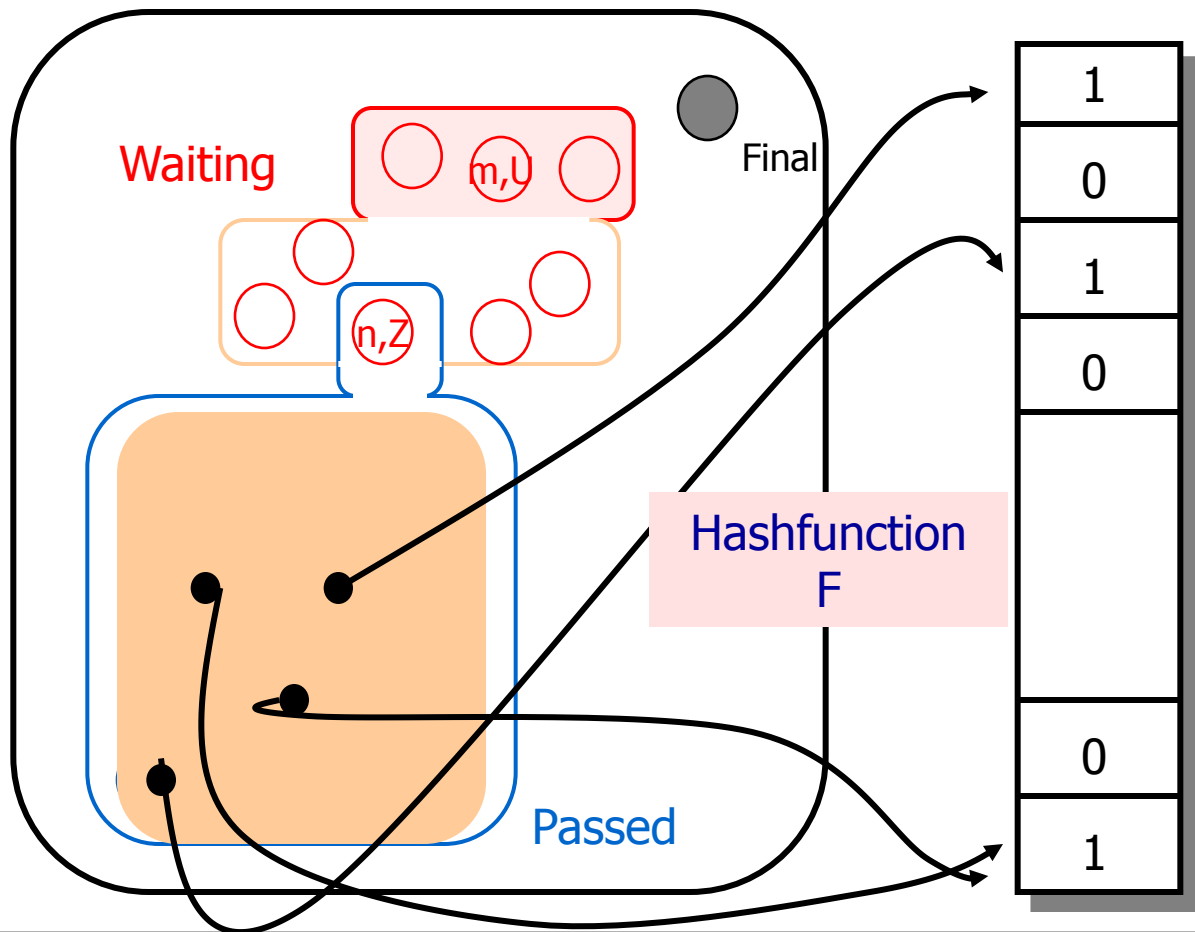
Kim Larsen [41]



# Under-approximation Bitstate Hashing



# Under-approximation Bitstate Hashing

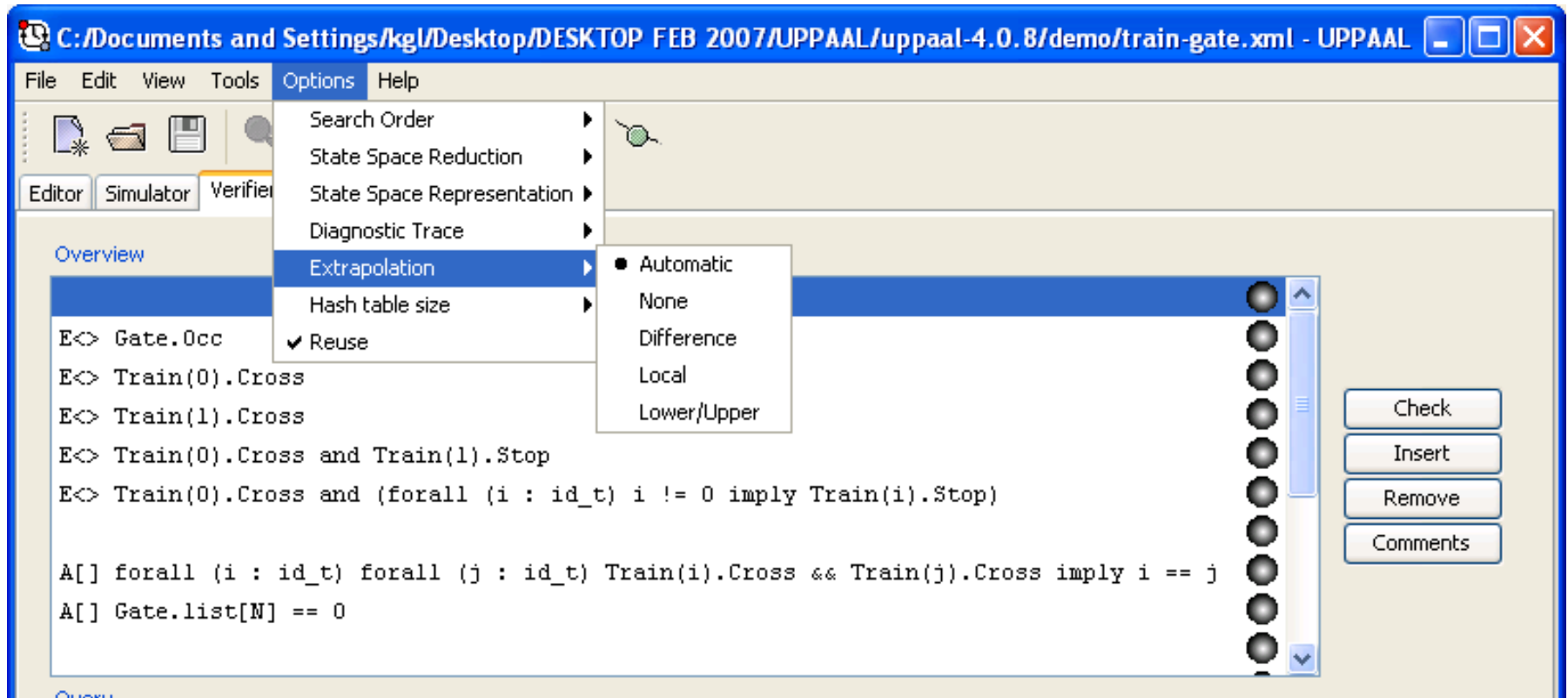


Passed=  
Bitarray

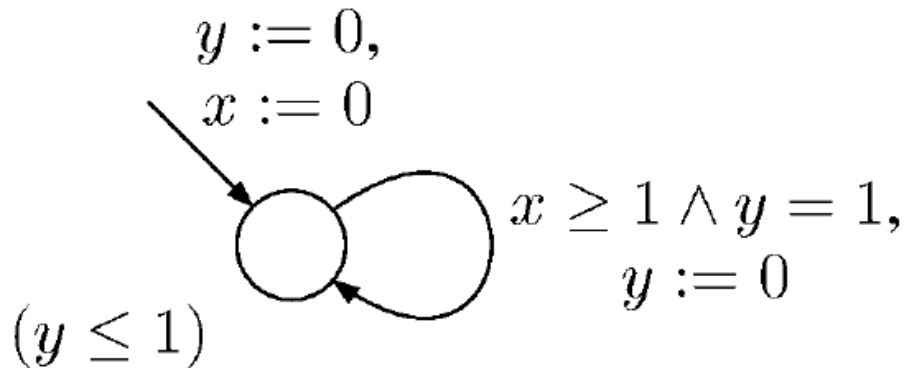
UPPAAL  
4 - 512 Mbits



# Extrapolation

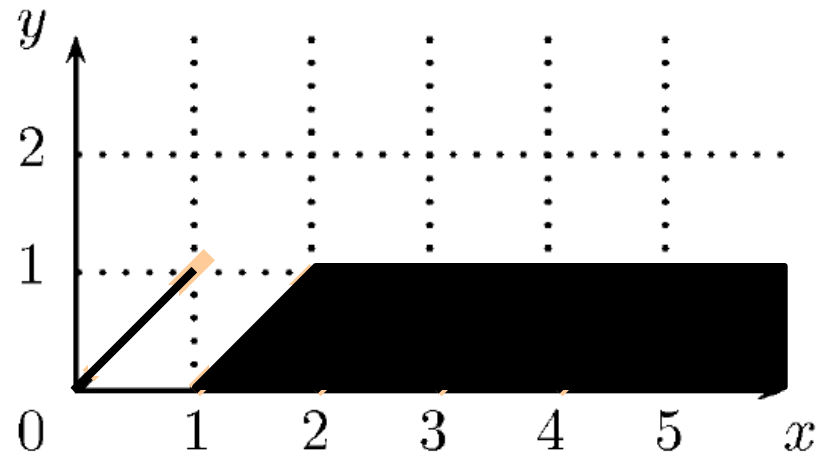


# Forward Symbolic Exploration



TERMINATION  
not  
garanteed

Need for  
Finite  
Abstractions



# Abstractions

$a : \mathcal{P}(R_{\geq 0}^X) \hookrightarrow \mathcal{P}(R_{\geq 0}^X)$  such that  $W \subseteq a(W)$

$$\frac{(\ell, W) \Rightarrow (\ell', W')}{(\ell, W) \Rightarrow_a (\ell', a(W'))} \quad \text{if } W = a(W)$$

We want  $\Rightarrow_a$  to be:

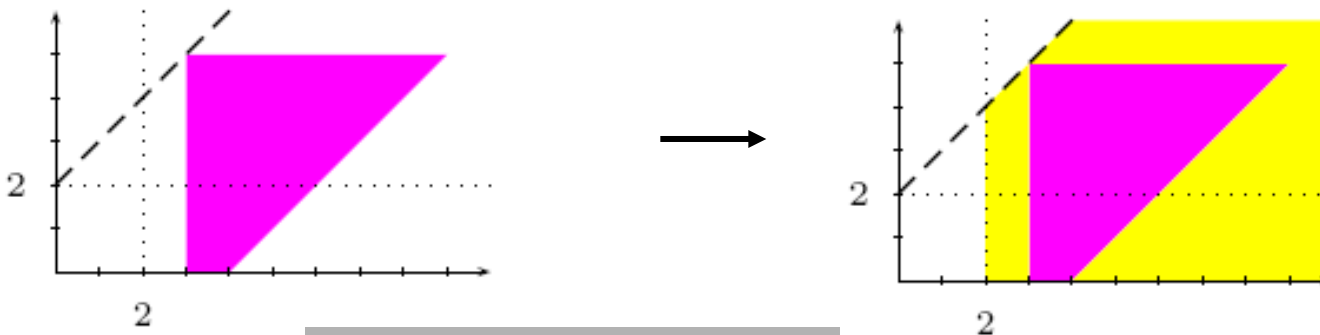
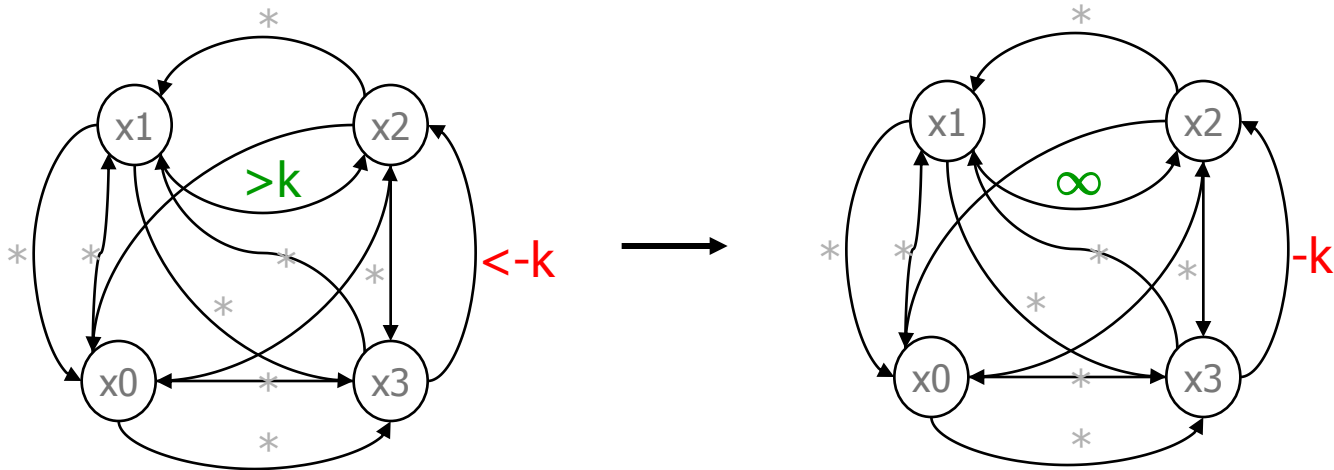
- **sound** & complete wrt reachability
- **finite**
- **easy** to compute
- as **coarse** as possible



# Abstraction by Extrapolation

[Daws, Tripakis 98]

Let  $k$  be the largest constant appearing in the TA

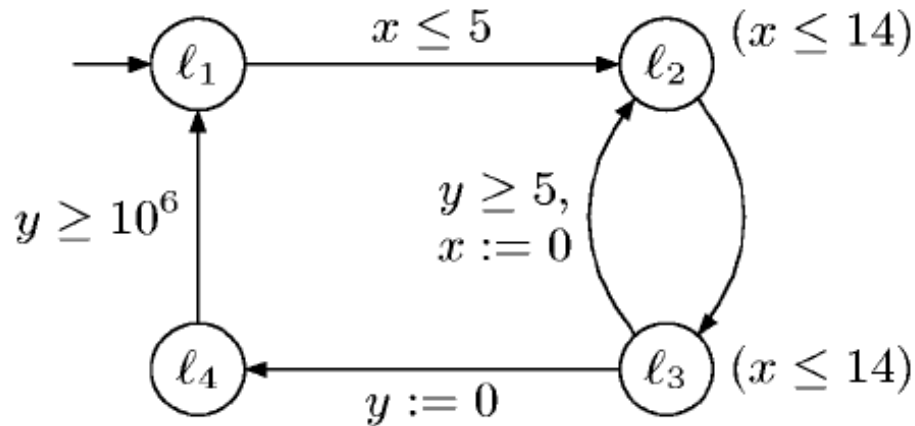


Sound & Complete  
Ensures Termination



# Location Dependency

[Behrmann, Bouyer,  
Fleury, Larsen 03]



$$k_x = 5 \quad k_y = 10^6$$

Will generate all symbolic states of the form

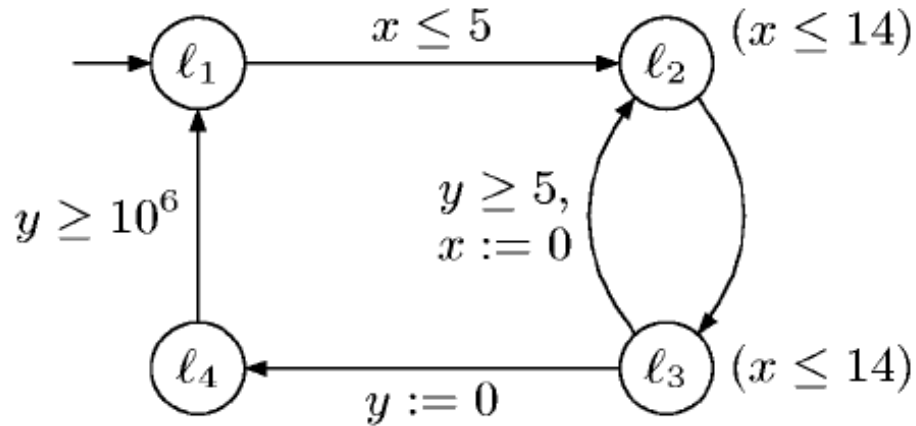
$$(l_2, x \in [0, 14], y \in [5, 14n], y-x \in [5, 14n-14])$$

for  $n \leq 10^6/14$  !!

But  $y \geq 10^6$  is not RELEVANT in  $l_2$



# Location Dependent Constants



$$k_x = 5 \quad k_y = 10^6$$

$$\begin{array}{ll}
 k_x^i & = 14 \quad \text{for } i \in \{1, 2, 3, 4\} \\
 k_y^i & = 5 \quad \text{for } i \in \{1, 2, 3\} \\
 & k_y^4 = 10^6
 \end{array}$$

$k_j^i$  may be found as solution to simple linear constraints!

Active Clock Reduction:

$$k_j^i = -\infty$$





# Experiments

Active by default

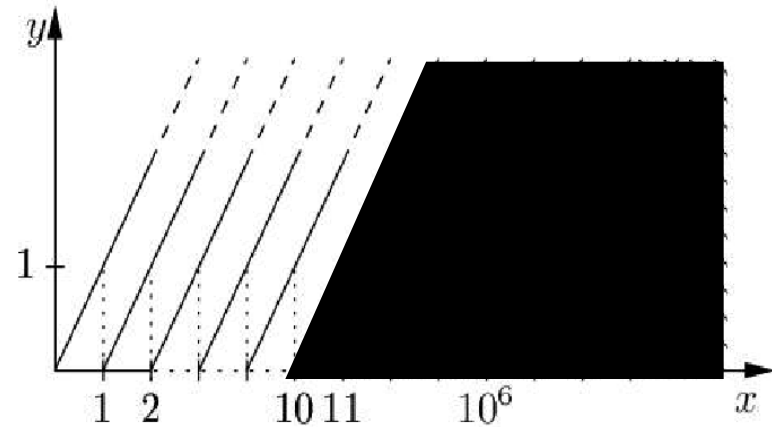
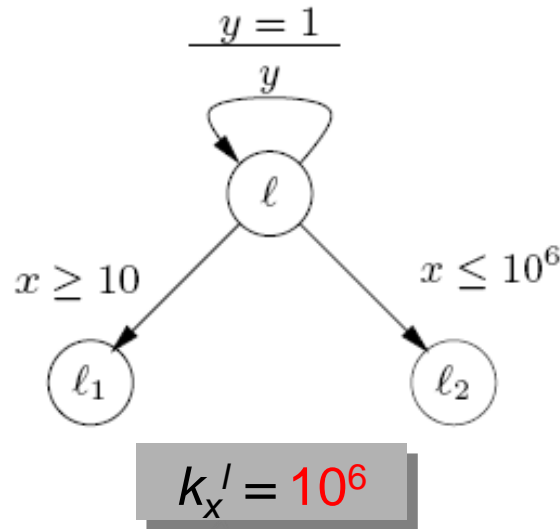


	<i>Constant BIG</i>	<i>Global Method</i>	<i>Active-clock Reduction</i>	<i>Local Constants</i>
<i>Naive Example</i>	$10^3$	0.05s/1MB	0.05s/1MB	0.00s/1MB
	$10^4$	4.78s/3MB	4.83s/3MB	0.00s/1MB
	$10^5$	484s/13MB	480s/13MB	0.00s/1MB
	$10^6$	stopped	stopped	0.00s/1MB
<i>Two Processes</i>	$10^3$	3.24s/3MB	3.26s/3MB	0.01s/1MB
	$10^4$	5981s/9MB	5978s/9MB	0.37s/2MB
	$10^5$	stopped	stopped	72s/5MB
<i>Asymmetric Fischer</i>	$10^3$	0.01s/1MB	0.01s/1MB	0.01s/1MB
	$10^4$	2.20s/3MB	2.20s/3MB	0.85s/2MB
	$10^5$	333s/19MB	333s/19MB	160s/13MB
	$10^6$	33307s/122MB	33238s/122MB	16330s/65MB
<i>Bang &amp; Olufsen</i>	25000	stopped	159s/243MB	123s/204MB



# Lower and Upper Bounds

[Behrmann, Bouyer,  
Larsen, Pelanek 04]



Given that  $x \leq 10^6$  is an *upper* bound implies that

$(l, v_x, v_y)$  *simulates*  $(l, v'_x, v_y)$

whenever  $v'_x \geq v_x \geq 10$ .

For reachability downward  
closure wrt **simulation**  
suffices!

# Advanced Extrapolation

		Classical			Loc. dep. Max			Loc. dep. LU			Convex Hull		
		-n1			-n2			-n3			-A		
Model		Time	States	Mem	Time	States	Mem	Time	States	Mem	Time	States	Mem
Fischer	f5	4.02	82,685	5	0.24	16,980	3	0.03	2,870	3	0.03	3,650	3
	f6	597.04	1,489,230	49	6.67	158,220	7	0.11	11,484	3	0.10	14,658	3
	f7				352.67	1,620,542	46	0.47	44,142	3	0.45	56,252	5
	f8							2.11	164,528	6	2.08	208,744	12
	f9							8.76	598,662	19	9.11	754,974	39
	f10							37.26	2,136,980	68	39.13	2,676,150	143
	f11							152.44	7,510,382	268			
CSMA/CD	c5	0.55	27,174	3	0.14	10,569	3	0.02	2,027	3	0.03	1,651	3
	c6	19.39	287,109	11	3.63	87,977	5	0.10	6,296	3	0.06	4,986	3
	c7				195.35	813,924	29	0.28	18,205	3	0.22	14,101	4
	c8							0.98	50,058	5	0.66	38,060	7
	c9							2.90	132,623	12	1.89	99,215	17
	c10							8.42	341,452	29	5.48	251,758	49
	c11							24.13	859,265	76	15.66	625,225	138
	c12							68.20	2,122,286	202	43.10	1,525,536	394
	bus	102.28	6,727,443	303	66.54	4,620,666	254	62.01	4,317,920	246	45.08	3,826,742	324
	philips	0.16	12,823	3	0.09	6,763	3	0.09	6,599	3	0.07	5,992	3
	sched	17.01	929,726	76	15.09	700,917	58	12.85	619,351	52	55.41	3,636,576	427



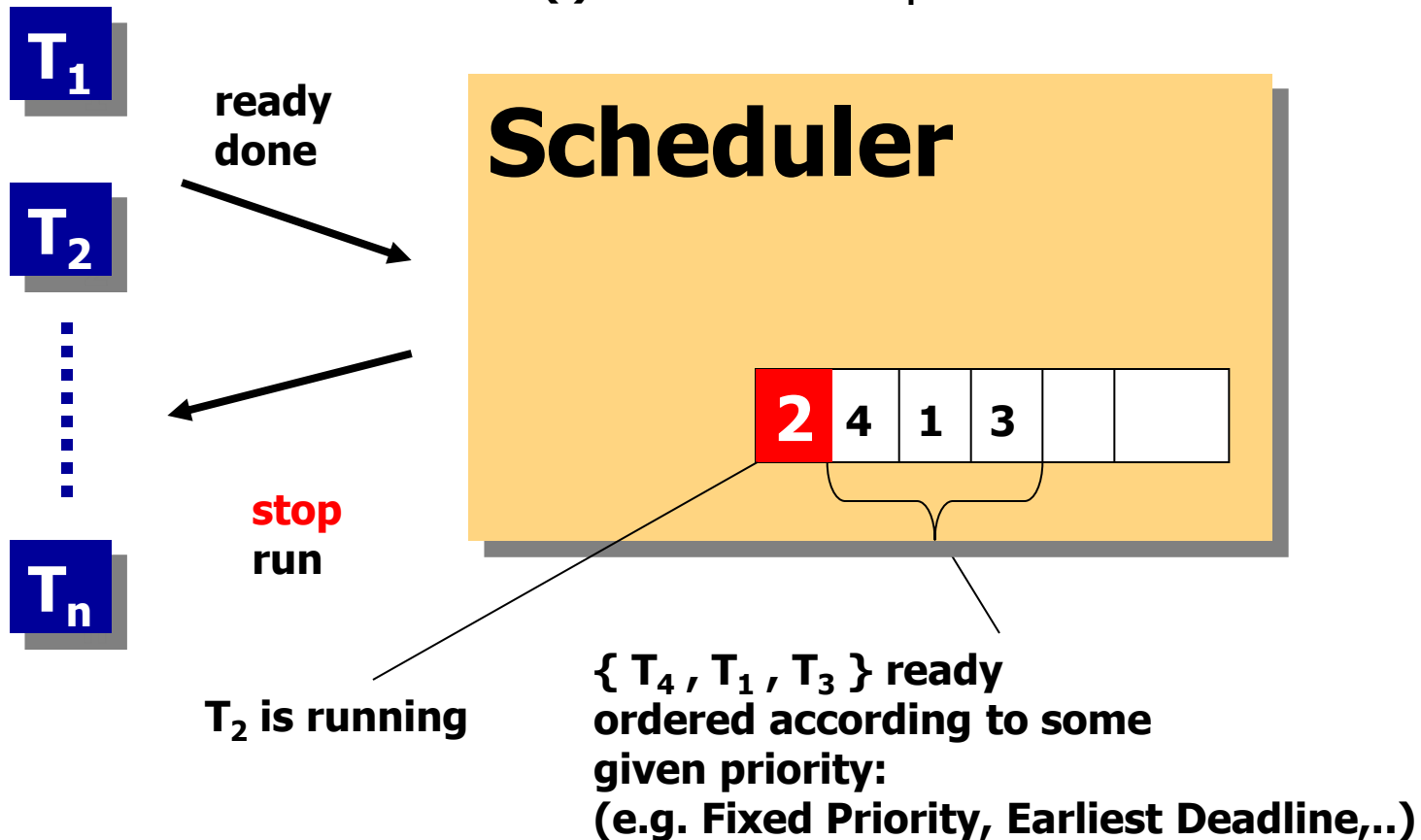
# Application: Schedulability Analysis



# Task Scheduling

*utilization of CPU*

$P(i), [E(i), L(i)], ..$  : period or  
earliest/latest arrival or .. for  $T_i$   
 $C(i)$ : execution time for  $T_i$   
 $D(i)$ : deadline for  $T_i$



# Classical Scheduling Theory

## Utilisation-Based Analysis

- A simple **sufficient but not necessary** schedulability test exists

$$U \equiv \sum_{i=1}^N \frac{C_i}{T_i} \leq N(2^{1/N} - 1)$$

$$U \leq 0.69 \text{ as } N \rightarrow \infty$$

Where C is WCET and T is period

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## Response Time Equation

$$R_i = C_i + \sum_{j \in hp(i)} \left\lceil \frac{R_j}{T_j} \right\rceil C_j$$

Where  $hp(i)$  is the set of tasks with priority higher than task  $i$

Solve by forming a recurrence relationship:

$$w_i^{n+1} = C_i + \sum_{j \in hp(i)} \left\lceil \frac{w_i^n}{T_j} \right\rceil C_j$$

The set of values  $w_i^0, w_i^1, w_i^2, \dots, w_i^n, \dots$  is monotonically non decreasing  
When  $w_i^n = w_i^{n+1}$  the solution to the equation has been found,  $w_i^0$  must not be greater than  $R_i$  (e.g. 0 or  $C_i$ )

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## Classical WCRT Analysis



- "Classical" scheduling analysis technique
- For all tasks  $i$ :  $WCRT_i \leq \text{Deadline}_i$

$$R_i = B_i + C_i + \sum_{j \in hp(i)} \left\lceil \frac{R_j}{T_j} \right\rceil C_j$$

Blocking times for priority inheritance protocol (BSW):

- $Blocking(i) = \sum_{r=1}^R usage(r, i) WCET_{CriticalSection}(r)$

Blocking times for priority ceiling protocol (ASW):

$$Blocking(i) = \max_{r=1}^R usage(r, i) WCET_{CriticalSection}(r)$$

Quasimodo Workshop, Eindhoven, Nov 6, 2009

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✓ Simple to perform

- Overly conservative

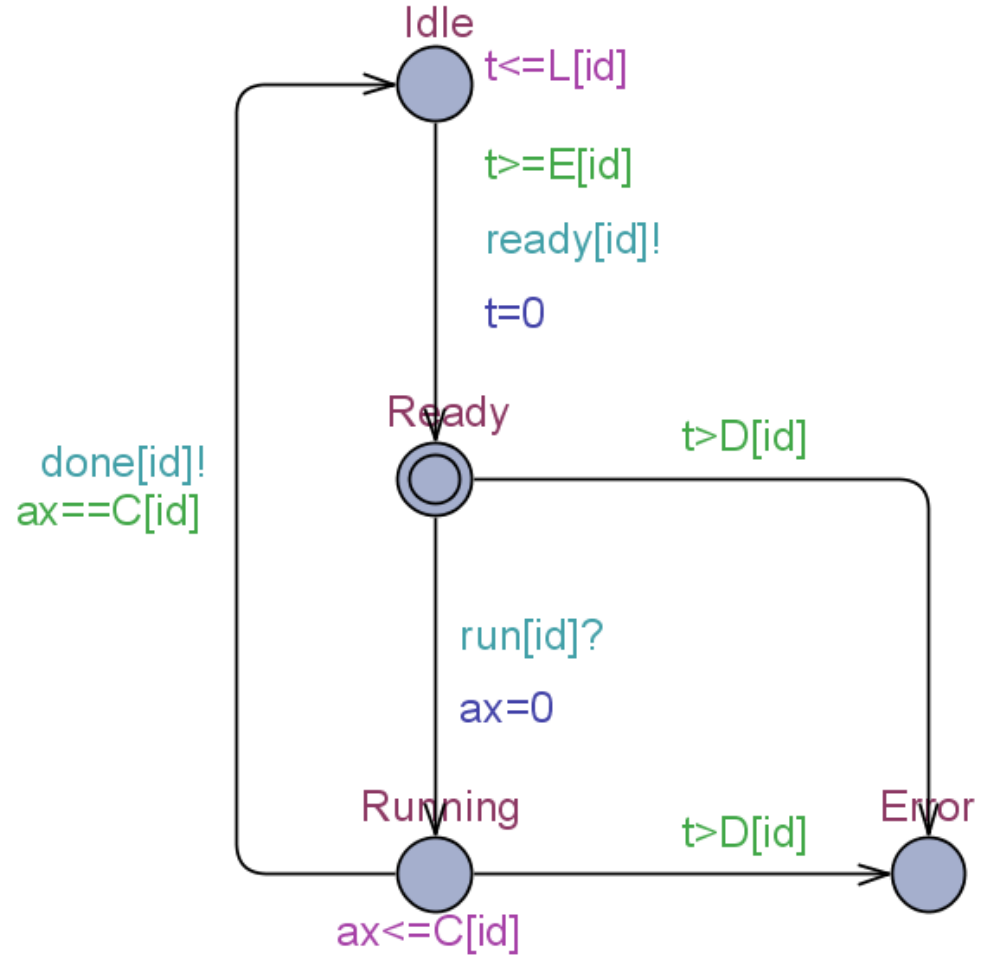
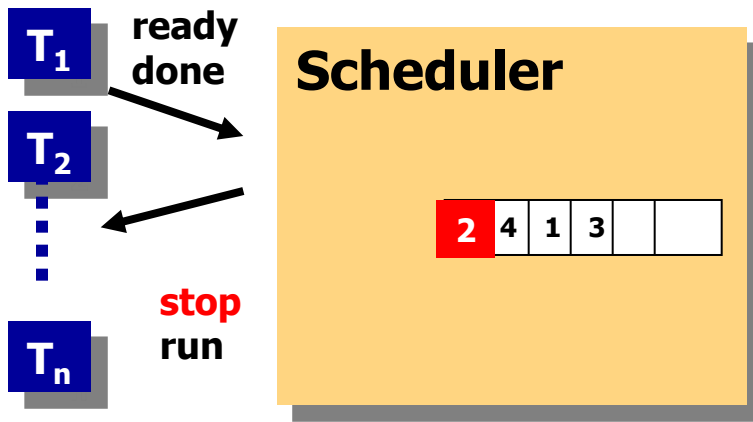
- Limited settings

- Single-processor

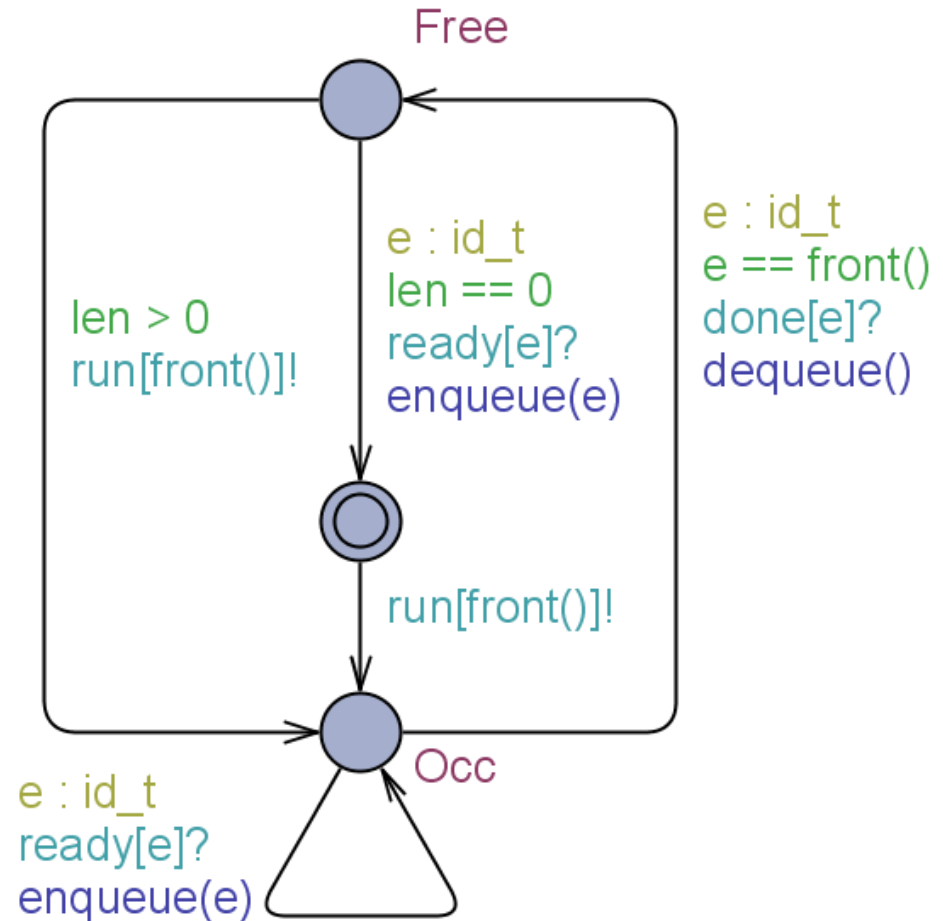
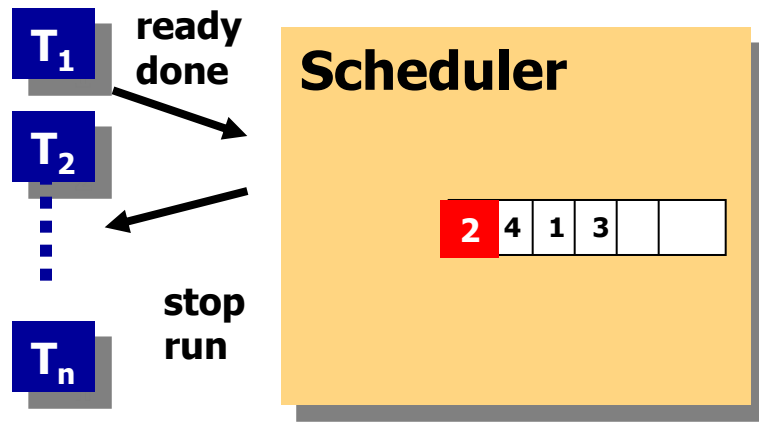
⇒ Do it in UPPAAL!



# Modeling Task



# Modeling Scheduler



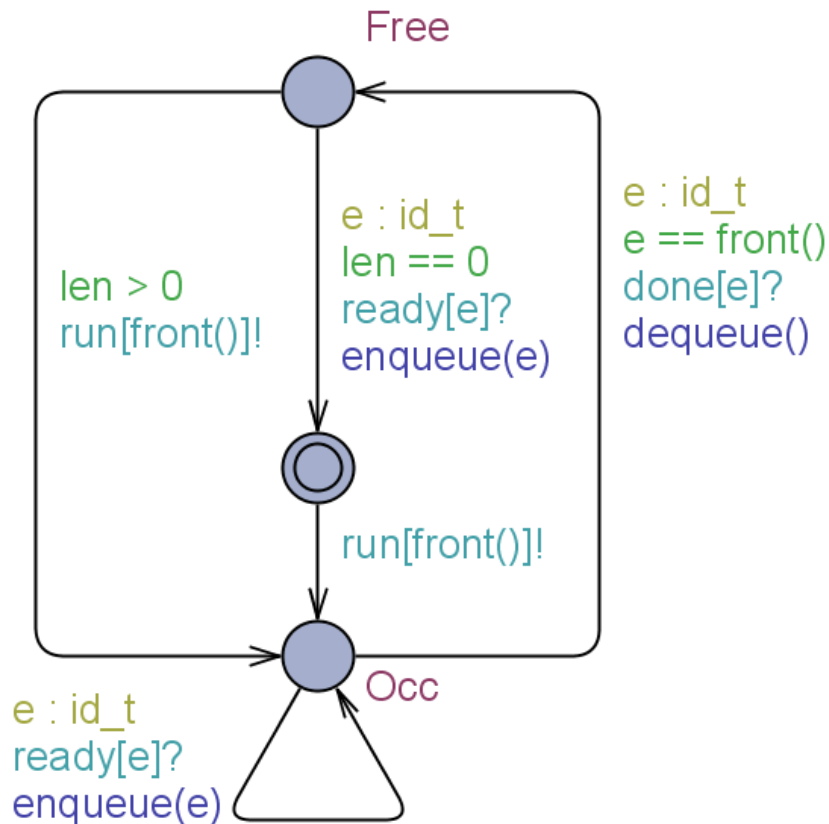
Implementation of enqueue/dequeue  
⇒ scheduling policy





# Modeling Queue

In UPPAAL 4.0  
User Defined Function

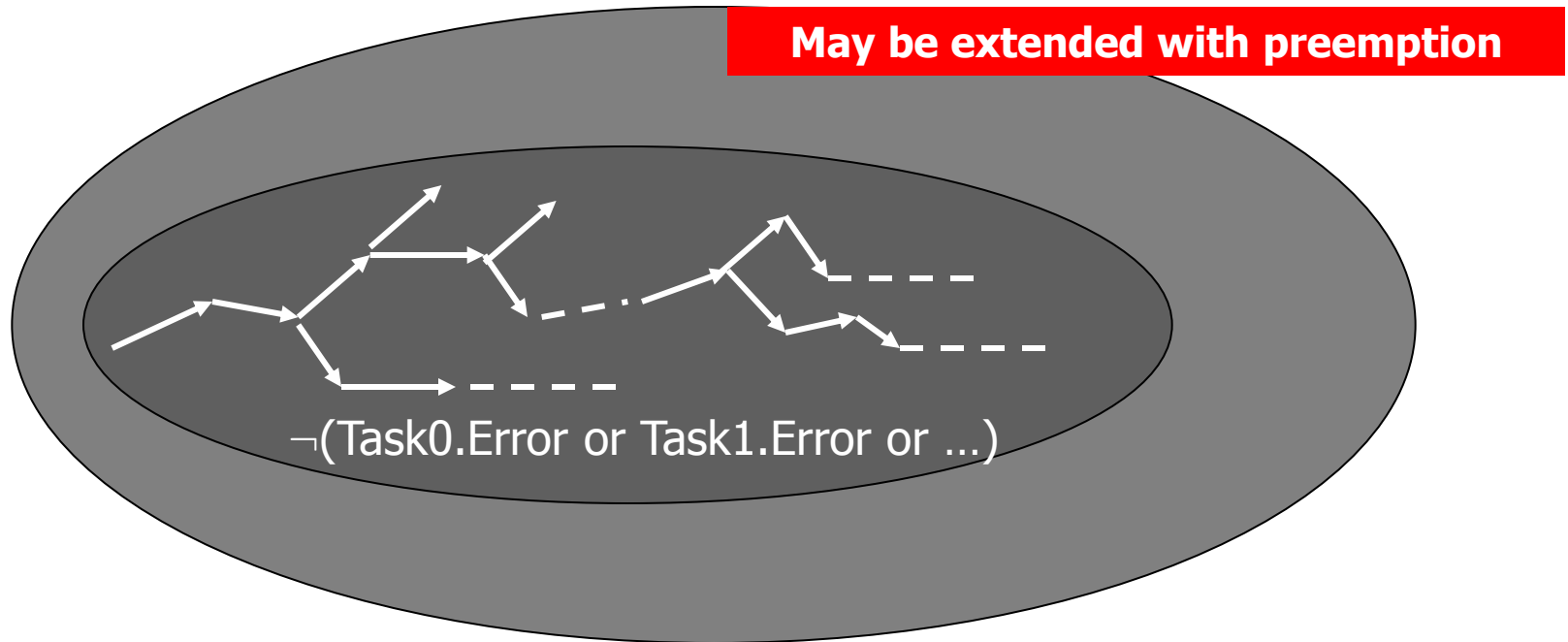


```
// Put an element at the end of the queue
void enqueue(id_t element)
{
    int tmp=0;
    list[len++] = element;
    if (len>0)
    {
        Sort by priority
        int i=len-1;
        while (i>1 && P[list[i]]>P[list[i-1]])
        {
            tmp = list[i-1];
            list[i-1] = list[i];
            list[i] = tmp;
            i--;
        }
    }
}
```

```
// Remove the front element of the queue
void dequeue()
{
    .....
}
```

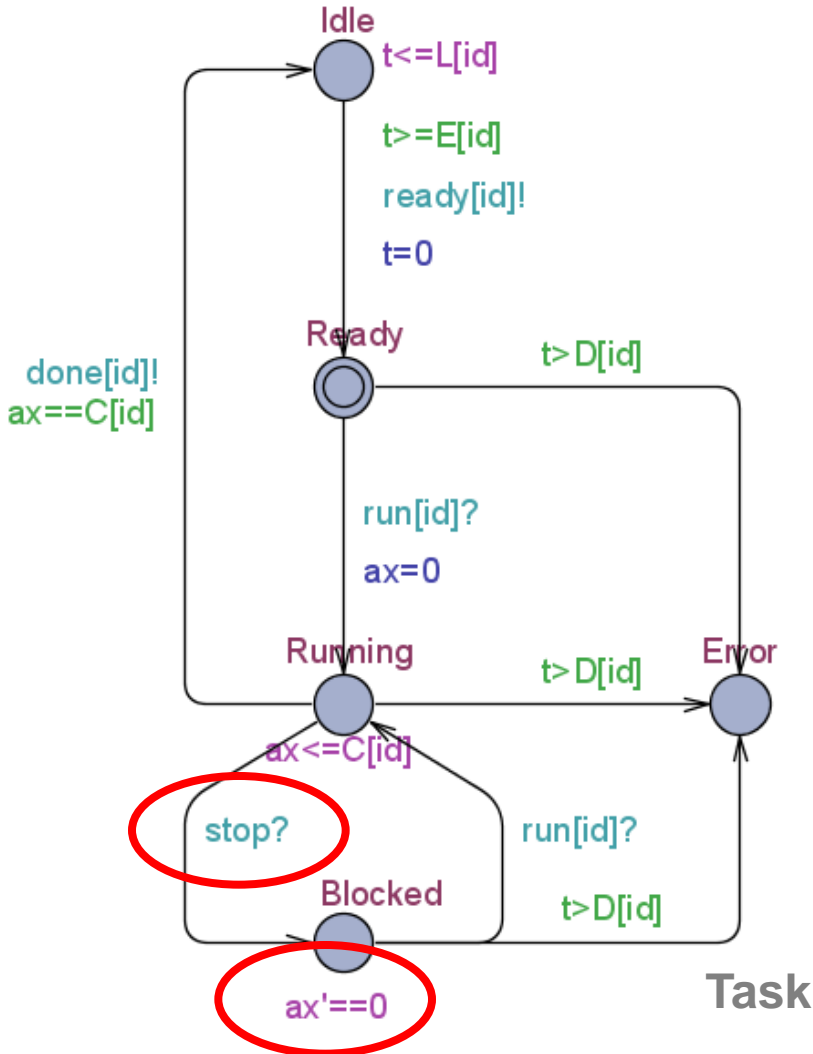


# Schedulability = Safety Property

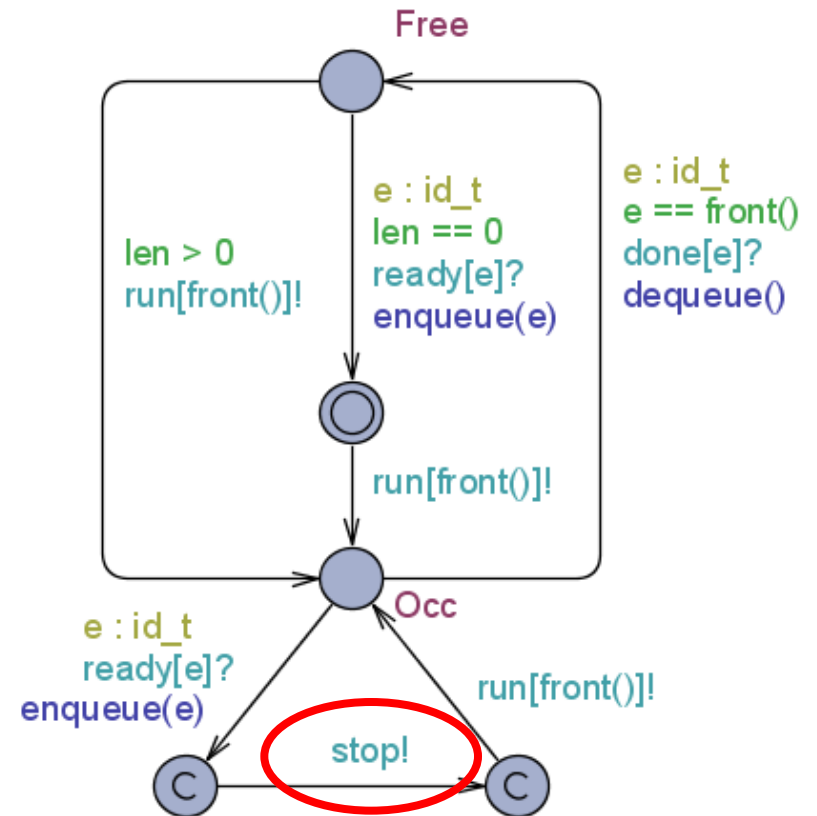


**$A \square \neg(\text{Task0.Error or Task1.Error or ...})$**

# Preemption – Stopwatches!



## Scheduler



Defeating undecidability 😊



# LAB-Exercises (cont)

<http://people.cs.aau.dk/~kgl/Shanghai2013/>

Exercise 1 (Brick Sorter)

Exercise 2 (Coffee Machine)

Excercise 19 (Train Crossing)

Exercise 28 (Jobshop Scheduling)

Exercise 14 (Gossiping Girls)

