

Verification & Modelling

Options & Patterns

Kim G Larsen



BRICS

Basic Research
in Computer Science



CENTER FOR INDLEJREDE SOFTWARE SYSTEMER

Outline

- UPPAAL
 - Modelling Formalism
 - Specification Formalism

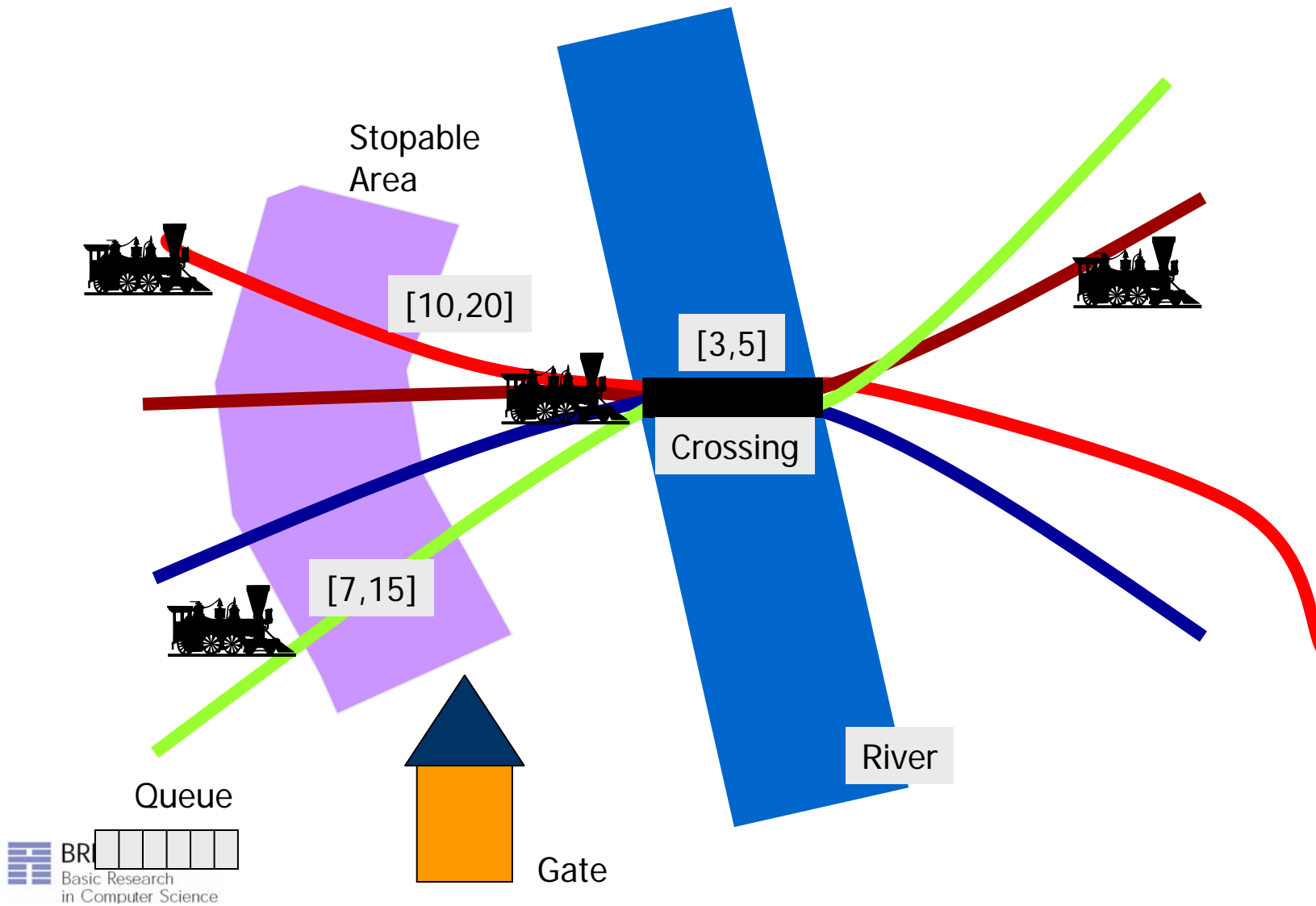
- UPPAAL Verification Engine

- Verification Options
& Modelling Patterns

- Real-Time Planning & Scheduling

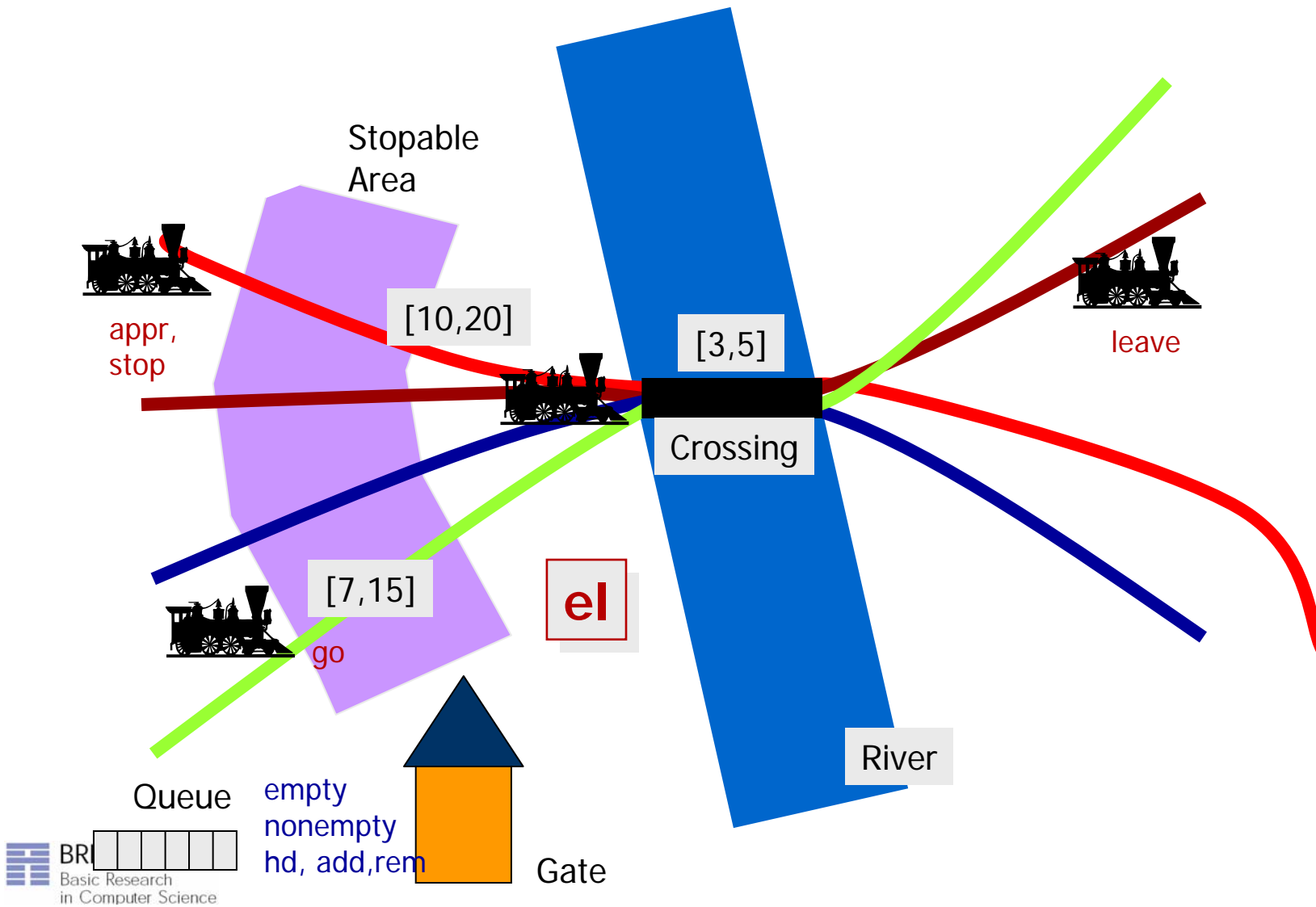
Train Crossing

Train Crossing



Train Crossing

Communication via channels and shared variable.



Specification Language

Logical Specifications

■ Validation Properties

- Possibly: $E \langle \rangle P$

■ Safety Properties

- Invariant: $A[] P$
- Pos. Inv.: $E[] P$

■ Liveness Properties

- Eventually: $A \langle \rangle P$
- Leadsto: $P \rightarrow Q$

■ Bounded Liveness

- Leads to within: $P \rightarrow_{\leq t} Q$

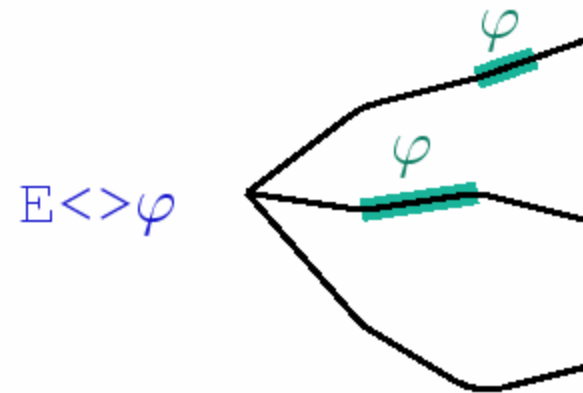
The expressions P and Q must be type safe, side effect free, and evaluate to a boolean.

Only references to integer variables, constants, clocks, and locations are allowed (and arrays of these).

Logical Specifications

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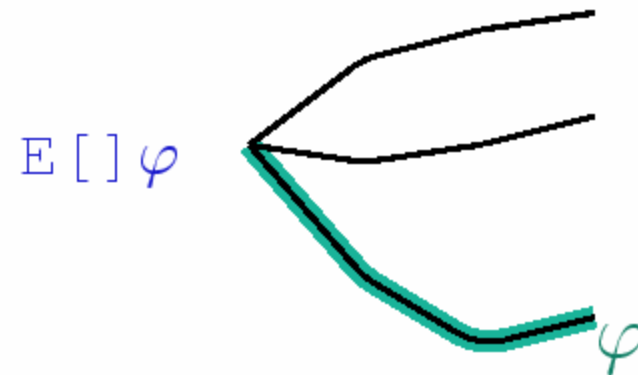
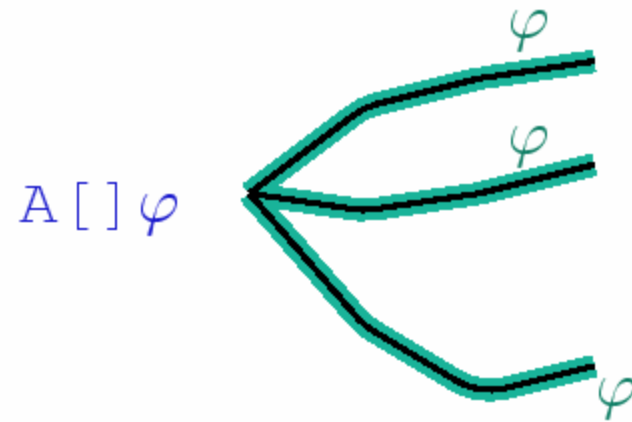
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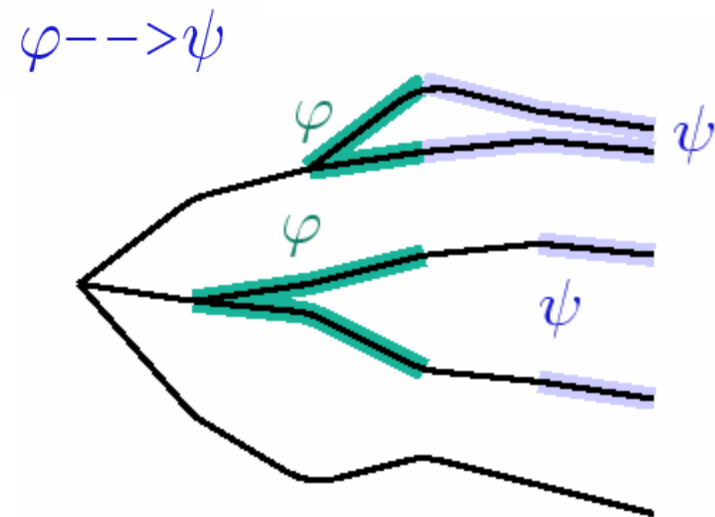
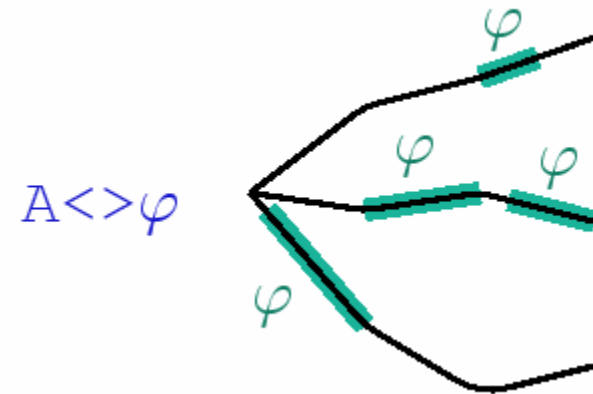
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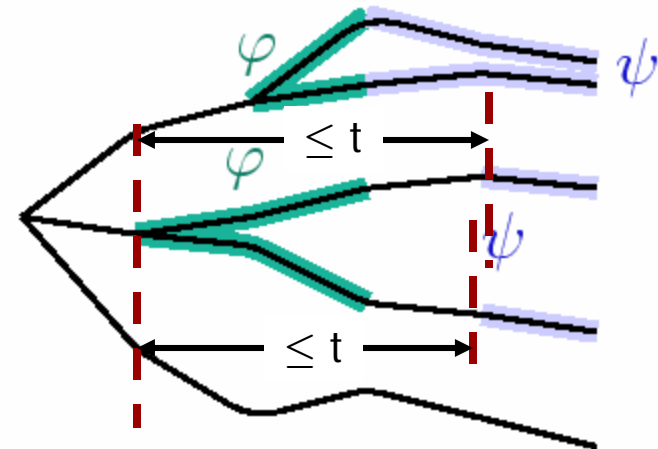
Logical Specifications

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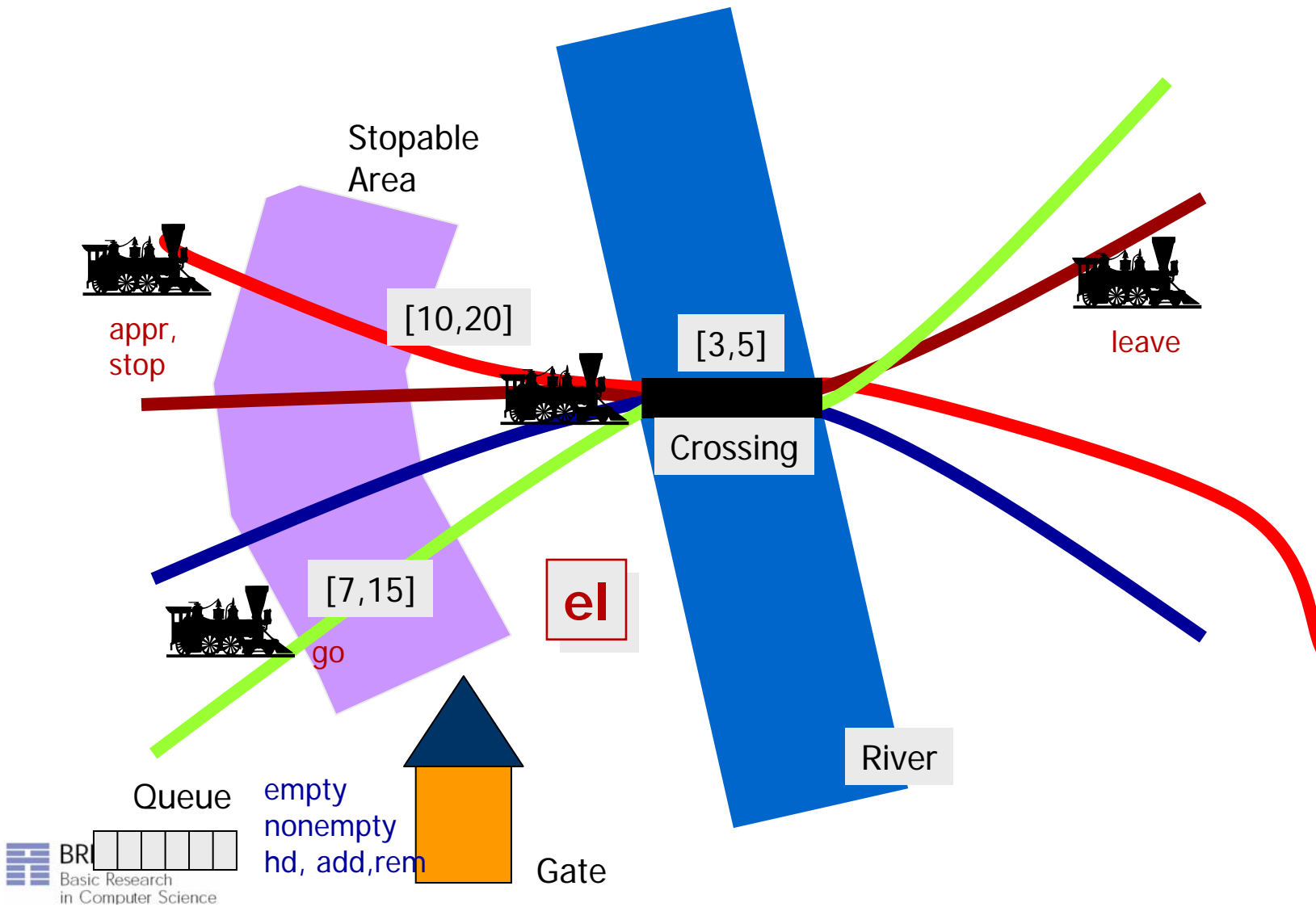
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Train Crossing

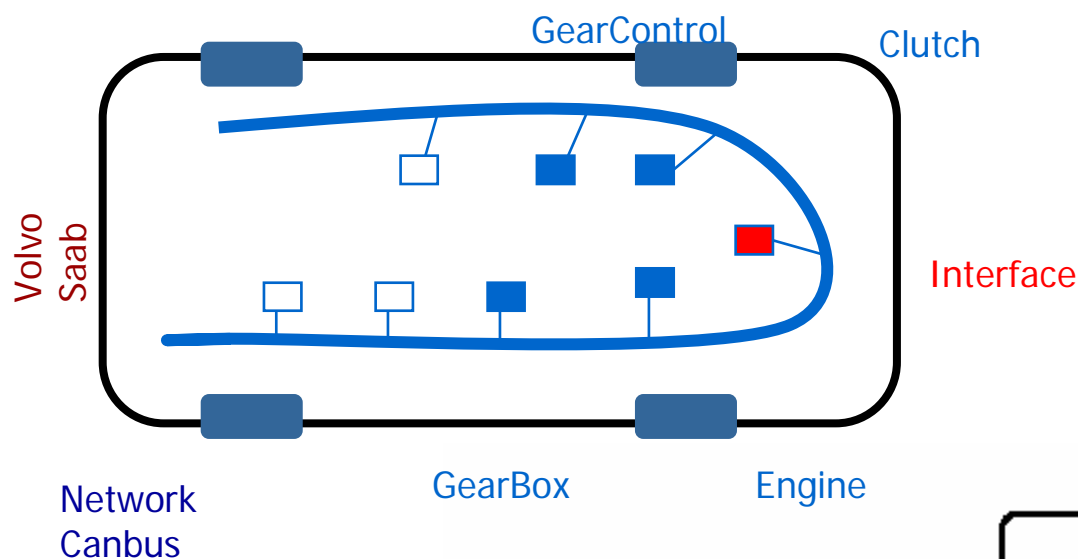
Communication via channels and shared variable.



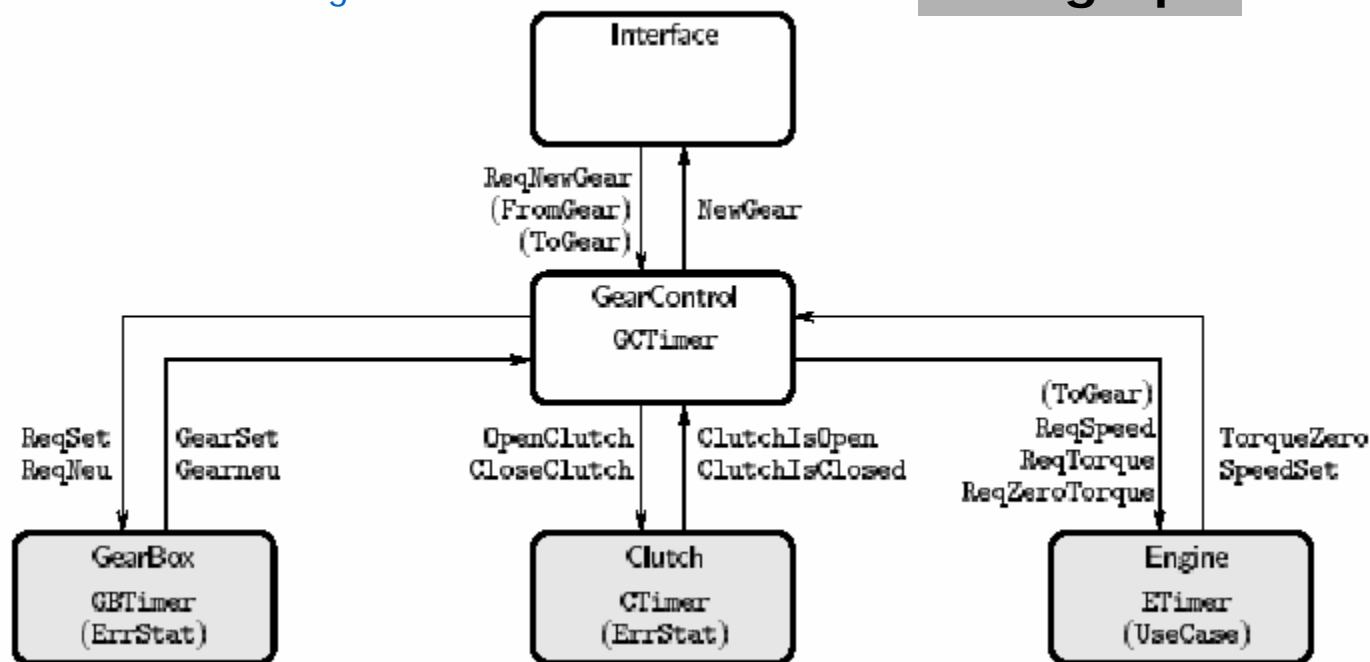
Gear Controller

with MECEL AB

Lindahl, Pettersson, Yi 1998

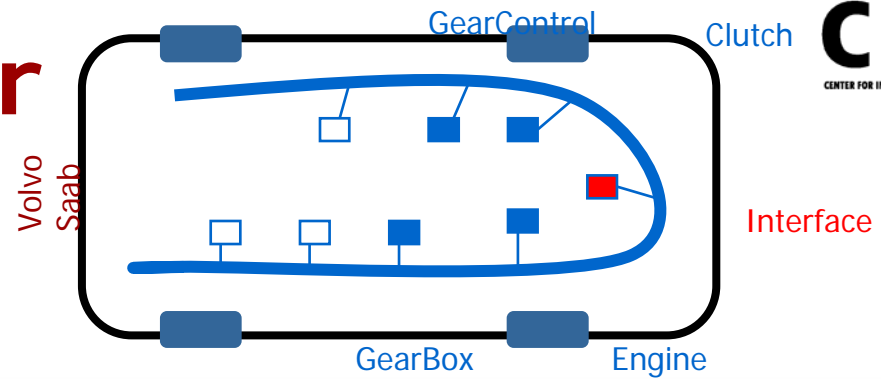


Flowgraph



Gear Controller

with MECEL AB



Requirements

$$\text{GearControl@Initiate} \rightsquigarrow_{\leq 1500} ((\text{ErrStat} = 0) \Rightarrow \text{GearControl@GearChanged}) \quad (1)$$

$$\begin{aligned} &\text{GearControl@Initiate} \rightsquigarrow_{\leq 1000} \\ & \quad ((\text{ErrStat} = 0 \wedge \text{UseCase} = 0) \Rightarrow \text{GearControl@GearChanged}) \end{aligned} \quad (2)$$

$$\text{Clutch@ErrorClose} \rightsquigarrow_{\leq 200} \text{GearControl@CCloseError} \quad (3)$$

$$\text{Clutch@ErrorOpen} \rightsquigarrow_{\leq 200} \text{GearControl@COpenError} \quad (4)$$

$$\text{GearBox@ErrorIdle} \rightsquigarrow_{\leq 350} \text{GearControl@GSetError} \quad (5)$$

$$\text{GearBox@ErrorNeu} \rightsquigarrow_{\leq 200} \text{GearControl@GNeuError} \quad (6)$$

$$\text{Inv} (\text{GearControl@CCloseError} \Rightarrow \text{Clutch@ErrorClose}) \quad (7)$$

$$\text{Inv} (\text{GearControl@COpenError} \Rightarrow \text{Clutch@ErrorOpen}) \quad (8)$$

$$\text{Inv} (\text{GearControl@GSetError} \Rightarrow \text{GearBox@ErrorIdle}) \quad (9)$$

$$\text{Inv} (\text{GearControl@GNeuError} \Rightarrow \text{GearBox@ErrorNeu}) \quad (10)$$

$$\text{Inv} (\text{Engine@ErrorSpeed} \Rightarrow \text{ErrStat} \neq 0) \quad (11)$$

$$\text{Inv} (\text{Engine@Torque} \Rightarrow \text{Clutch@Closed}) \quad (12)$$

$$\bigwedge_{i \in \{R, N, 1, \dots, 5\}} \text{Poss} (\text{Gear@Gear}_i) \quad (13)$$

$$\bigwedge_{i \in \{R, 1, \dots, 5\}} \text{Inv} ((\text{GearControl@Gear} \wedge \text{Gear@Gear}_i) \Rightarrow \text{Engine@Torque}) \quad (14)$$

Case-Studies: Controllers

- Gearbox Controller [TACAS'98]
- Bang & Olufsen Power Controller [RTPS'99, FTRTFT'2k]
- SIDMAR Steel Production Plant [RTCSA'99, DSVV'2k]
- Real-Time RCX Control-Programs [ECRTS'2k]
- Experimental Batch Plant (2000)
- RCX Production Cell (2000)
- Terma, Verification of Memory Management for Radar (2001)
- Scheduling Lacquer Production (2005)
- Memory Arbiter Synthesis and Verification for a Radar Memory Interface Card [NJC'05]

Case Studies: Protocols

- Philips Audio Protocol [HS'95, CAV'95, RTSS'95, CAV'96]
- Collision-Avoidance Protocol [SPIN'95]
- Bounded Retransmission Protocol [TACAS'97]
- Bang & Olufsen Audio/Video Protocol [RTSS'97]
- TDMA Protocol [PRFTS'97]
- Lip-Synchronization Protocol [FMICS'97]
- Multimedia Streams [DSVIS'98]
- ATM ABR Protocol [CAV'99]
- ABB Fieldbus Protocol [ECRTS'2k]
- IEEE 1394 Firewire Root Contention (2000)
- Distributed Agreement Protocol [Formats05]
- **Leader Election for Mobile Ad Hoc Networks [Charme05]**

The UPPAAL Verification Engine



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Overview

- Zones and DBMs
- Minimal Constraint Form
- Clock Difference Diagrams

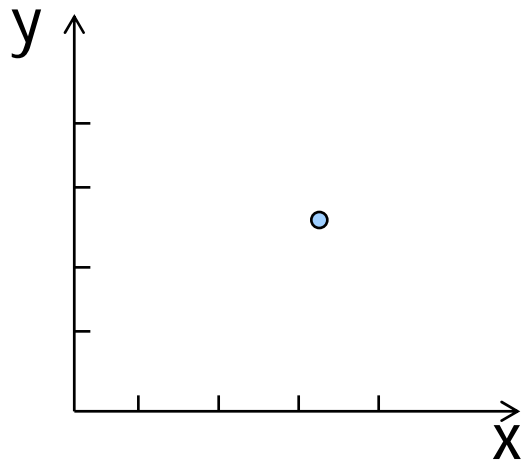
- Distributed UPPAAL [CAV2000, STTT2004]
- Unification & Sharing [FTRTFT2002, SPIN2003]
- Acceleration [FORMATS2002]
- Static Guard Analysis [TACAS2003, TACAS2004]
- Storage-Strategies [CAV2003]

Zones

From infinite to finite

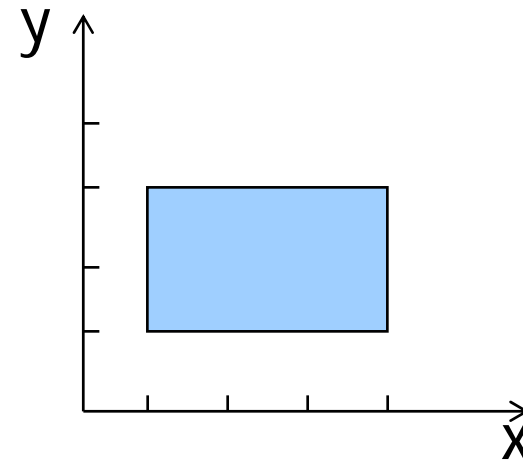
State

$(n, x=3.2, y=2.5)$



Symbolic state (set)

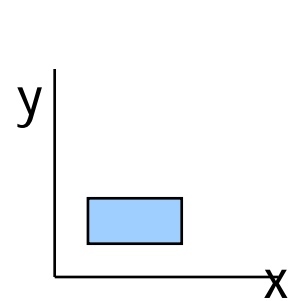
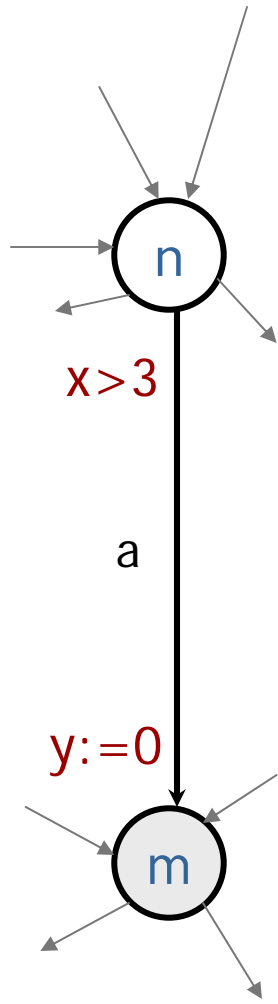
$(n, 1 \leq x \leq 4, 1 \leq y \leq 3)$



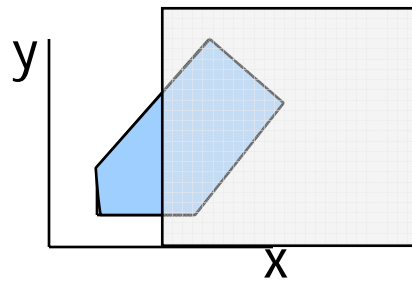
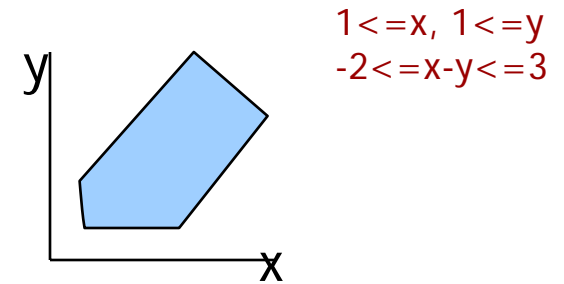
Zone:

conjunction of
 $x - y \leq n, x \leq n$

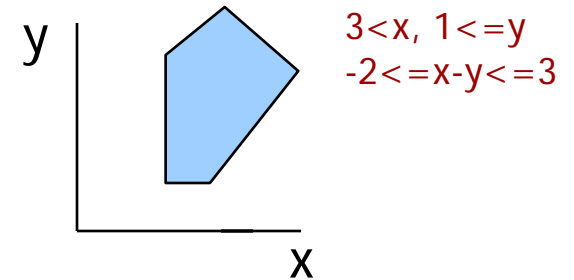
Symbolic Transitions



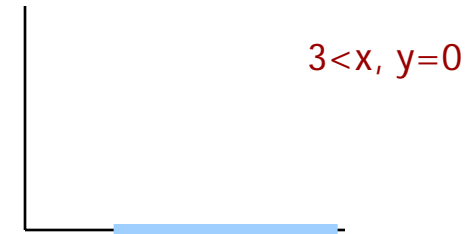
delays to



conjuncts to

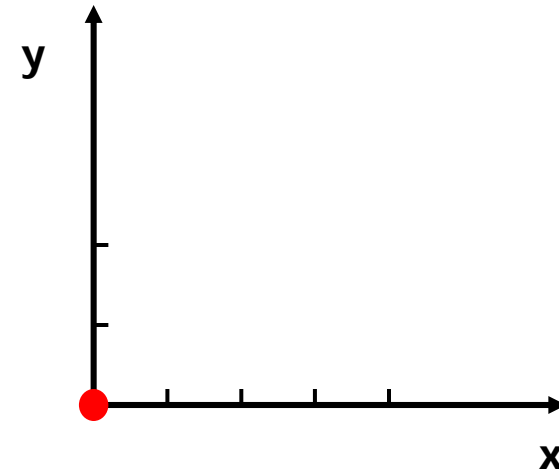
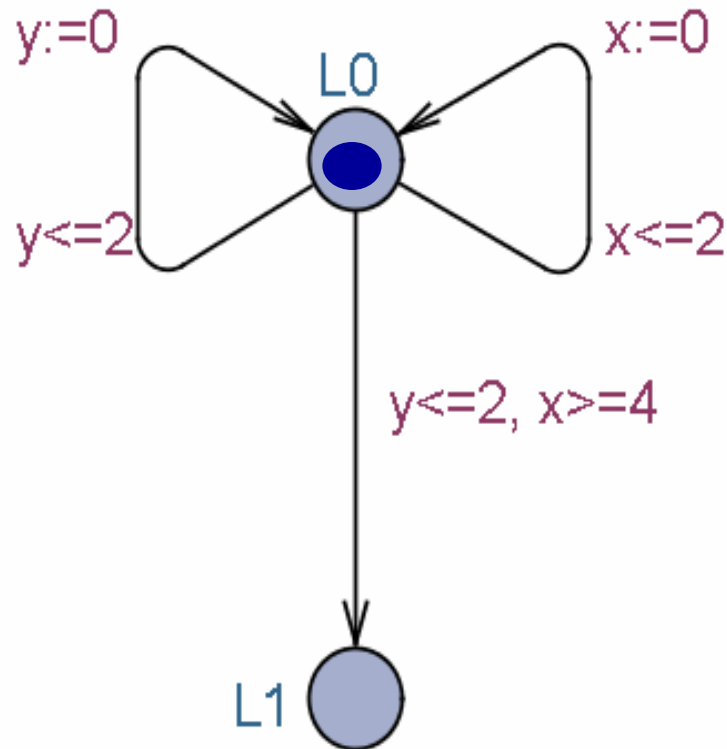


projects to



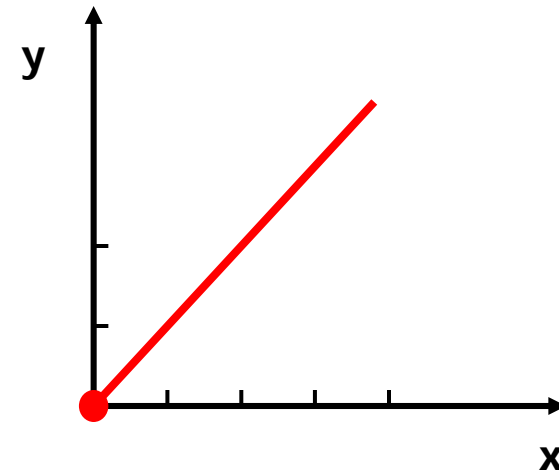
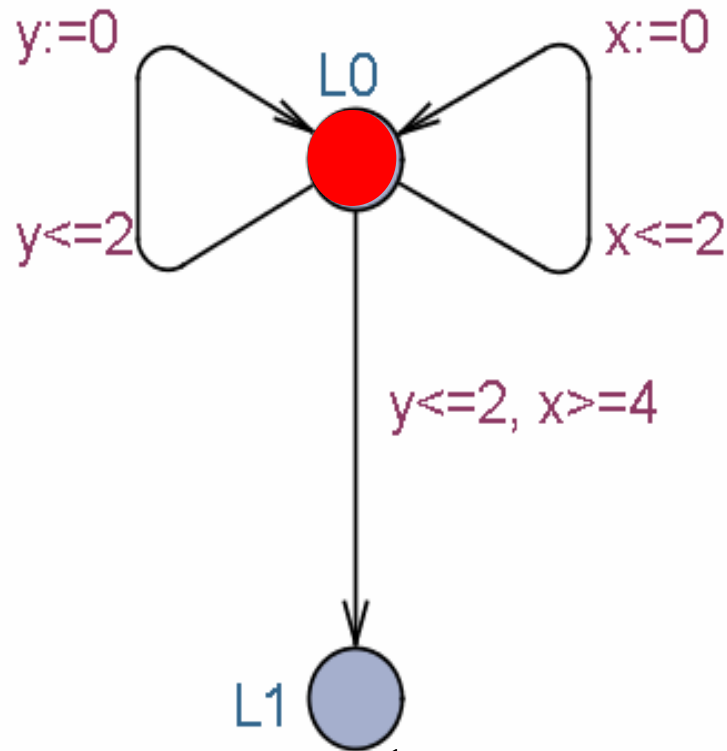
Thus $(n, 1 \leq x \leq 4, 1 \leq y \leq 3) = a \Rightarrow (m, 3 < x, y = 0)$

Symbolic Exploration



Reachable?

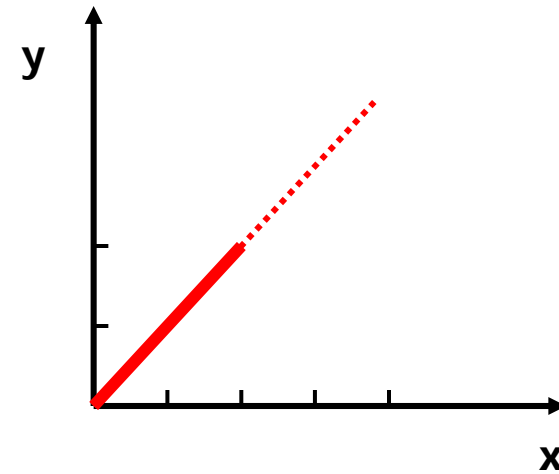
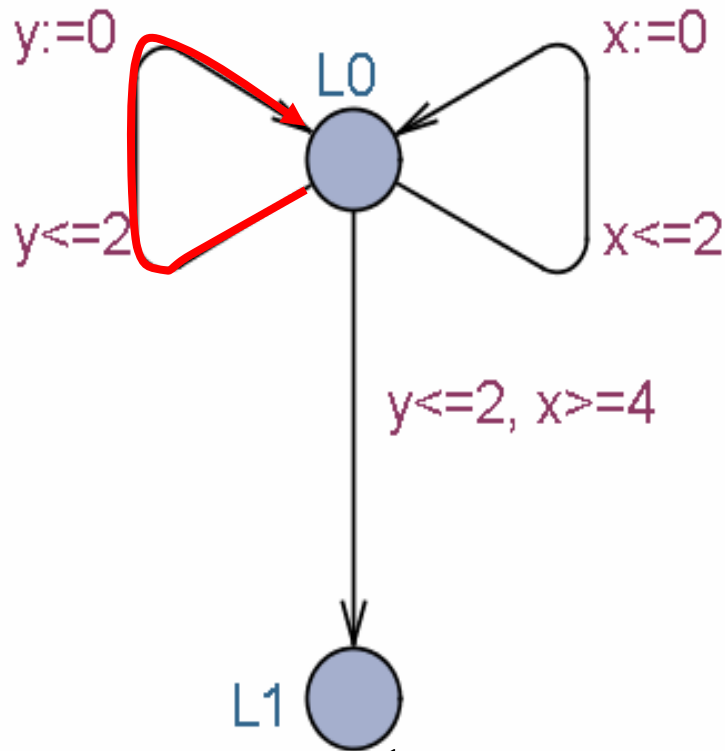
Symbolic Exploration



Delay

Reachable?

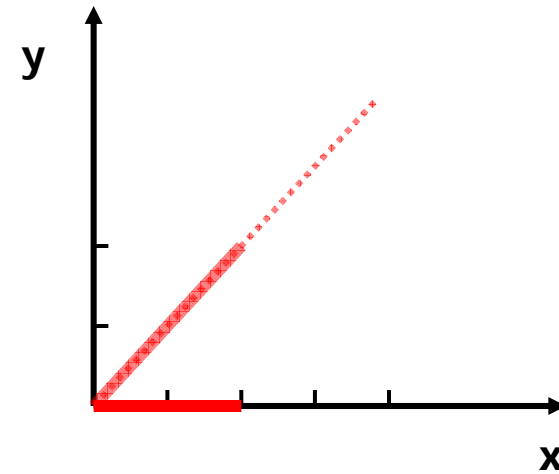
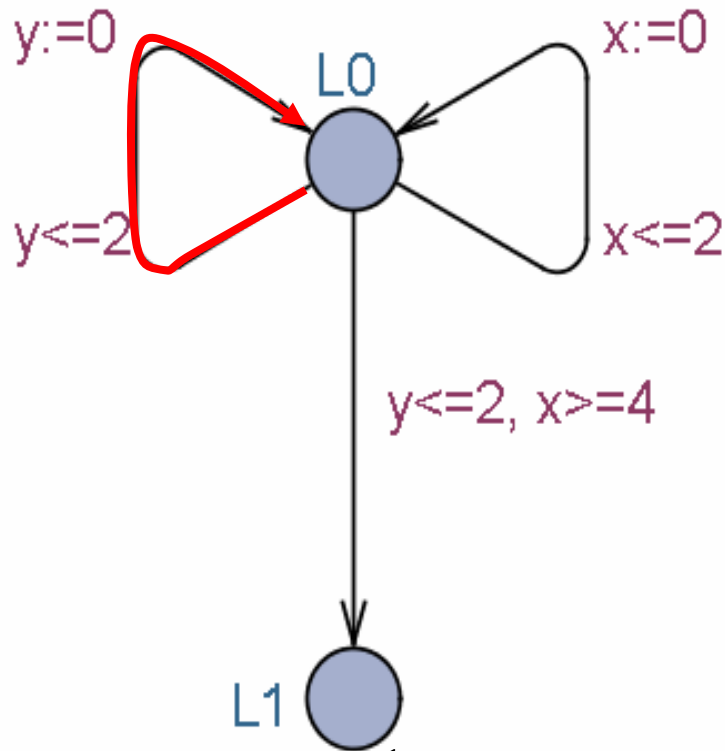
Symbolic Exploration



Left

Reachable?

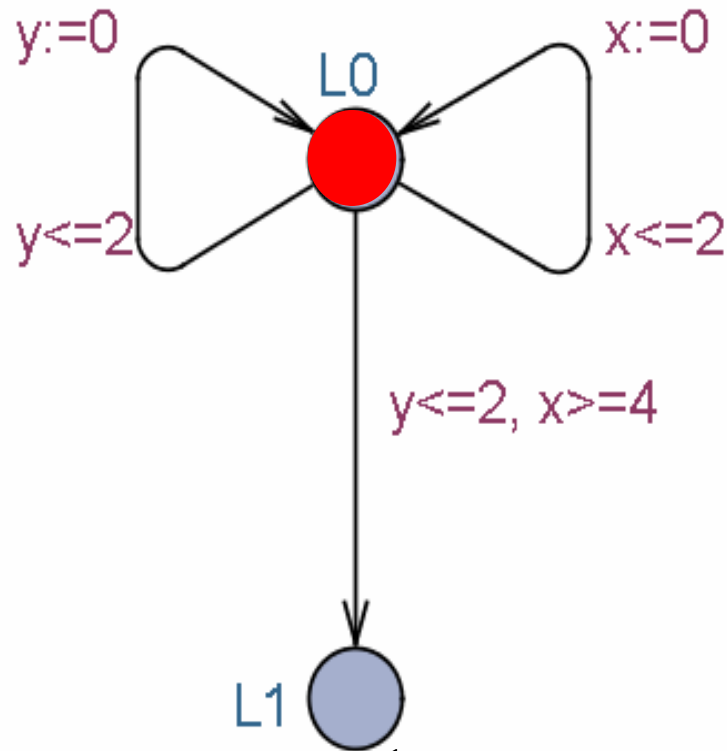
Symbolic Exploration



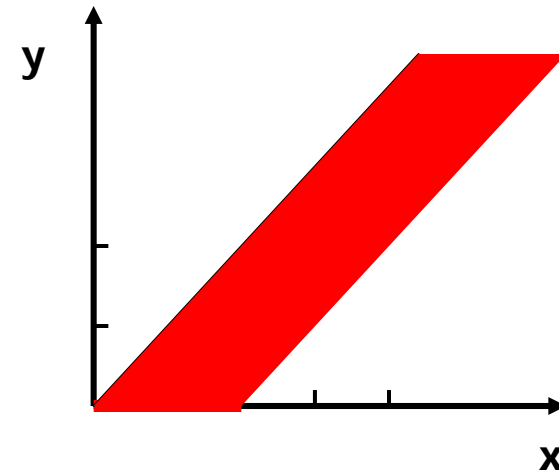
Left

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Symbolic Exploration

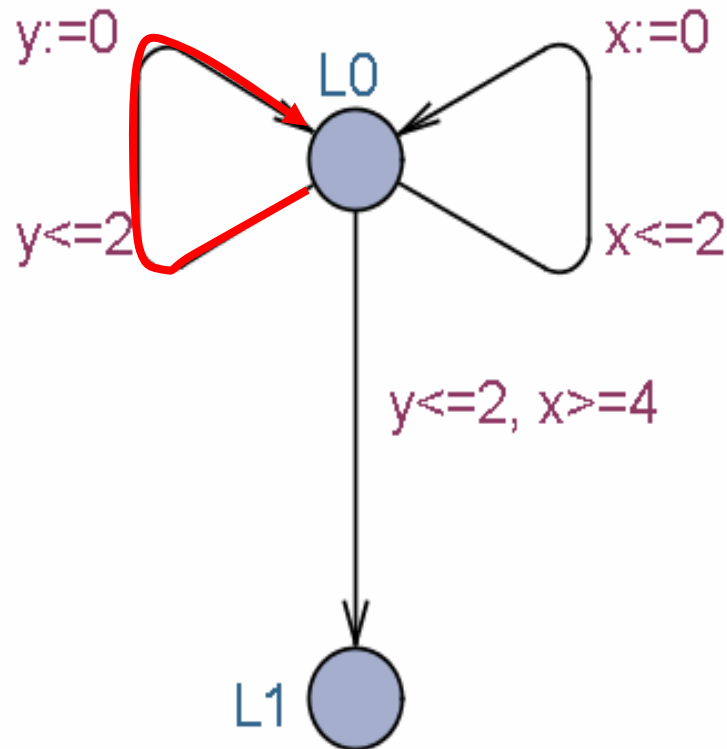


Reachable?

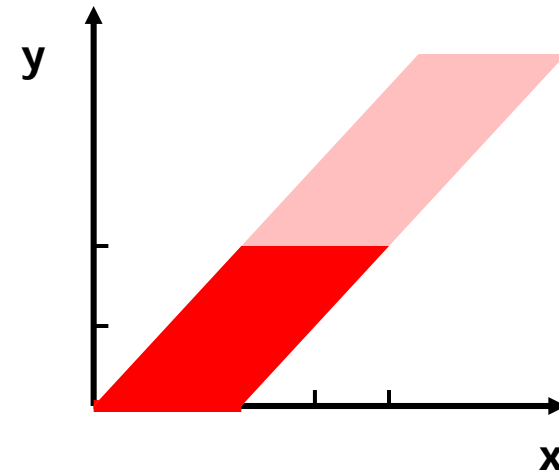


Delay

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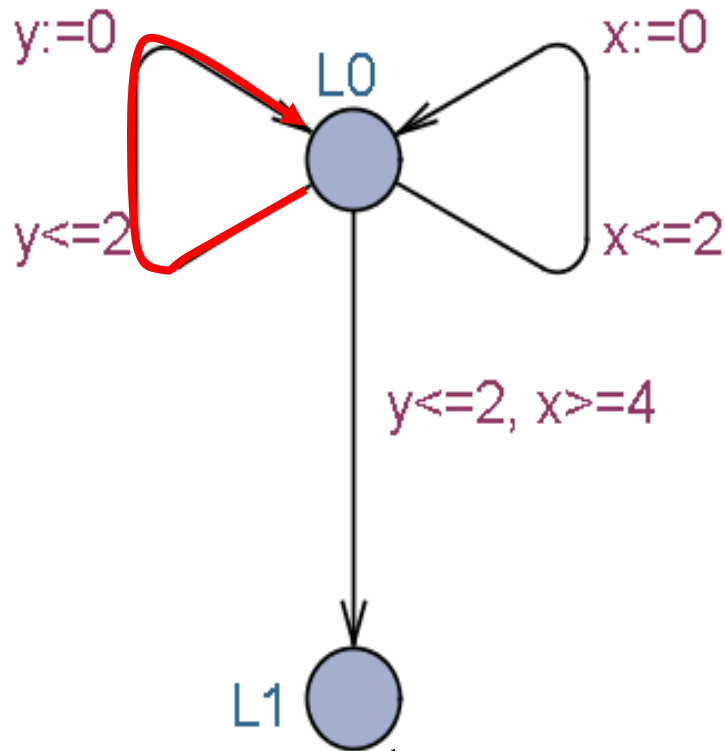


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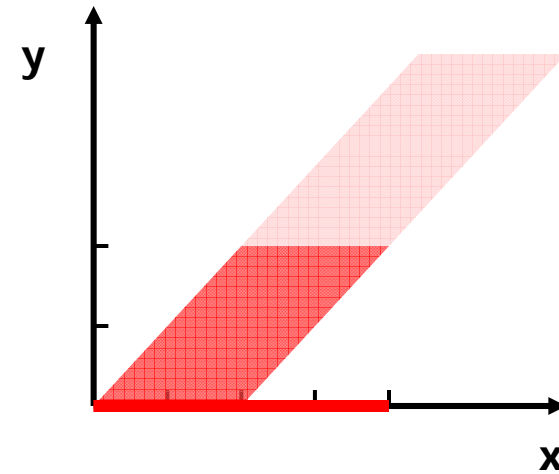


Left

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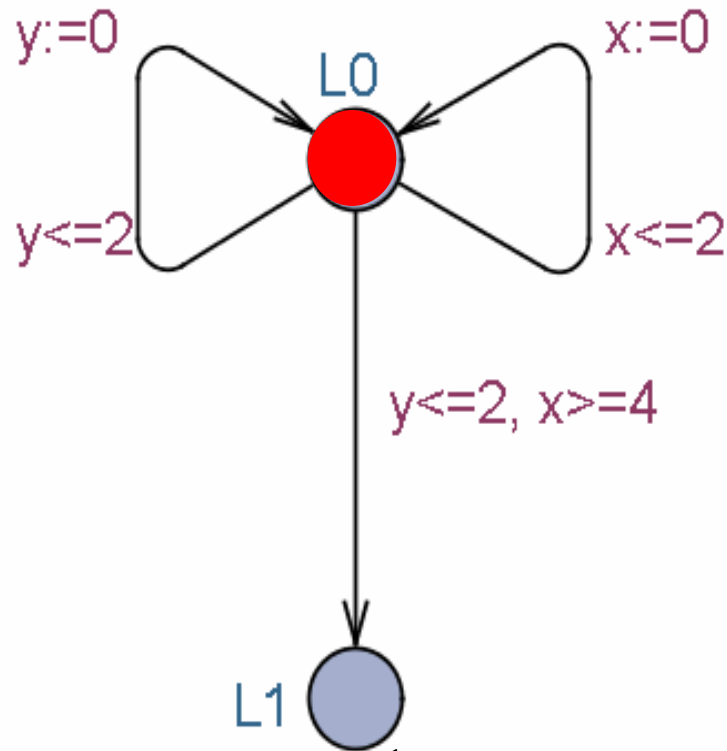


Reachable?

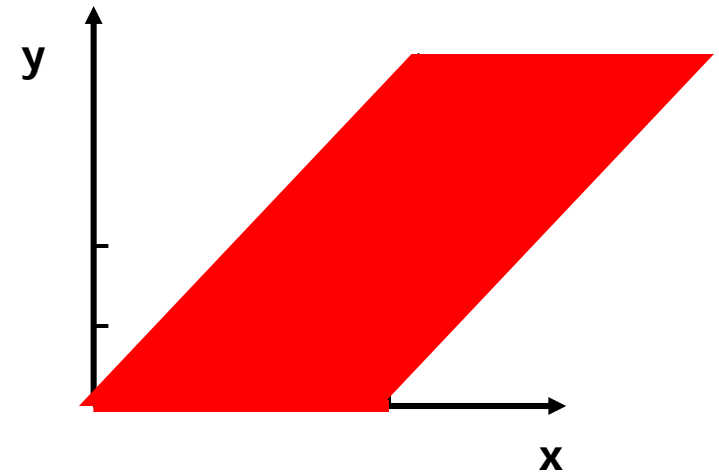


Left

Symbolic Exploration

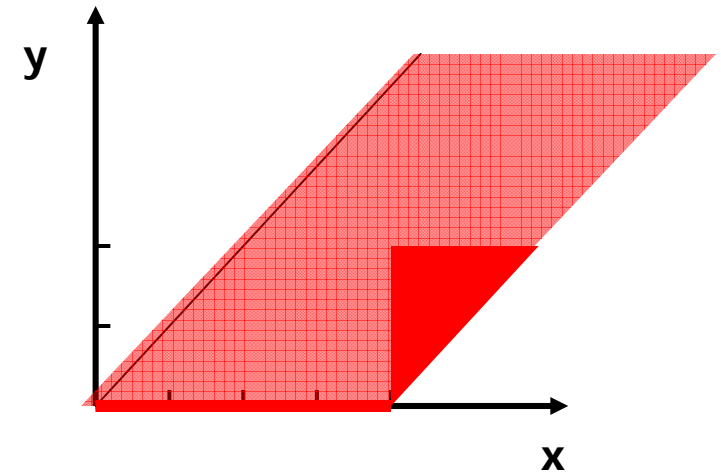
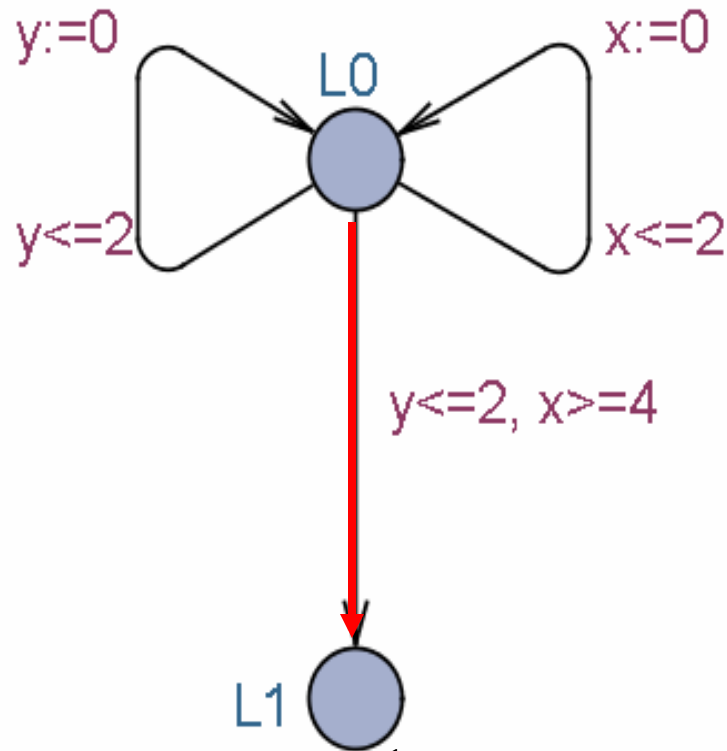


Reachable?



Delay

Symbolic Exploration

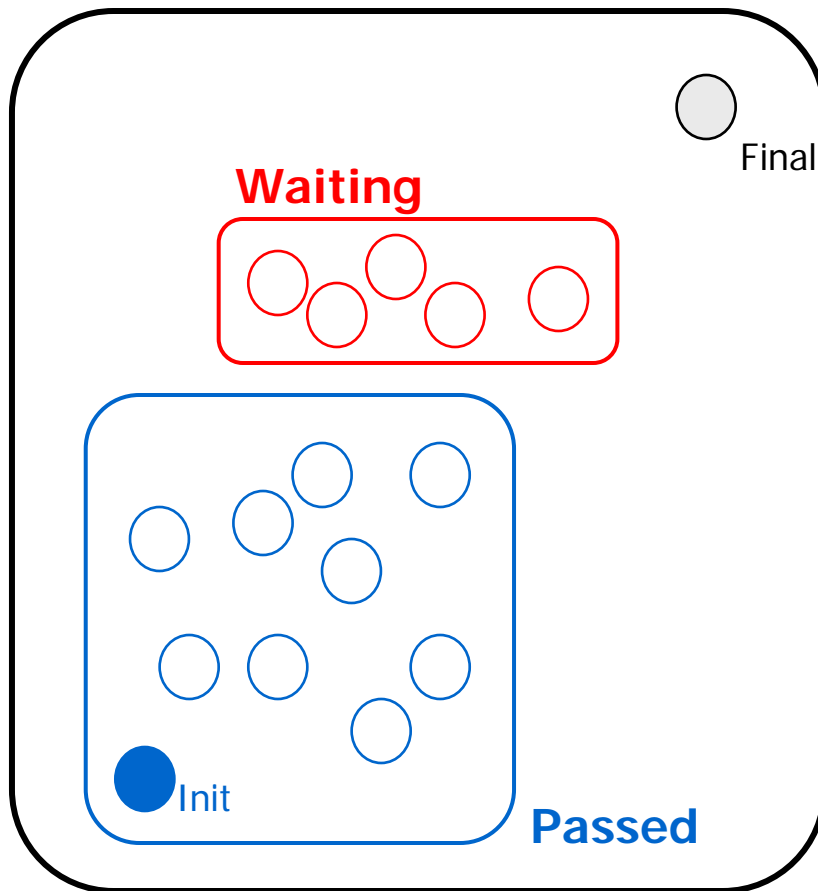


Down

Reachable?

Forward Reachability

Init -> Final ?



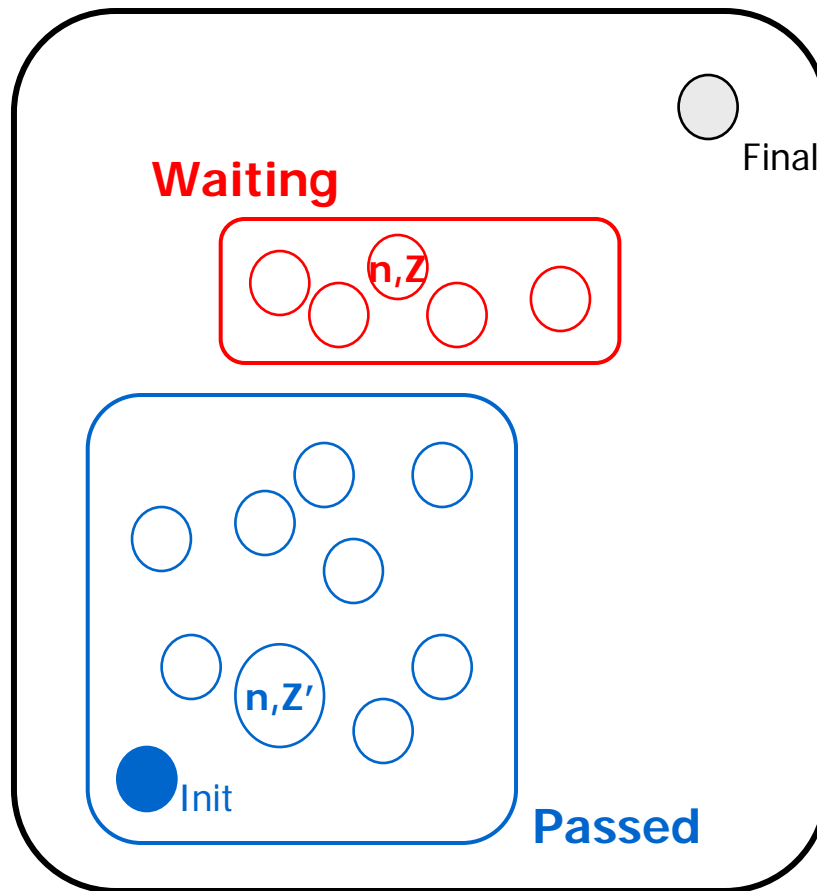
INITIAL Passed := \emptyset ;
 Waiting := $\{(n0, Z0)\}$

REPEAT

UNTIL Waiting = \emptyset
 or
 Final is in **Waiting**

Forward Reachability

Init \rightarrow Final ?



INITIAL **Passed** := \emptyset ;
Waiting := $\{(n_0, Z_0)\}$

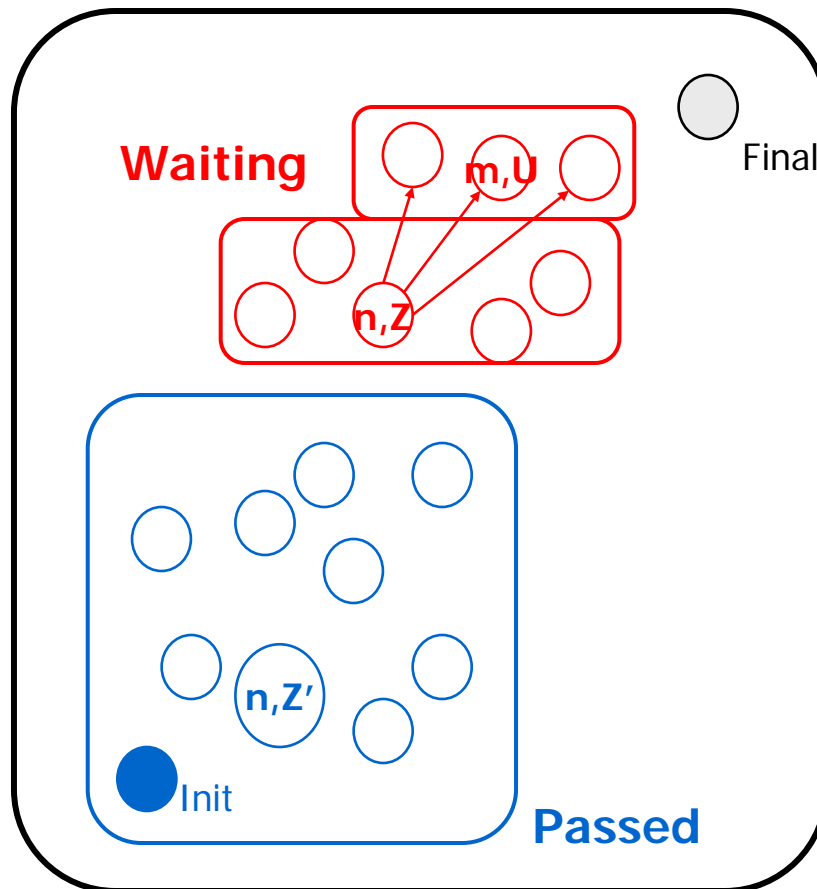
REPEAT

- pick (n, Z) in **Waiting**
- **if** for some $Z' \supseteq Z$
 (n, Z') in **Passed** then **STOP**

UNTIL **Waiting** = \emptyset
 or
 Final is in **Waiting**

Forward Reachability

Init \rightarrow Final ?



INITIAL **Passed** := \emptyset ;
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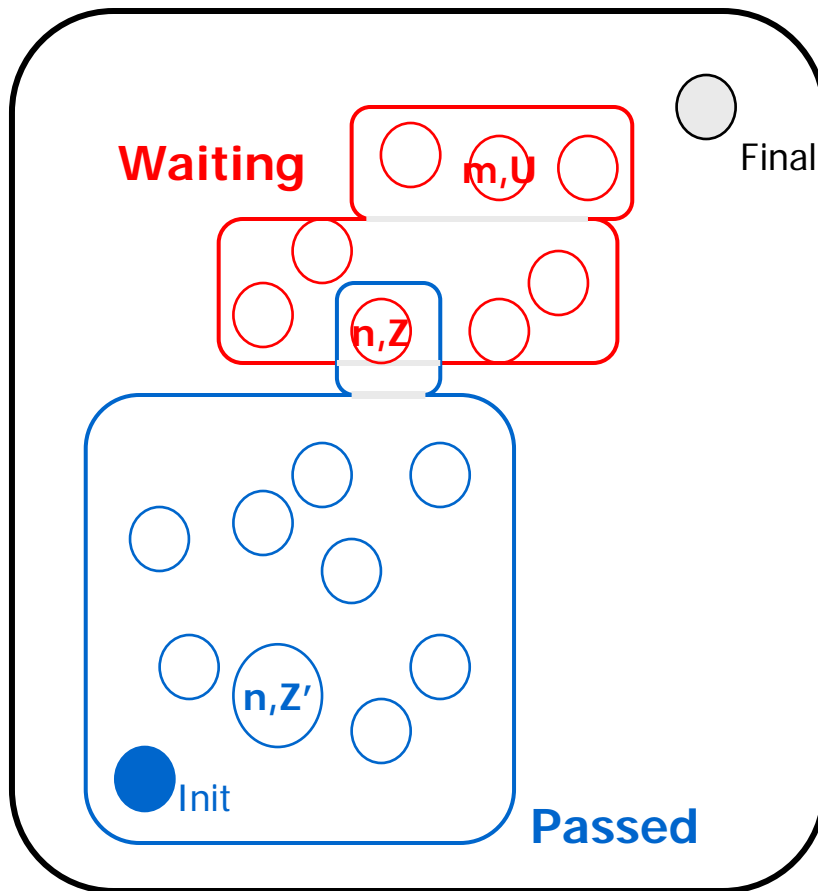
REPEAT

- pick (n, Z) in **Waiting**
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- **else** /explore/ add
 $\{(m, U) : (n, Z) \Rightarrow (m, U)\}$
to **Waiting**;

UNTIL **Waiting** = \emptyset
or
Final is in **Waiting**

Forward Reachability

Init \rightarrow Final ?



INITIAL **Passed** := \emptyset ;
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REPEAT

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to **Waiting**;
Add (n, Z) to **Passed**

UNTIL **Waiting** = \emptyset
or
Final is in **Waiting**

Zones

Difference Bounded Matrices

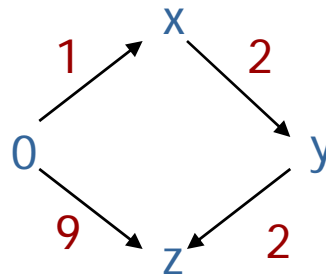
Bellman 1958, Dill 1989

Inclusion

D1

$$\begin{array}{l} x \leq 1 \\ y - x \leq 2 \\ z - y \leq 2 \\ z \leq 9 \end{array}$$

Graph

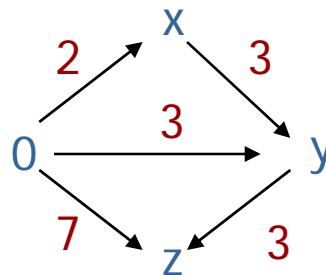


? \subseteq ?

D2

$$\begin{array}{l} x \leq 2 \\ y - x \leq 3 \\ y \leq 3 \\ z - y \leq 3 \\ z \leq 7 \end{array}$$

Graph



Zones

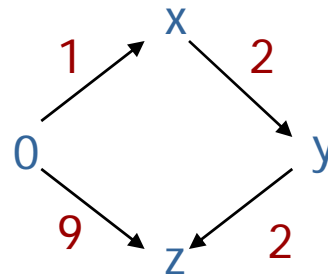
Difference Bounded Matrices

Inclusion

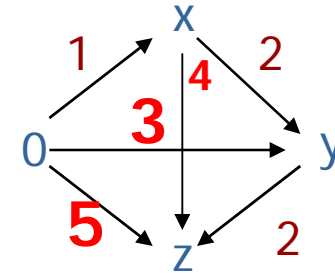
D1

$$\begin{aligned} x &\leq 1 \\ y - x &\leq 2 \\ z - y &\leq 2 \\ z &\leq 9 \end{aligned}$$

Graph



Shortest
Path
Closure

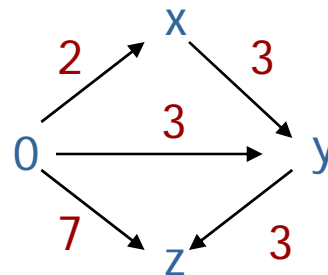


$$? \subseteq ?$$

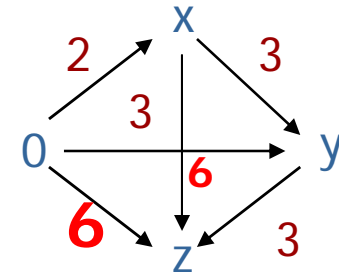
D2

$$\begin{aligned} x &\leq 2 \\ y - x &\leq 3 \\ y &\leq 3 \\ z - y &\leq 3 \\ z &\leq 7 \end{aligned}$$

Graph



Shortest
Path
Closure



Zones

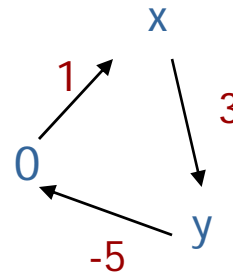
Difference Bounded Matrices

Emptiness

D

$$\begin{array}{l} x \leq 1 \\ y \geq 5 \\ y - x \leq 3 \end{array}$$

Graph



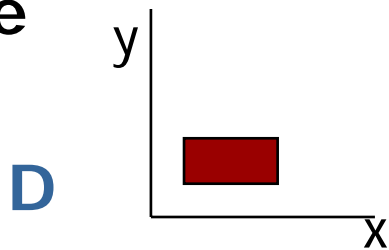
Negative Cycle
iff
empty solution set

Compact

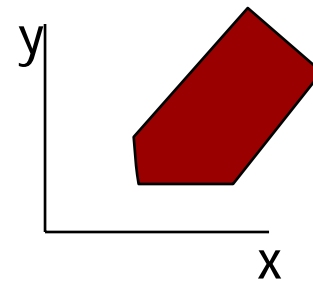
Zones

Difference Bounded Matrices

Future

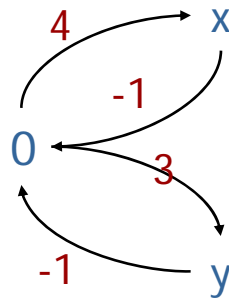


$$\begin{aligned} 1 \leq x \leq 4 \\ 1 \leq y \leq 3 \end{aligned}$$

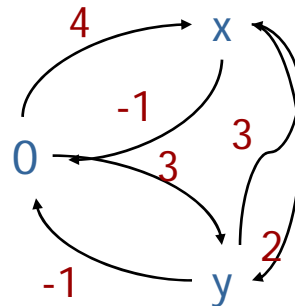


Future D

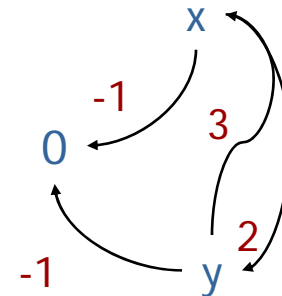
$$\begin{aligned} 1 \leq x, 1 \leq y \\ -2 \leq x - y \leq 3 \end{aligned}$$



Shortest Path Closure



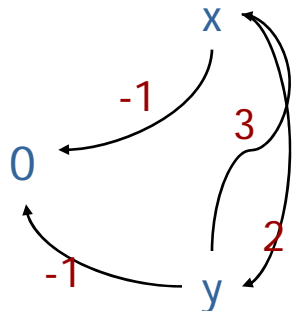
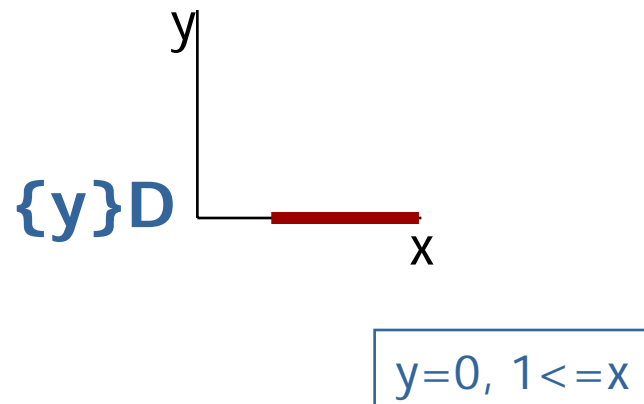
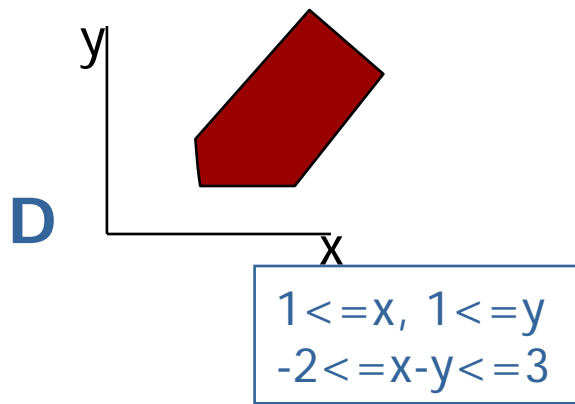
Remove upper bounds on clocks



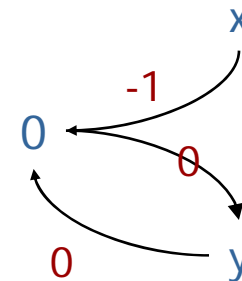
Zones

Difference Bounded Matrices

Reset



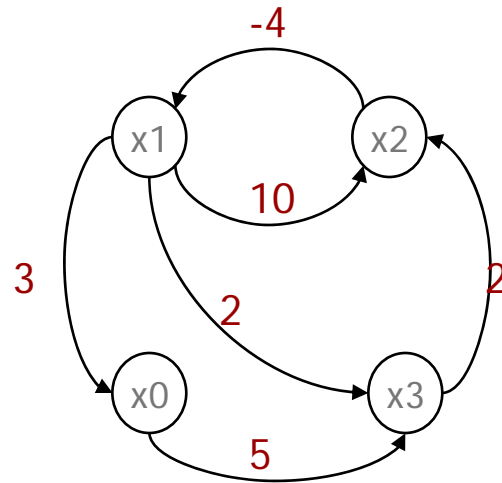
Remove all bounds involving y and set y to 0



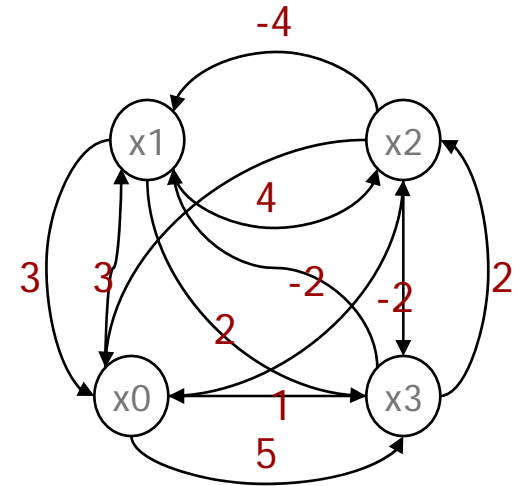
Zones

Difference Bounded Matrices

$x_1 - x_2 \leq 4$
 $x_2 - x_1 \leq 10$
 $x_3 - x_1 \leq 2$
 $x_2 - x_3 \leq 2$
 $x_0 - x_1 \leq 3$
 $x_3 - x_0 \leq 5$



**Shortest
Path
Closure
 $O(n^3)$**

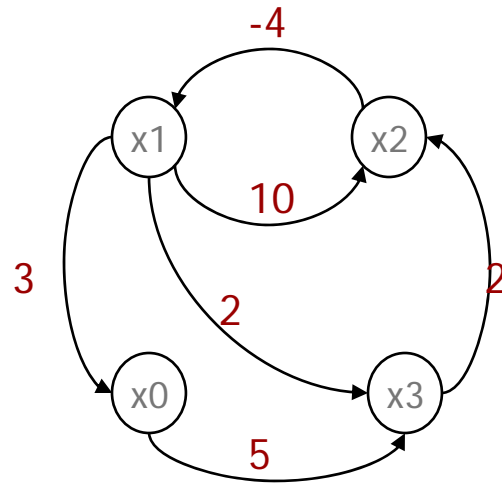


Zones

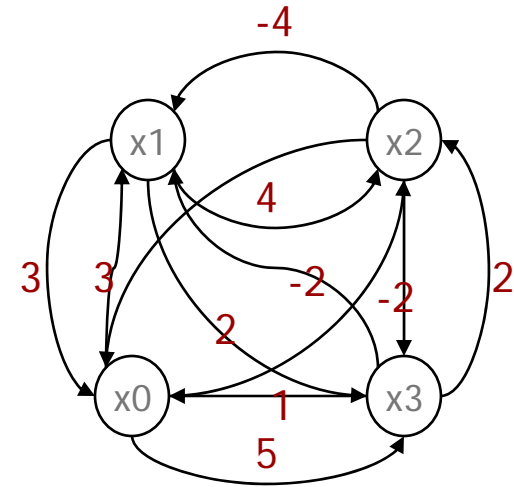
Minimal Constraint Form

RTSS 1997

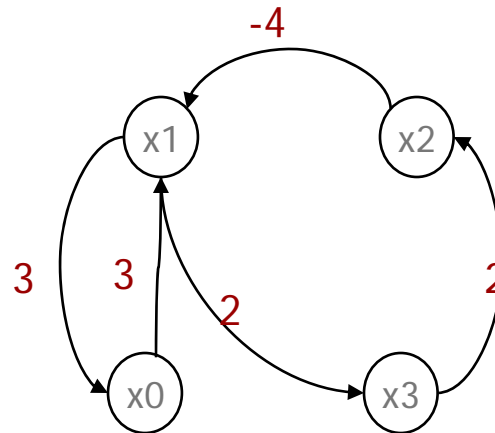
$x_1 - x_2 \leq 4$
 $x_2 - x_1 \leq 10$
 $x_3 - x_1 \leq 2$
 $x_2 - x_3 \leq 2$
 $x_0 - x_1 \leq 3$
 $x_3 - x_0 \leq 5$



Shortest Path Closure
 $O(n^3)$

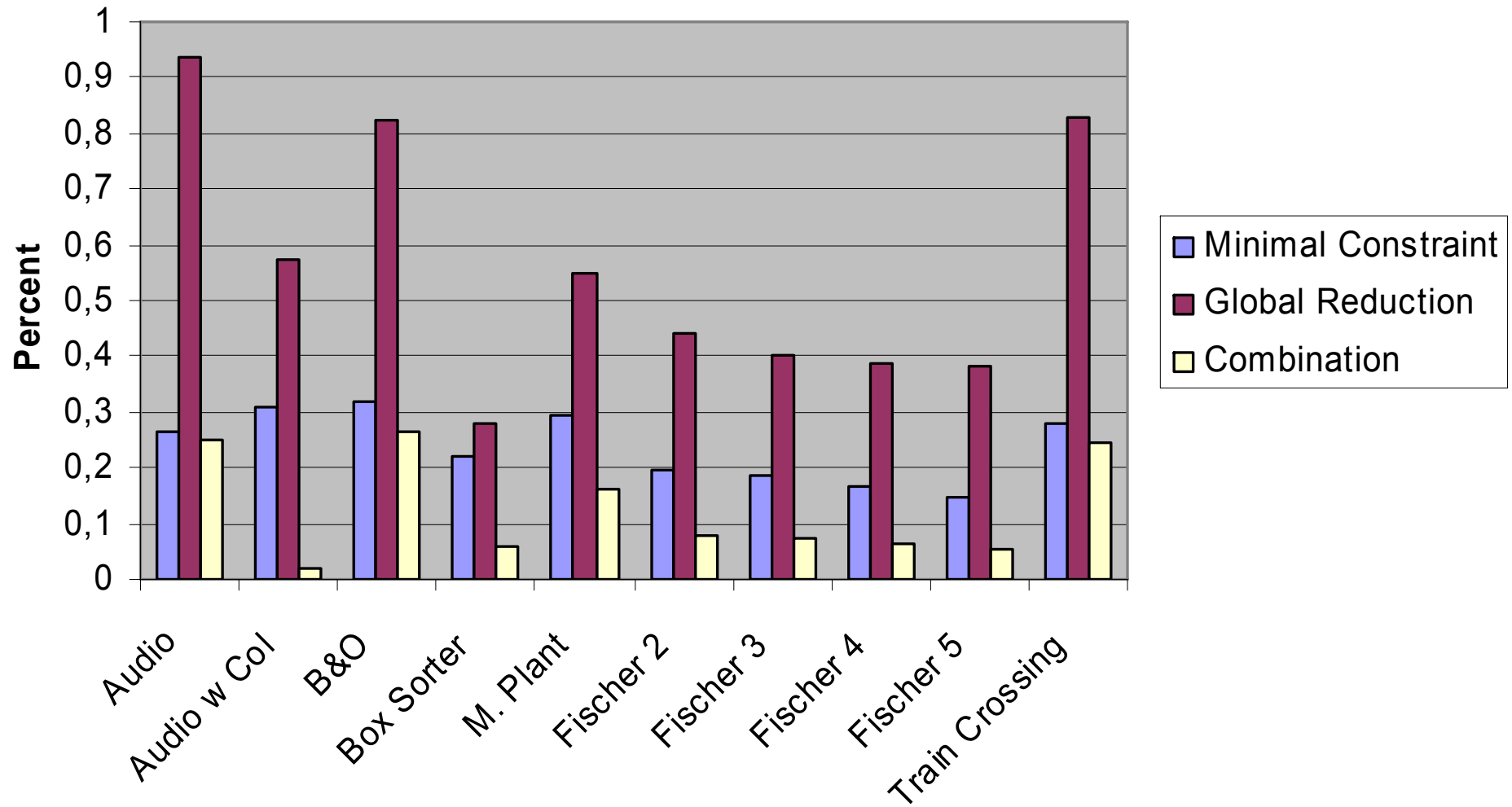


Shortest Path Reduction
 $O(n^3)$

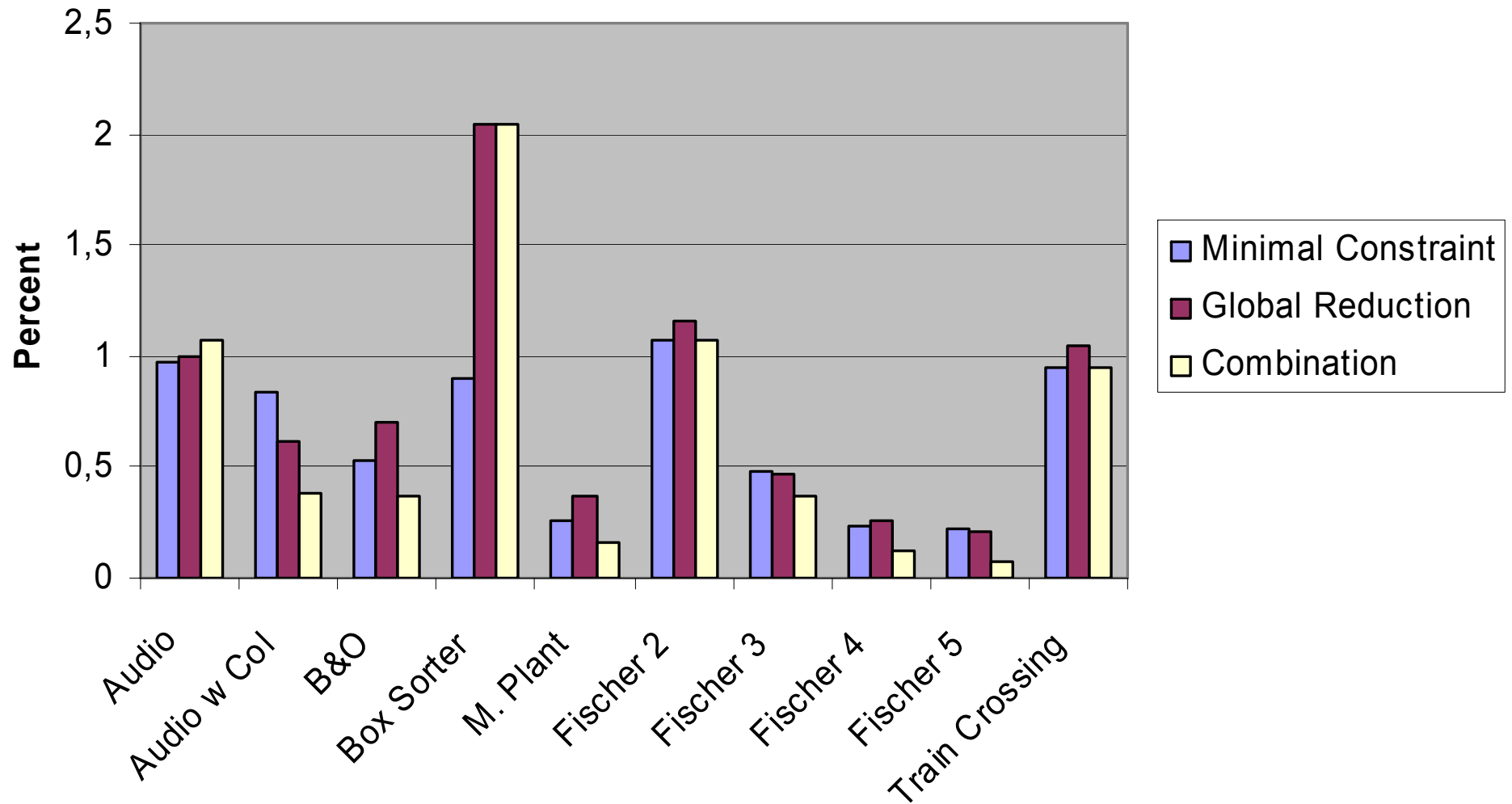


Space worst $O(n^2)$
practice $O(n)$

SPACE PERFORMANCE

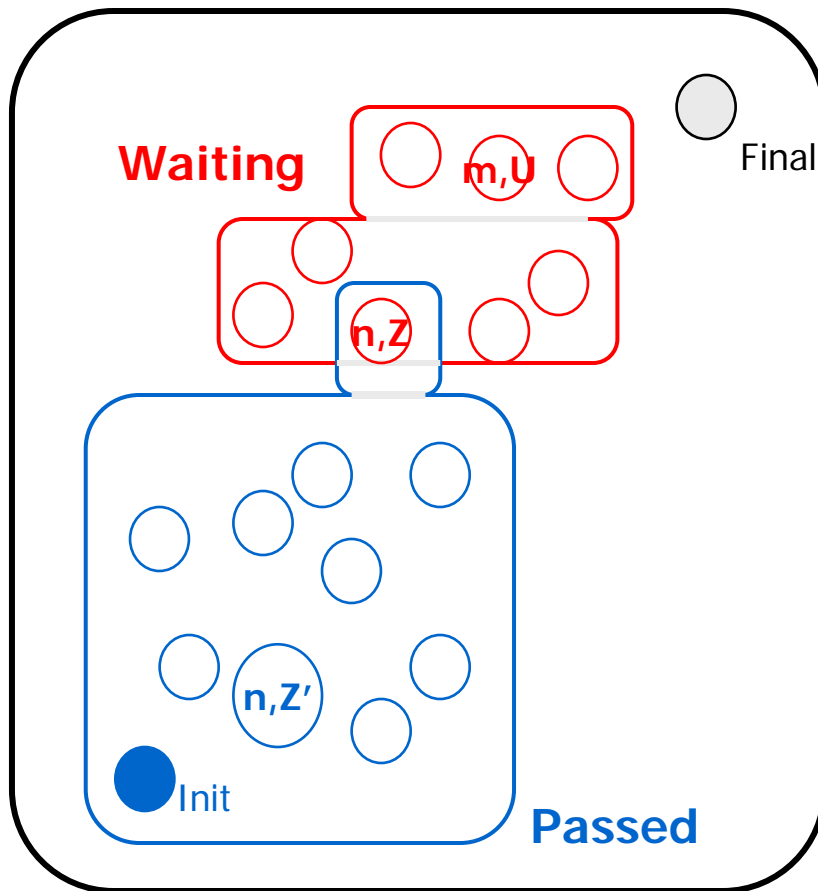


TIME PERFORMANCE



Earlier Termination

Init \rightarrow Final ?



INITIAL **Passed** := \emptyset ;
Waiting := $\{(n_0, Z_0)\}$

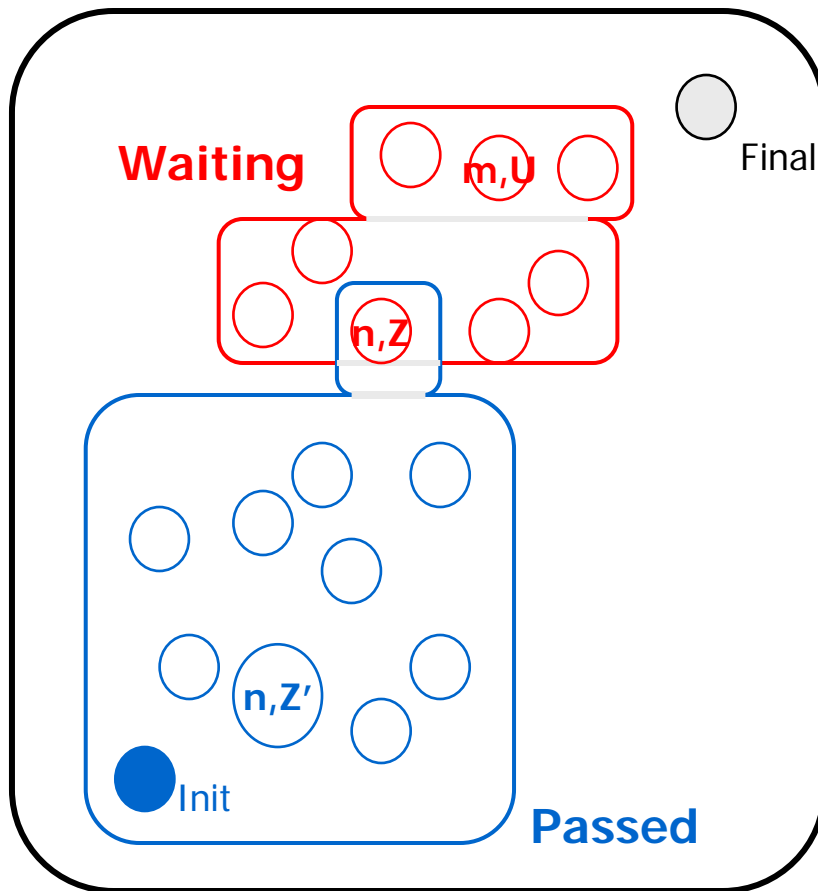
REPEAT

- pick (n, Z) in **Waiting**
- **if** for some $Z' \supseteq Z$
 (n, Z') in **Passed** then **STOP**
- **else** /explore/ add
 $\{(m, U) : (n, Z) \Rightarrow (m, U)\}$
to **Waiting**;
Add (n, Z) to **Passed**

UNTIL **Waiting** = \emptyset
or
Final is in **Waiting**

Earlier Termination

Init \rightarrow Final ?



INITIAL **Passed** := \emptyset ;
Waiting := $\{(n_0, Z_0)\}$

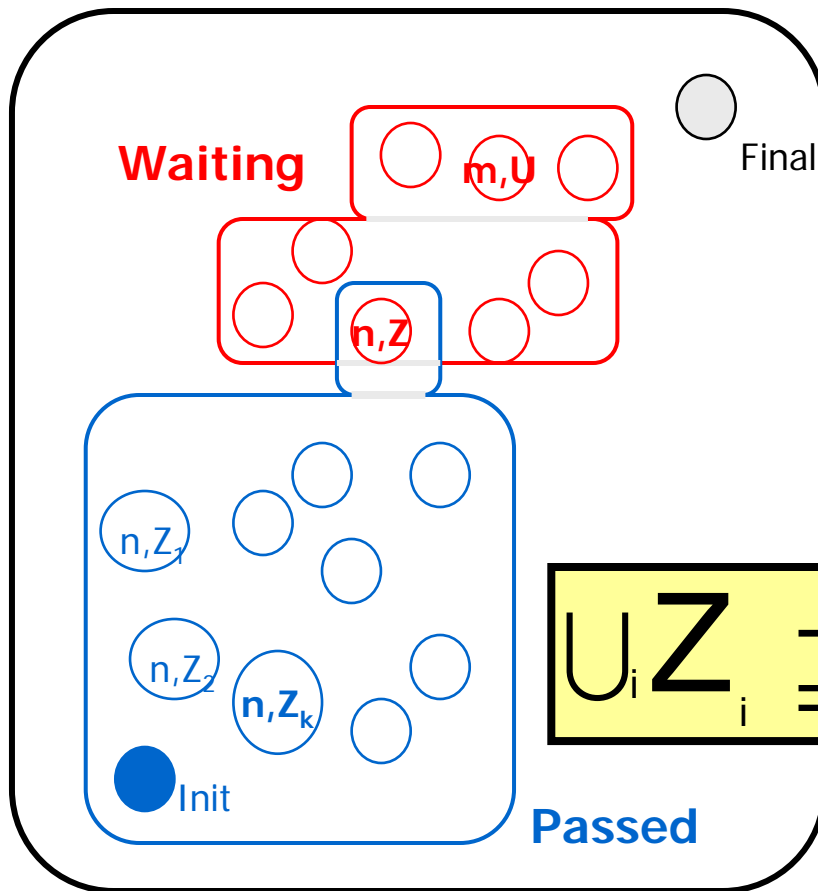
REPEAT

- pick (n, Z) in **Waiting**
- **if** for some $Z' \supseteq Z$
 (n, Z') in **Passed** then **STOP**
- **else** /explore/ add
 $\{(m, U) : (n, Z) \Rightarrow (m, U)\}$
to **Waiting**;
Add (n, Z) to **Passed**

UNTIL **Waiting** = \emptyset
or
Final is in **Waiting**

Earlier Termination

Init \rightarrow Final ?



```
INITIAL Passed := ∅;
       Waiting := {(n0, Z0)}
```

REPEAT

- pick (n, Z) in **Waiting**

- if for some $Z' \supseteq Z$
 (n, Z') in **Passed** then STOP

- else explore/ add
 $\{ (m, U) : (n, Z) \Rightarrow (m, U) \}$
 to **Waiting**;

Add (n, Z) to **Passed**

UNTIL **Waiting** = ∅

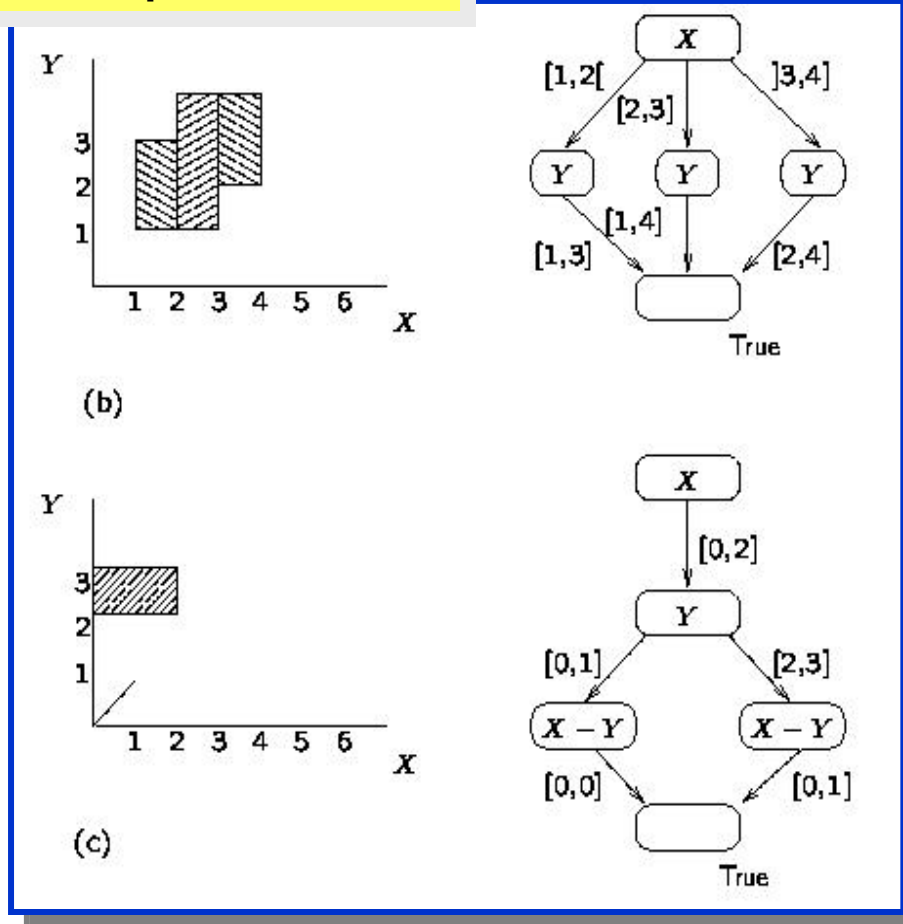
or

Final is in **Waiting**

Clock Difference Diagrams

= Binary Decision Diagrams + Difference Bounded Matrices CAV99

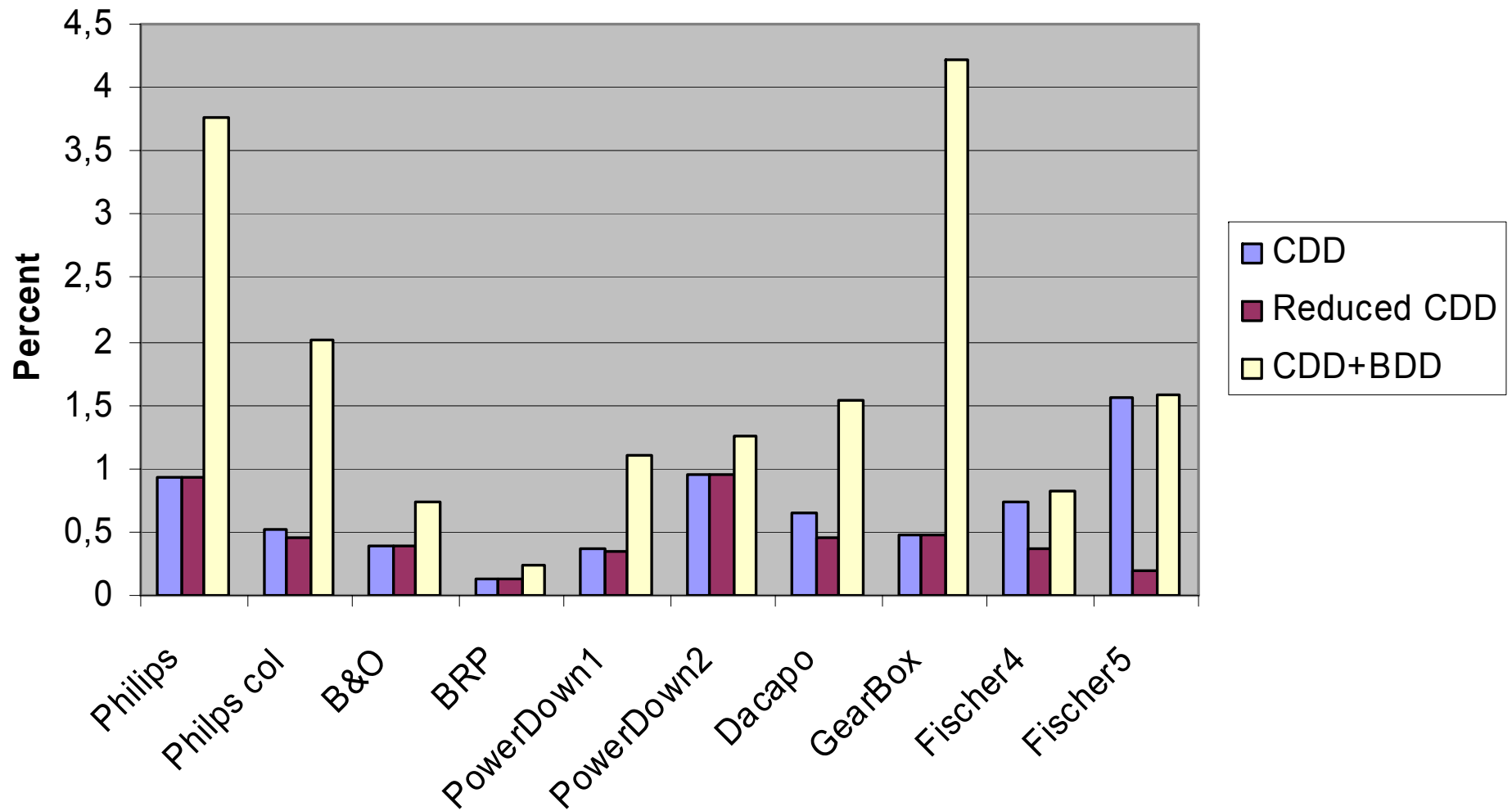
CDD-representations



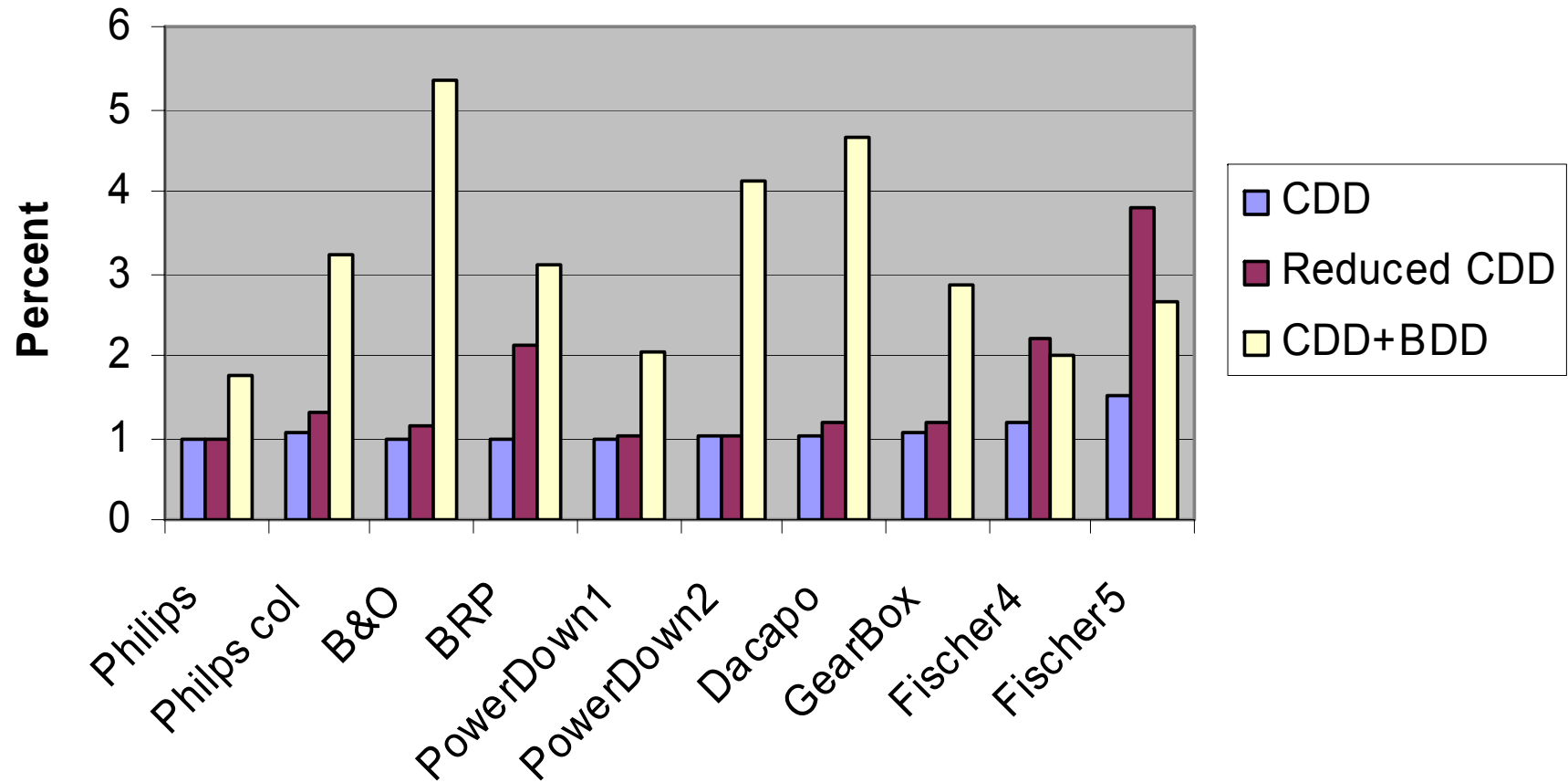
- Nodes labeled with differences
- Maximal sharing of substructures (also across different CDDs)
- Maximal intervals
- Linear-time algorithms for set-theoretic operations.

- NDD's [Maler et. al](#)
- DDD's [Møller, Lichtenberg](#)

SPACE PERFORMANCE



TIME PERFORMANCE



Verification Options

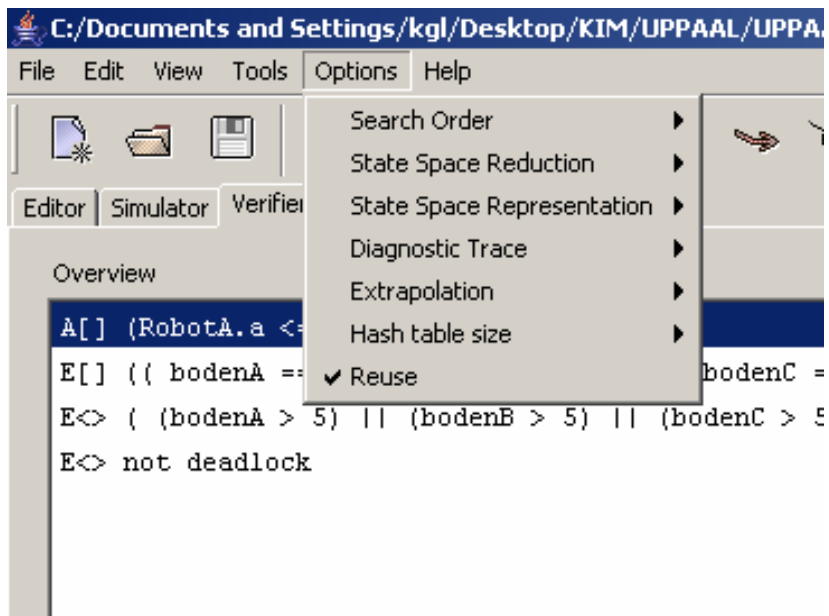


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Verification Options



Search Order

- Depth First
- Breadth First

State Space Reduction

- None
- Conservative
- Aggressive

State Space Representation

- DBM
- Compact Form
- Under Approximation
- Over Approximation

Diagnostic Trace

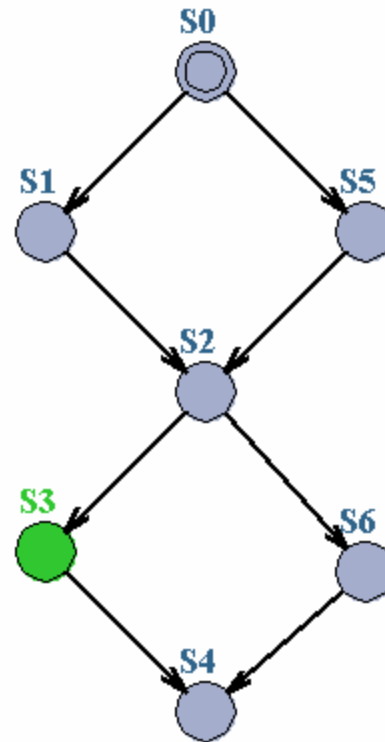
- Some
- Shortest
- Fastest

Extrapolation

Hash Table size

Reuse

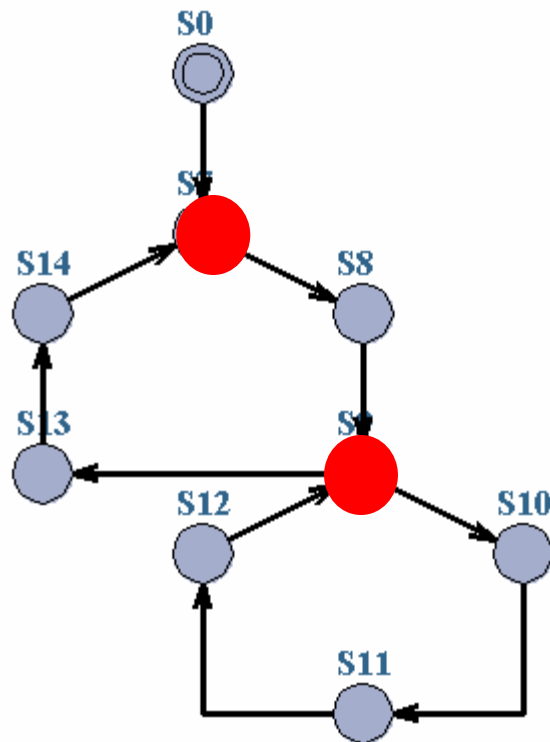
State Space Reduction



However,
Passed list useful for
 efficiency

No Cycles: **Passed** list not needed for *termination*

State Space Reduction



Cycles:

Only symbolic states involving loop-entry points need to be saved on **Passed** list

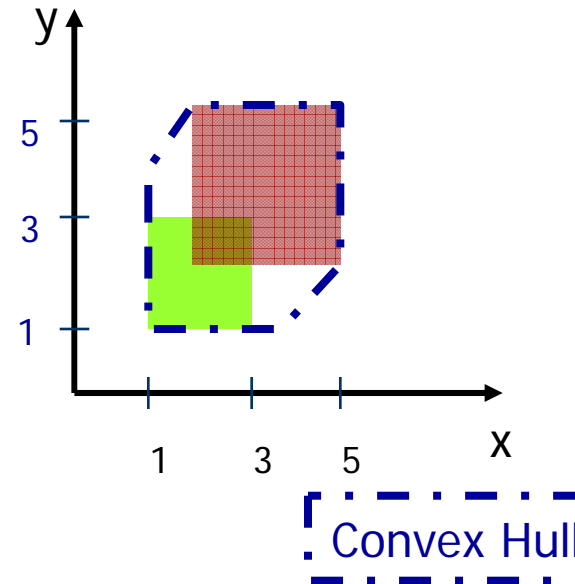
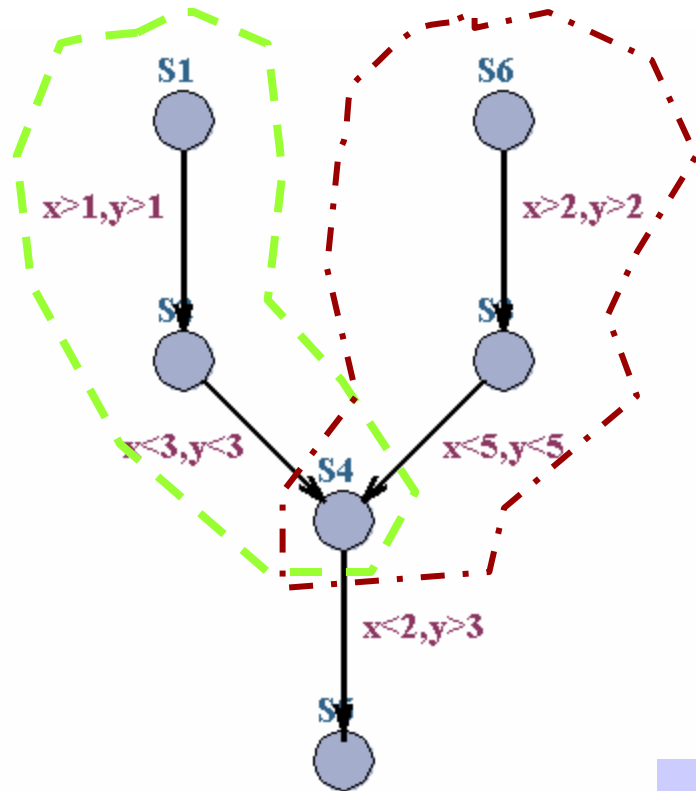
To Store or Not to Store

Behrmann, Larsen,
Pelanek 2003

	entry points	covering set	successors	random $p = 0.1$	distance $k = 10$	combination $k = 3$
Fischer 3,077	27.1% 1.00	42.1% 1.66	47.9% 1.00	53.7% 4.51	67.6% 2.76	56.9% 6.57
BRP 6,060	70.5% 1.01	16.5% 1.20	19.8% 1.03	18.3% 1.78	15.8% 1.34	7.6% 1.68
Token Ring 15,103	33.0% 1.16	10.3% 1.46	20.7% 1.03	17.2% 1.63	17.5% 1.43	16.8% 7.40
Train-gate 16,666	71.1% 1.22	27.4% 1.55	24.2% 1.68	31.8% 2.90	24.2% 2.11	19.8% 5.08
Dacapo 30,502	29.4% 1.07	24.3% 1.08	24.9% 1.07	12.2% 1.21	12.7% 1.16	7.0% 1.26
CSMA 47,857	94.0% 1.06	75.9% 2.62	81.2% 1.40	105.9% 7.66	114.9% 2.83	120.3% 6.82
BOCDP 203,557	25.2% 1.00	22.5% 1.01	6.5% 1.08	10.2% 1.02	9.3% 1.01	4.5% 1.09
BOPDP 1,013,072	14.7% 2.40	13.2% 1.33	42.1% 1.02	15.2% 1.52	11% 1.14	4.3% 1.74
Buscoupler 3,595,108	53.2% 1.29	13.6% 2.48	40.5% 1.18	31.7% 3.17	24.6% 2.13	14.3% 8.73

Over-approximation

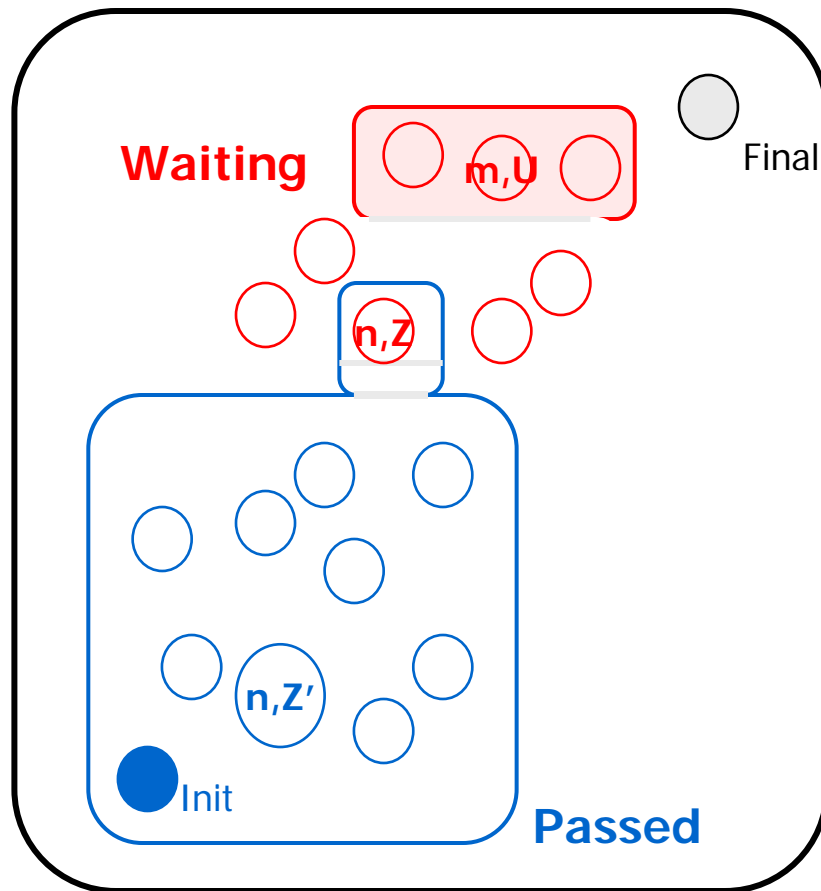
Convex Hull



TACAS04: An **EXACT** method performing as well as Convex Hull has been developed based on abstractions taking max constants into account distinguishing between clocks, locations and \leq & \geq

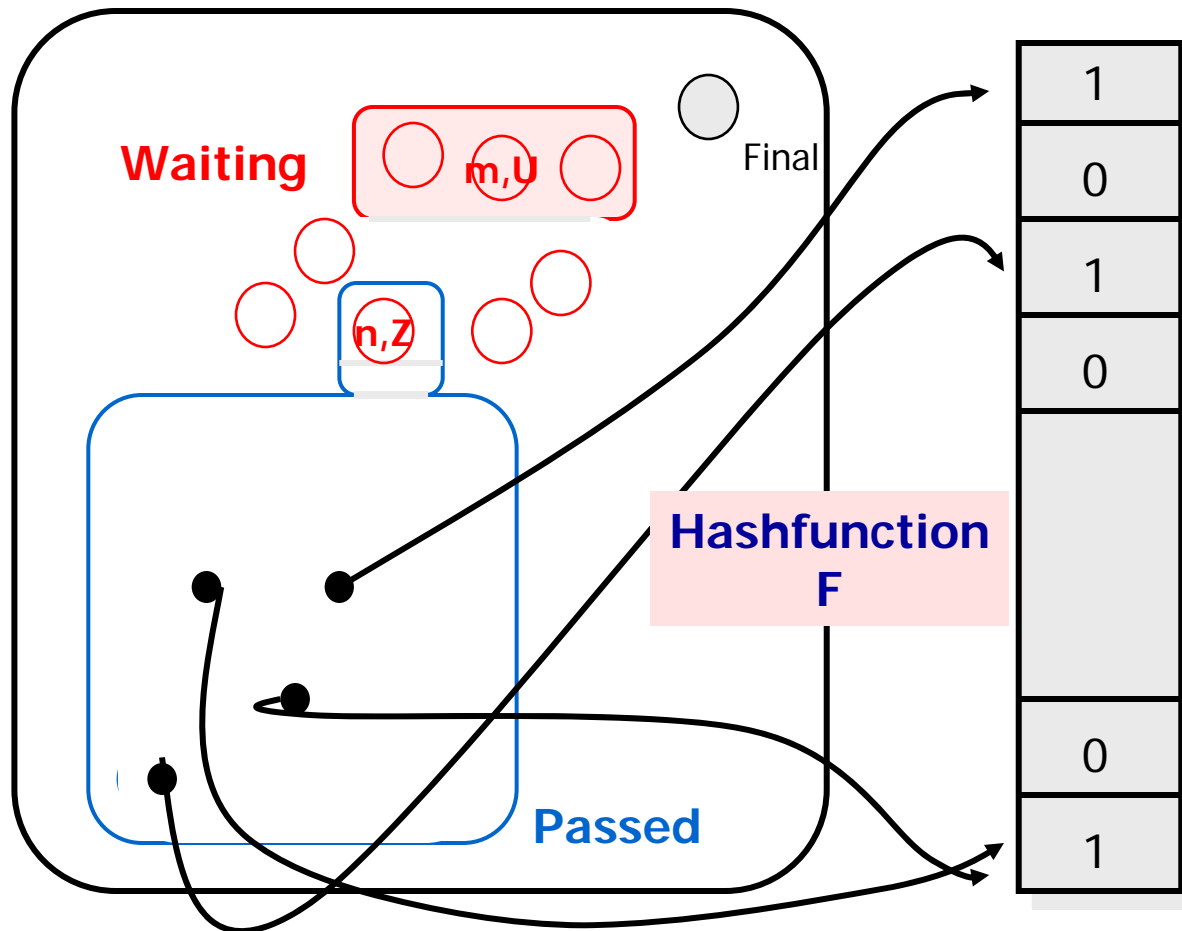
Under-approximation

Bitstate Hashing



Under-approximation

Bitstate Hashing



Passed=
Bitarray

UPPAAL
8 Mbits

Modelling Patterns



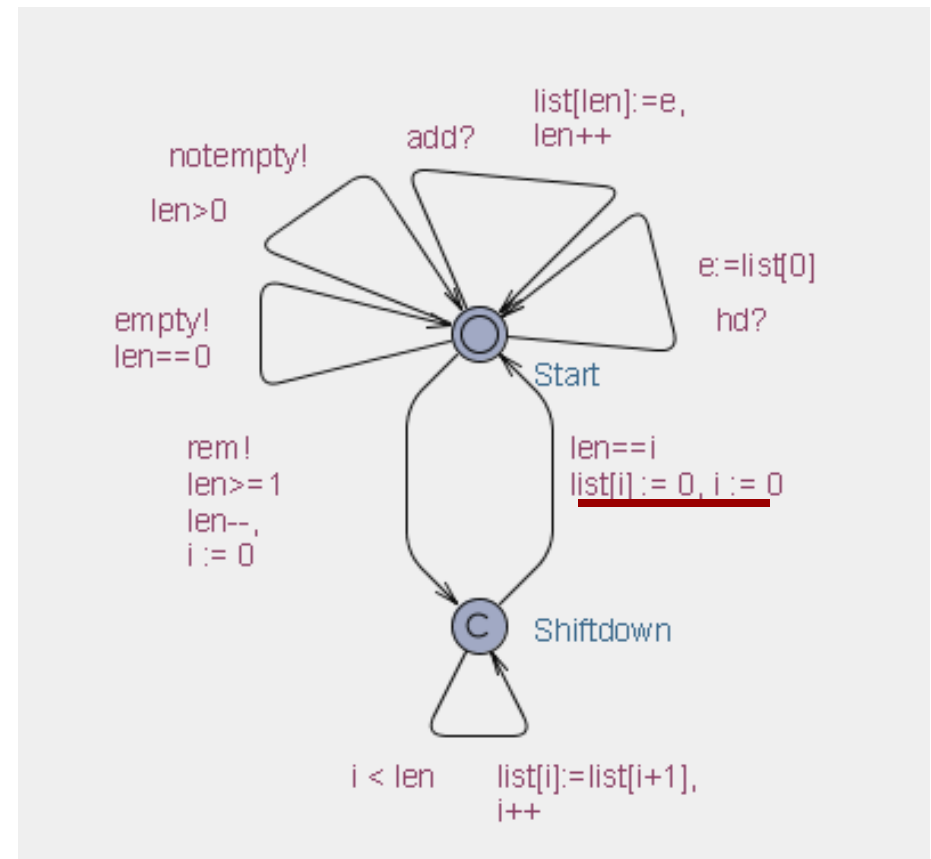
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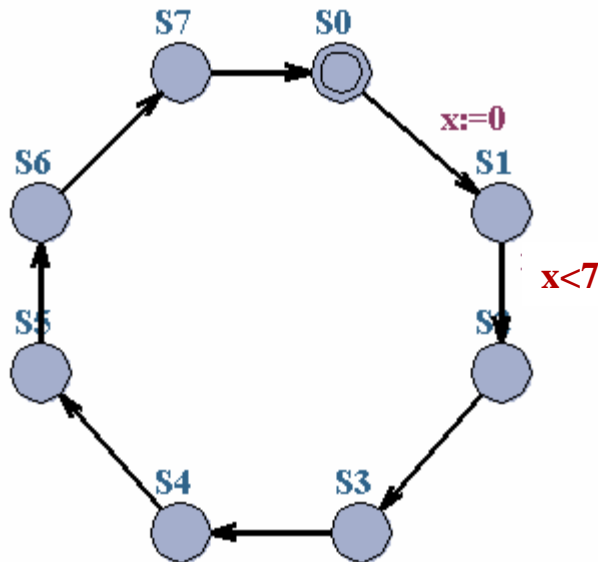
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Variable Reduction

- Reduce size of state space by explicitly resetting variables when they are not used!
- Automatically performed for clock variables (active clock reduction)



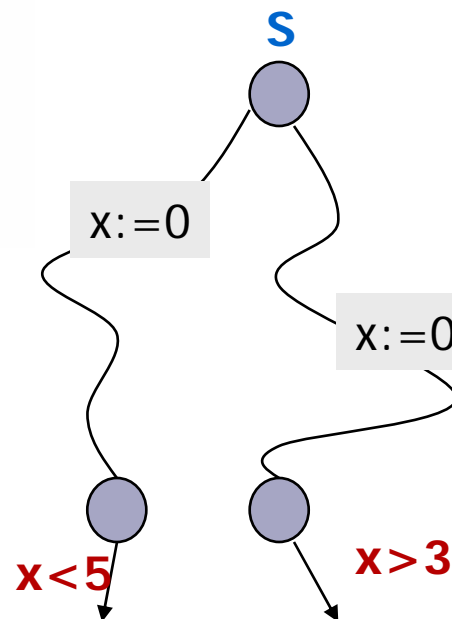
Variable Reduction



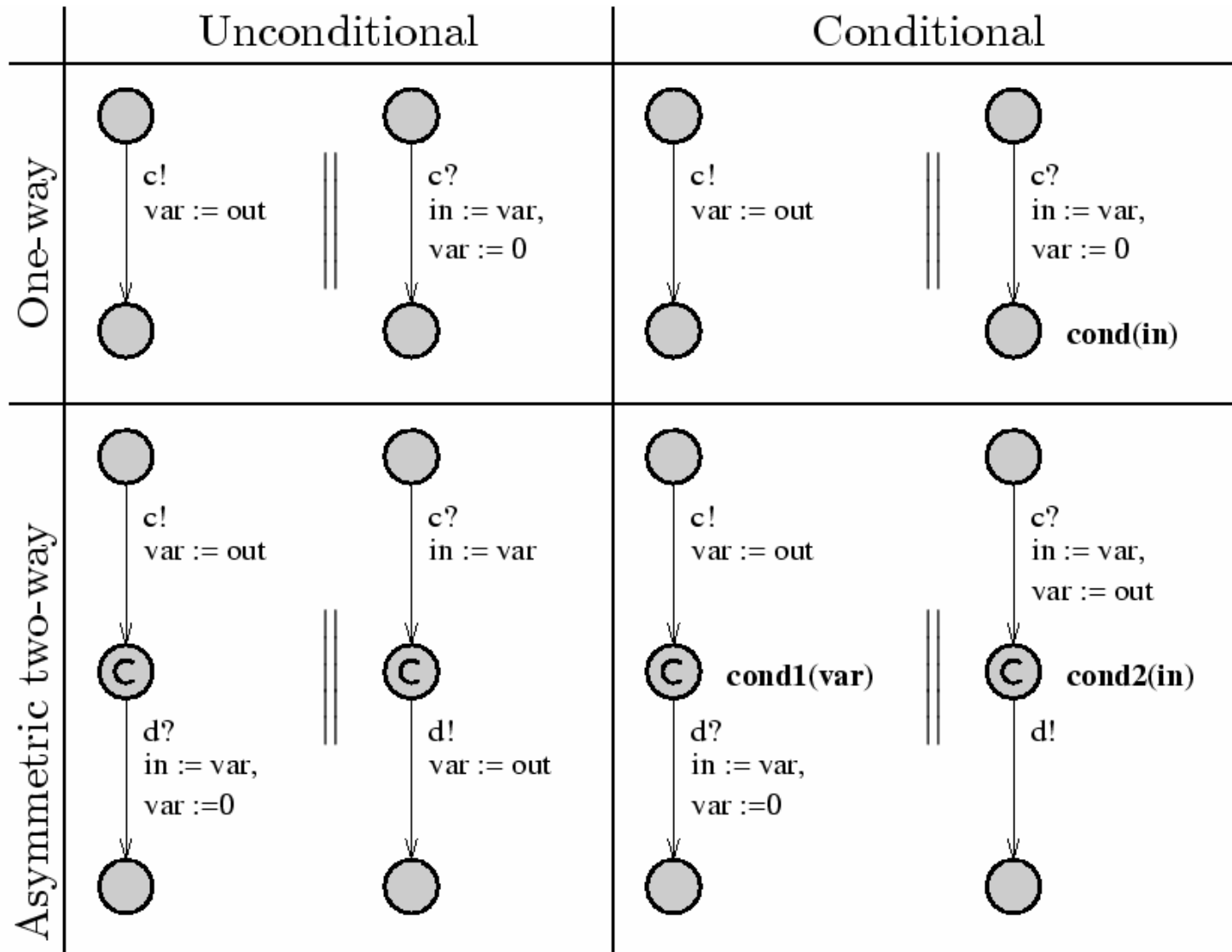
x is only *active* in location **S1**

Definition

x is *inactive* at **S** if on all path from **S**, x is always reset before being tested.

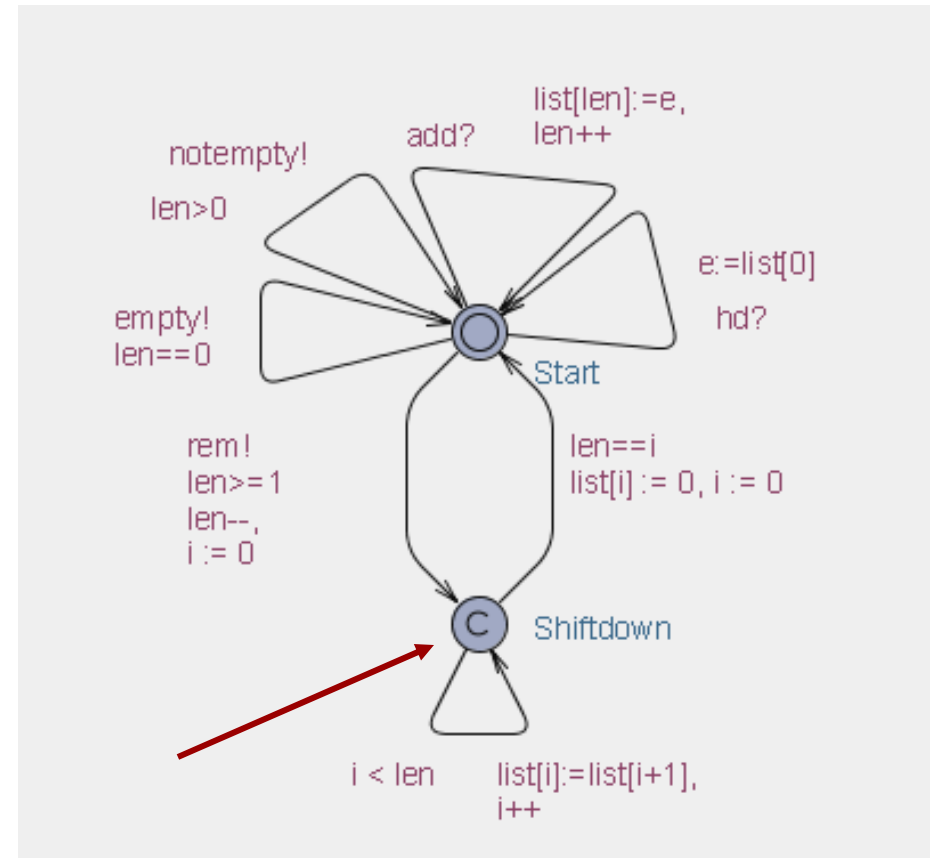


Synchronous Value Passing



Atomicity

- To allow encoding of control structure (for- or while-loops, conditionals, etc.) without erroneous interleaving
- To allow encoding of multicasting.
- Heavy use of committed locations.

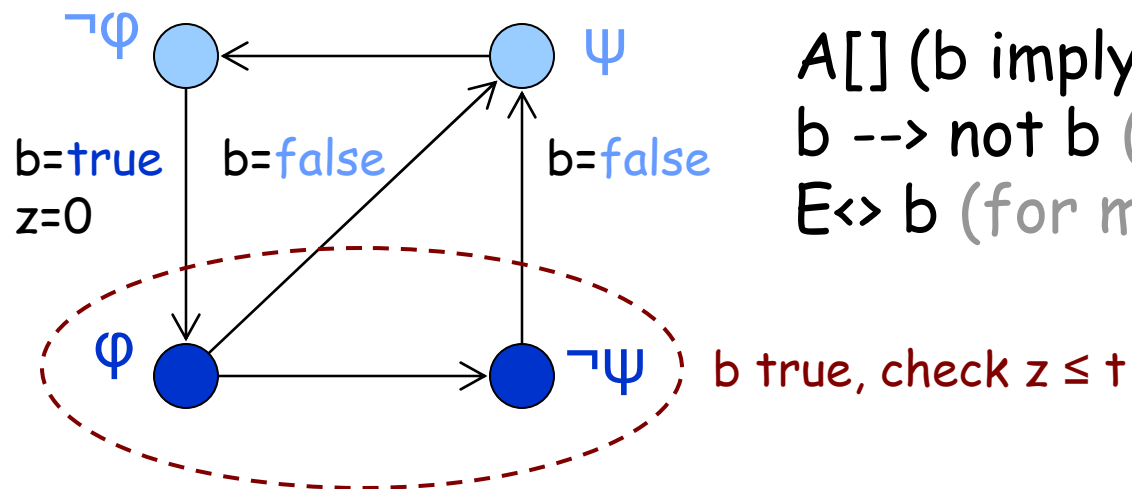


Bounded Liveness

- **Intent:** Check for properties that are guaranteed to hold eventually within some upper (time) bound.
 - Provide additional information (with a valid bound).
 - More efficient verification.
 - $\phi \text{ leadsto}_{\leq t} \psi$ reduced to $A \square (b \Rightarrow z \leq t)$ with bool b set to *true* and clock z reset when ϕ starts to hold. When ψ starts to hold, set b to *false*.

Bounded Liveness

- The truth value of b indicates whether or not ψ should hold in the future.



$A[] (b \text{ imply } z \leq t)$

$b \dashrightarrow \text{not } b$ (for non zenoness)

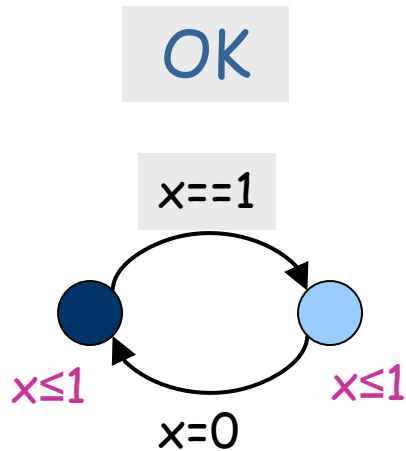
$E \langle \rangle b$ (for meaningful check)

Zenoness

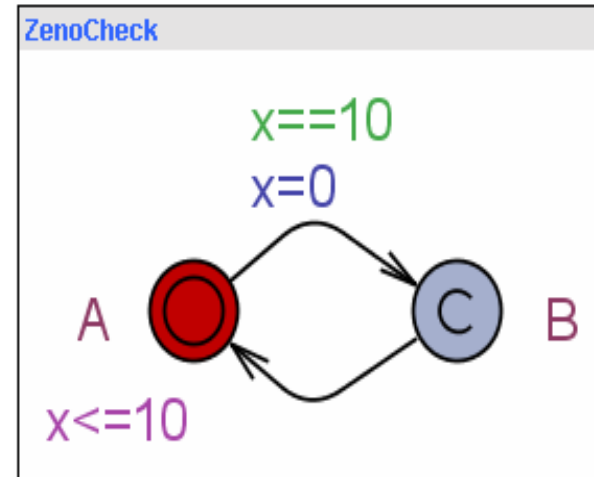
- **Problem:** UPPAAL does not check for zenoness directly.
 - A model has “zeno” behavior if it can take an infinite amount of actions in finite time.
 - That is usually not a desirable behavior in practice.
 - Zeno models may wrongly conclude that some properties hold though they logically should not.
 - Rarely taken into account.

- **Solution:** Add an observer automata and check for non-zenoness, i.e., that time will always pass.

Zenoness



Detect by
•adding the
observer:



Constant (10) can be anything (>0), but choose it well w.r.t. your model for efficiency. Clocks 'x' are local.

•and check the property

ZenoCheck.A \dashrightarrow **ZenoCheck.B**

PRELIMINARY

more to come later
in lectures by
Jacob Illum Rasmussen



Optimal Real Time Planning & Scheduling

with Gerd Behrmann, Ed Brinksma, Ansgar Fehnker,
Thomas Hune, Paul Pettersson, Judi Romijn,
Frits Vaandrager, Patricia Bouyer, Franck Cassez,
Emmanuel Fleury, Arne Skou, Jacob Rasmussen,
K. Subramani



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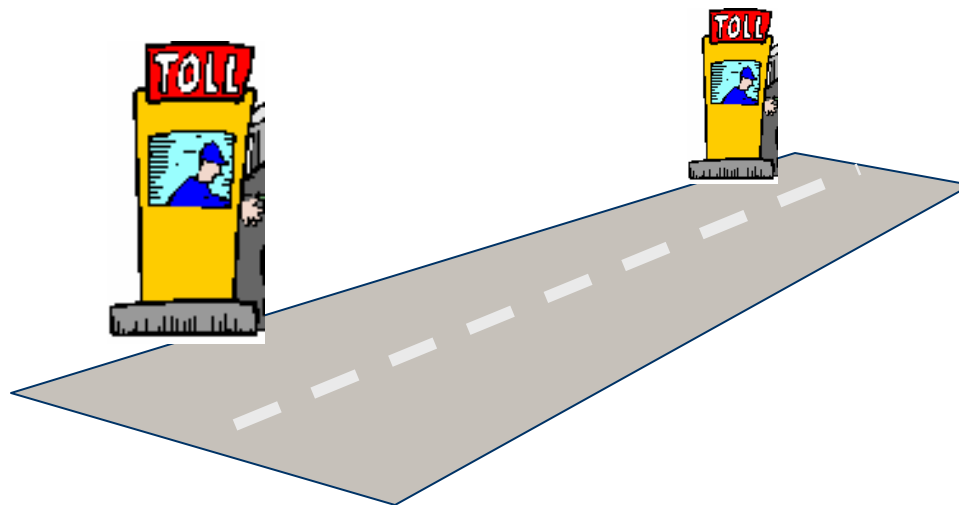
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Real Time Scheduling

- Only 1 "BroBizz"
- Cheat is possible
 (drive close to car with "Bizz")



UNSAFE



5

Crossing Times



10



20



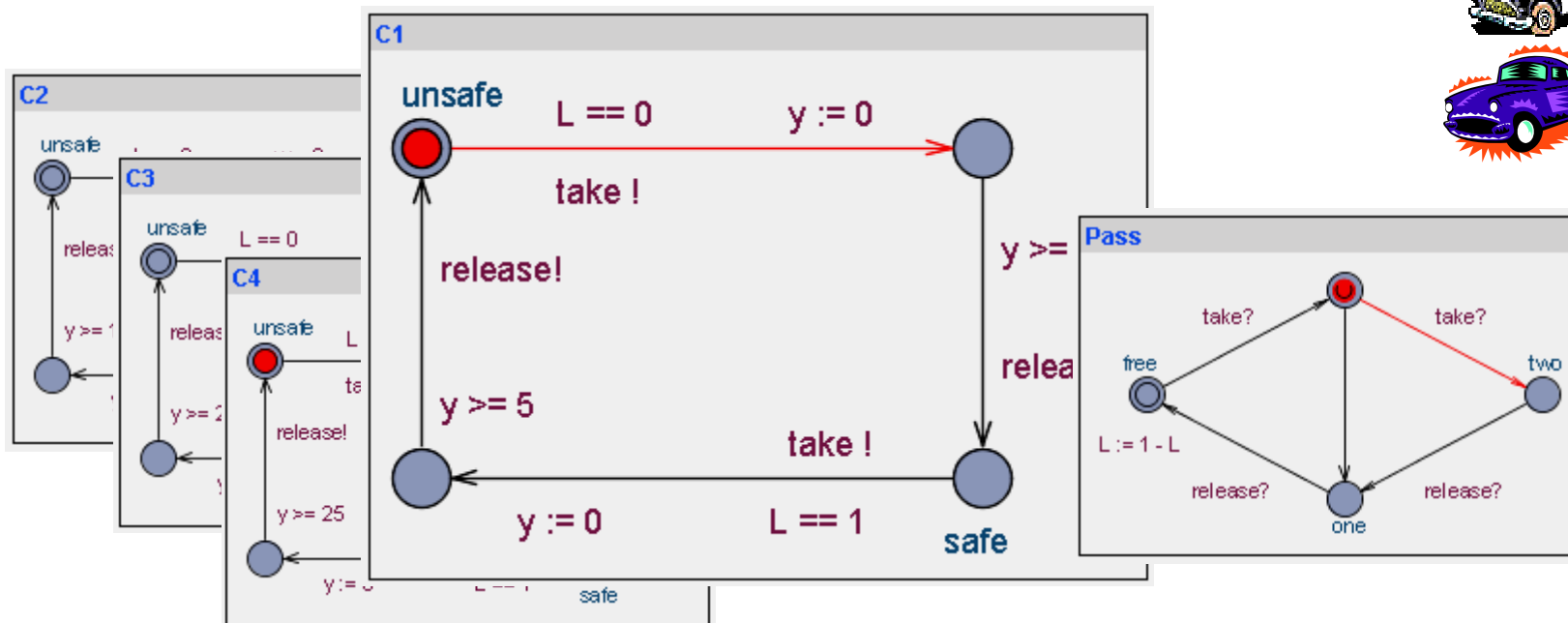
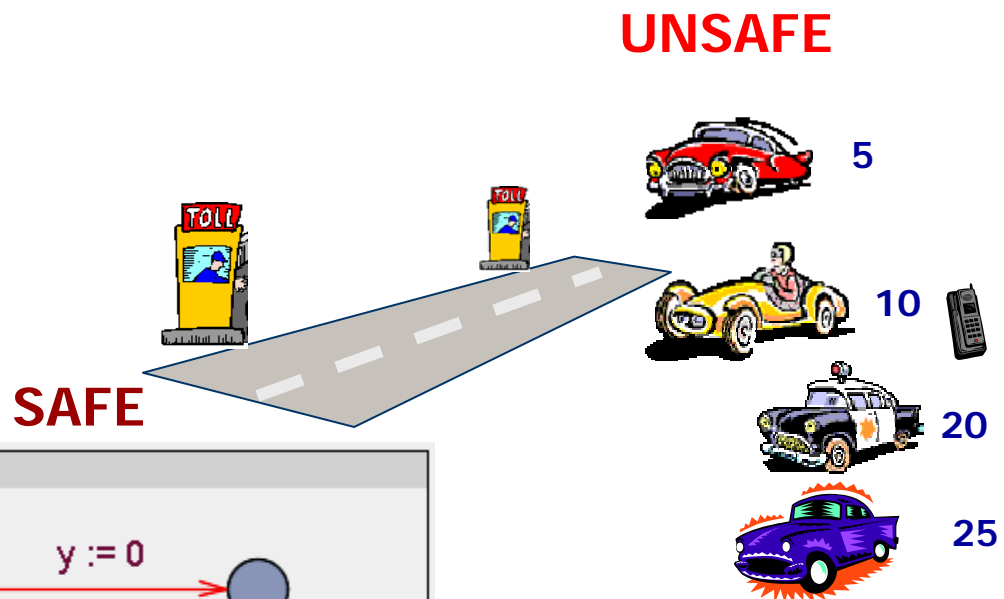
25

SAFE

CAN THEY MAKE IT TO SAFE WITHIN 70 MINUTES ???

Real Time Scheduling

Solve Scheduling Problem using **UPPAAL**



Rush Hour



OBJECTIVE:
Get your
CAR out

EEF Summerschool on
Concurrency,
Kapellerput

END

