


Parallel Computing
In A Nutshell


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B2-206



Overview

<p>Hardware</p> <p>Models</p> <ul style="list-style-type: none"> • PRAM • hyperthreading • cache coherence • interconnection networks • bandwidth/latency • superscalar processors • pipelines <p>Practice</p> <ul style="list-style-type: none"> • Caches. 	<p>Software/Algorithms</p> <p>Models</p> <ul style="list-style-type: none"> • communication costs • mapping techniques • task dependency • load balancing • communication algorithms • optimality, scalability <p>Practice</p> <ul style="list-style-type: none"> • message passing (MPI) • pthreads • applications (matrix, graphs, sorting, search).
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Cache

Keywords: Cache, cache coherence, false sharing.

- What it is.
- Cache coherence protocol.
 - Why we need one – to keep multiple copies of the same data consistent.
- False sharing – performance issue.

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Examples

- Matrix multiplication (matrix*matrix, matrix*vector).
- Prefix computation.
- LU decomposition.
- Gaussian elimination.

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Keyword: PRAM

PRAM Model

- Global, synchronous, parallel RAM.
- Classes: EREW, CREW, ERCW, CRCW.
 - Most powerful: CRCW.
 - Least powerful: EREW.
 - Simulation of CRCW on EREW & keep optimality.
- Issue: How to resolve contention?

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Design Principle

Construct optimal algorithms to run as **fast as possible**.

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Construct optimal algorithms using as **many processors as possible**!

Keywords: Brent scheduling principle, degree of concurrency, modeling.

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Brent's Scheduling Principle

Theorem

If a parallel computation consists of **k phases** taking time t_1, t_2, \dots, t_k using a_1, a_2, \dots, a_k processors in phases $1, 2, \dots, k$ then the computation can be done in time $O(a/p + t)$ using p processors where $t = \sum(t_i)$, $a = \sum(a_i)$.

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Optimality

Keywords: Optimality, sequential/parallel execution time, complexity.

- Complexity: What to evaluate?
- Compare to optimal sequential algorithm (running time W).
- Parallel execution time (T_p) vs. total work ($p * T_p$).
- Optimal iff **$p * T_p = \Theta(W)$** .

Fundamental concept.

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Interconnection Networks

- Bus, cross-bar, multistage (omega networks).
- Issues: Performance vs. cost.

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Keywords: Network topologies, meshes, hypercubes.

Topologies

- Linear array, rings.
- 2-D, 3-D... meshes.
- Hypercubes.
- Tree based.
- Issue: How to evaluate them?
 - Cost vs. performance.
 - Define metrics.

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Linear Arrays and Meshes

(a) (b)

Figure 2.15 Linear arrays: (a) with no wraparound links; (b) with wraparound link.

(a) (b) (c)

Figure 2.16 Two and three dimensional meshes: (a) 2-D mesh with no wraparound; (b) 2-D mesh with wraparound link (2-D torus); and (c) a 3-D mesh with no wraparound.


Hypercubes

0-D hypercube 1-D hypercube 2-D hypercube 3-D hypercube

4-D hypercube

Figure 2.17 Construction of hypercubes from hypercubes of lower dimension.


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Hypercubes

- Very good but very expensive.
- Naming scheme property – neighbors IDs differ by only one bit.
- E-cube routing protocol.

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Metrics

- **Diameter**: Maximum distance $p_a \leftrightarrow p_b$.
- **Connectivity**: Measure of multiplicity of paths.
- **Bisection width**: Minimum number of links to cut in order to partition the network in 2 equal halves.
- **Bisection bandwidth**: Minimum volume of communication allowed between 2 halves.
- **Cost**: Number of communication links, i.e., wires.

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Keywords: Latency, bandwidth.



Communication Costs

- Model **latency & bandwidth**.
- Total time for communication =
 - Startup time (t_s), *once per message*
 - + Per-hop time (t_h), *between directly connected nodes*
 - + Per-word transfer time (t_w), *1/bandwidth*.
 - Generic: $T = t_s + mt_w$

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Keyword: congestion.



Topology Embedding

- Embed one topology in another (hypercube on mesh...).
- Embedding metrics: Dilation, Expansion, **Congestion**.
- Possible to map denser into sparser:
 - Map (expensive) logical topology into (cheaper) physical hardware!
 - Mesh with links faster by $\sqrt{p}/2$ than hypercube links has same performance!

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Keywords: Concurrency, task dependency graph, degree of concurrency.



Decomposition

- Decomposition into concurrent tasks.
- Task dependency graph.
- Granularity: number & size of tasks.
- Maximal degree of concurrency.
- Critical path.
- Task interaction graph (dependency = sub-graph).

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Keyword: Load balancing.



Process Mapping

- Mapping = Assignment of task to processes.
- Decomposition techniques:
 - Recursive (divide & conquer).
 - Data decomposition (1-D, 2-D block data partitioning).
 - Exploratory decomposition (search).
 - Block-cyclic decompositions.
 - Graph partitioning.

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Dynamic Mapping

- Centralized scheme (Master-slave).
- Distributed scheme (exchange).
- Goals: Minimize contention & hot spots, maximize data locality.

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Communication Primitives

- Algorithms & cost analysis for:
 - Broadcast one-to-all, all-to-all.
 - Reduce all-to-one, all-to-all.
 - Scatter & gather.
 - Circular shifts.
- For different topologies (arrays, meshes, hypercubes).

Keywords: Broadcast communication primitives.

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Analytical Modeling – Performance Metrics

- What is scalability? How to measure it?
- Measure overhead: $T_O = pT_P - T_S$
- Speedup: $S = T_S / T_P$ – recall: w.r.t. T_S **best** sequential algorithm running time.
- Efficiency (normalized speedup) $E = S/p$.
- Cost optimal if E is a constant.

Keywords: Speedup, efficiency.

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Cost Optimality

- 2 techniques to be cost optimal (not always work, cheating in a sense):
 - Re-schedule the tasks on fewer processes (Brent's scheduling principle).
 - Decrease granularity: Assign n/p elements per process.
- 2 techniques, one effect: Decrease p , increase work/process.

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Keyword: Scalability.

Scalability

- Fix problem size, increase processors: Decrease efficiency.
- Fix processors, increase problem size: Increase efficiency.
- Related to communication overhead.
- Scalability measures the ability to increase speedup in function of p .
 - Fix E : How to increase W in function of p ?
 $W = KT_o(W, p)$. Isoefficiency function.

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MPI

- Point to point communication.
- Group communication – communicator.
- Topology support.

6 Golden Functions

<code>MPI_Init</code>	Initializes MPI.
<code>MPI_Finalize</code>	Terminates MPI.
<code>MPI_Comm_size</code>	Determines the number of processes.
<code>MPI_Comm_rank</code>	Determines the label of the calling process.
<code>MPI_Send</code>	Sends a message.
<code>MPI_Recv</code>	Receives a message.

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Focus: Communication.

MPI

- Key principles:
 - partitioned address space,
 - explicit parallelization.
- Blocking vs. non blocking communication.
- Deadlocks (circular send).
- Collective communication primitives.
- Barriers.

+ More scalable, costs explicit.
- Load balancing, mapping.

Keyword: **Deadlock.**

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Focus: Synchronization.

Multithreading – Pthreads

- Latency hiding but need more bandwidth.
- Implicit scheduling & load balancing.
- Thread creation, termination, cancellation, mutex-locks, condition variables.
- Critical sections, race conditions.
- How to implement monitors.

+ Implicit, easier.
- Less scalable.

Keywords: **Race, critical sections.**

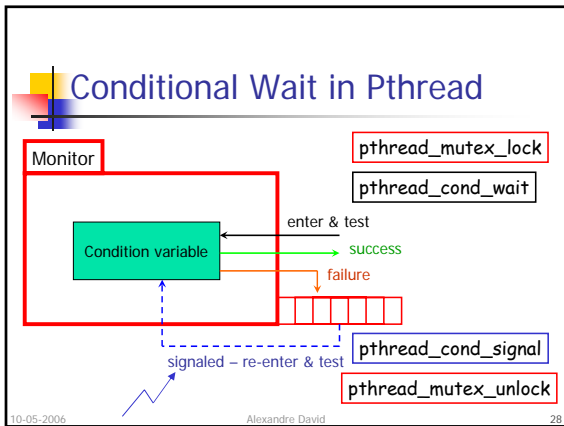
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Keyword: Monitors.

Pthread Constructs

- Monitors with condition variables.
- Read-write locks.
- Barriers.

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- ## Applications
- Dense matrix algorithms.
 - 1-D, 2-D, 3-D block mapping for matrix multiplication.
 - Gaussian elimination.
 - Sorting.
 - Bitonic sort, odd-even sort, quicksort.
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- ## Applications
- Keywords: Acceleration & deceleration anomalies.
- Graphs.
 - Adjacency matrix vs. adjacency lists.
 - Minimum spanning tree.
 - Single-source & all-pairs shortest path.
 - Discrete optimization problems formulated as search.
 - Super-linear anomalies.
 - Work splitting, load balancing (Round-robin, random polling).
 - Termination detection.
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